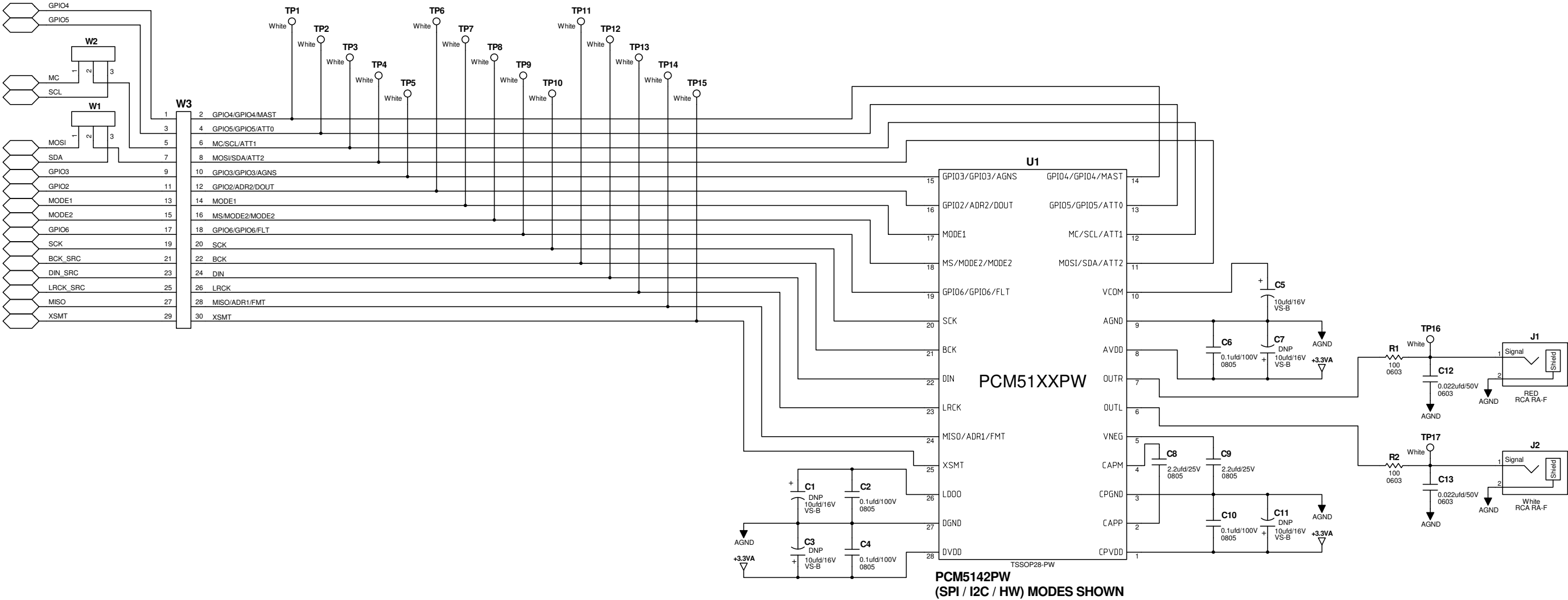


PCM51XXEVM-U



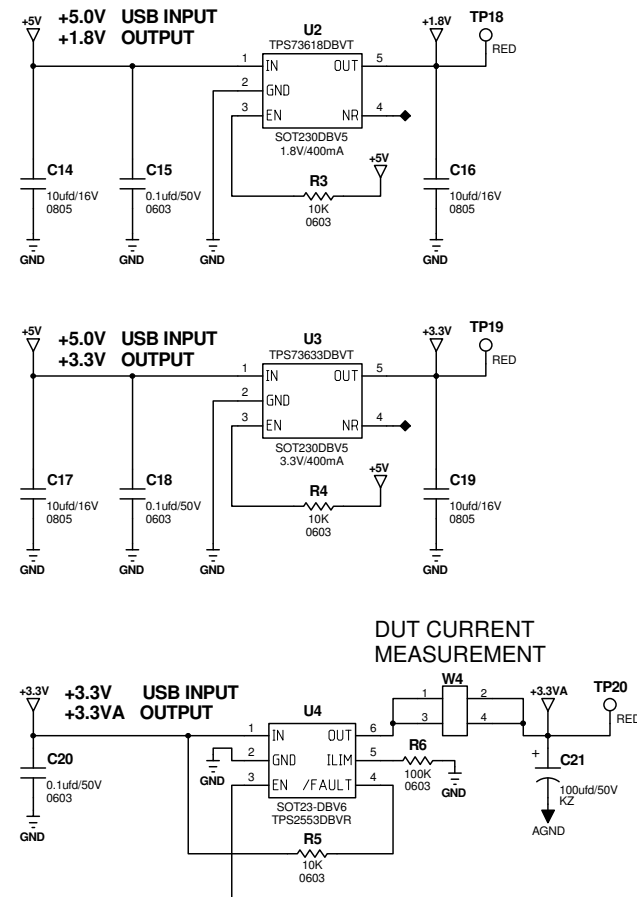
PCM51XX (DUT OPTIONS)

NOTE:
THIS BOARD SUPPORTS MULTIPLE DEVICES UNDER TEST (DUT).
WHEN USED WITH DEVICES WITH FEWER PINS, SOME PIN PADS WILL BE UNUSED.
ALIGN ALL DEVICES WITH PIN #1 IN PROPER LOCATION.

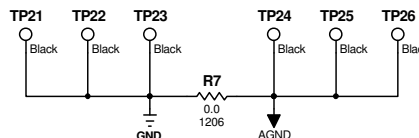
				SPI MODE		I2C MODE		HW MODE	
NO PIN		NO PIN		15	AGNS	15	GPIO3	15	AGNS
NO PIN		NO PIN		16	TEST	16	GPIO2	16	DOUT
NO PIN		NO PIN		17	NC	17	MODE1	17	MODE1
NO PIN		NO PIN		18	NC	18	MS	18	MODE2
11	FLT	10	VCOM	19	FLT	19	GPIO6	19	FLT
12	SCK	9	AGND	20	SCK	20	SCK	20	SCK
13	BCK	8	AVDD	21	BCK	21	BCK	21	BCK
14	DIN	7	OUTR	22	DIN	22	DIN	22	DIN
15	LRCK	6	OUTL	23	LRCK	23	LRCK	23	LRCK
16	FMT	5	VNEG	24	FMT	24	ADR1	24	FMT
17	XSMT	4	CAPM	25	XSMT	25	XSMT	25	XSMT
18	LDOO	3	CPGND	26	LDOO	26	LDOO	26	LDOO
19	DGND	2	CAPP	27	DGND	27	DGND	27	DGND
20	DVDD	1	CPVDD	28	DVDD	28	DVDD	28	DVDD

PCM51XXEVM-U

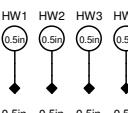
POWER



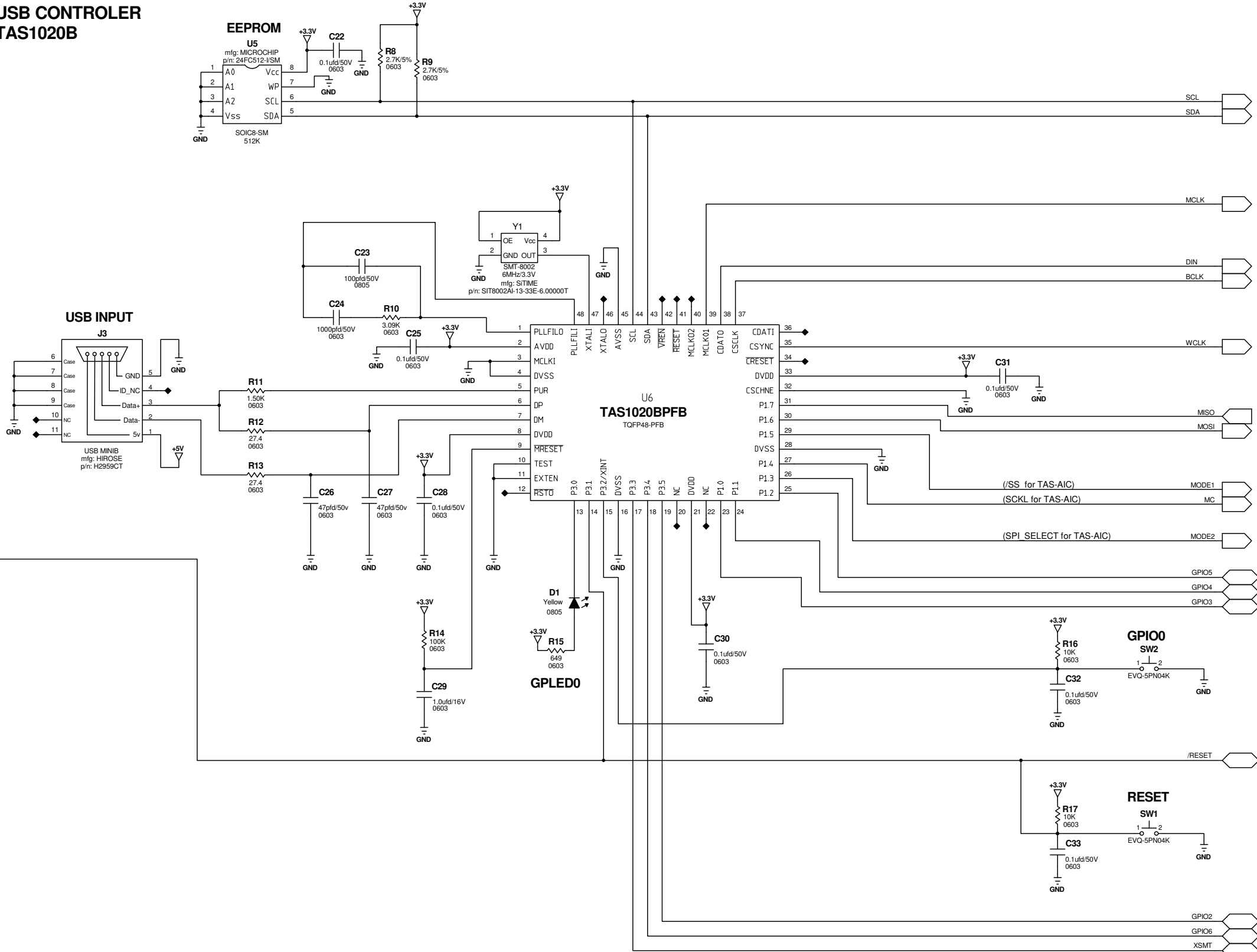
GROUND



STANDOFFS

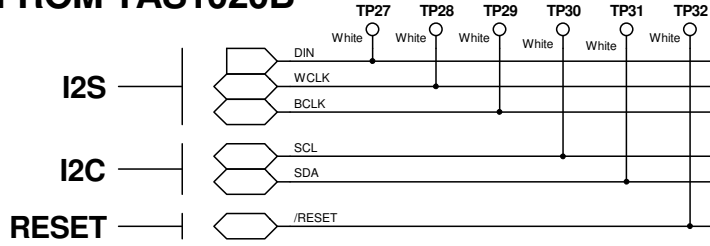


USB CONTROLEF TAS1020B

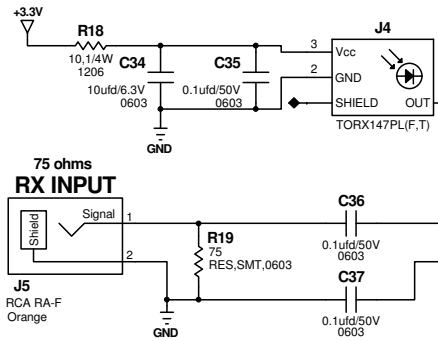


PCM51XXEVM-U

I2S FROM TAS1020B



SPDIFF INPUTS

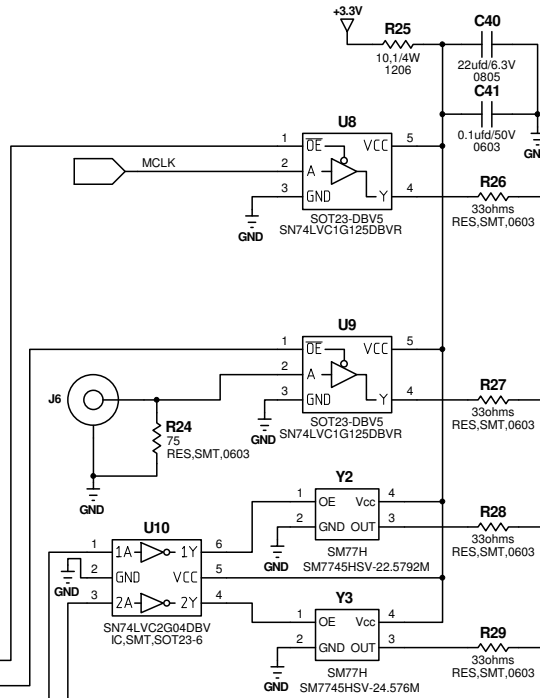
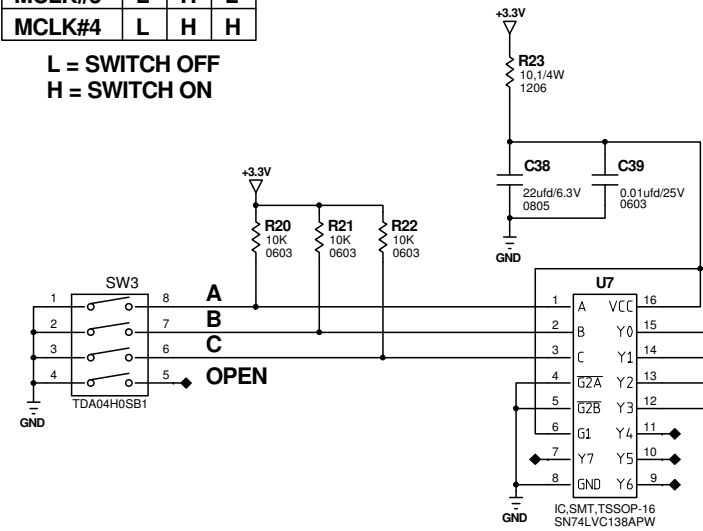


ALTERNATE MCLK SELECTION

FOR MCLK SOURCES OTHER THAN FROM SRC4392 (U12).

MCLK SELECTION U7 PINS 1,2,&3			
	A	B	C
MCLK#1	L	L	L
MCLK#2	H	L	L
MCLK#3	L	H	L
MCLK#4	L	H	H

L = SWITCH OFF
H = SWITCH ON



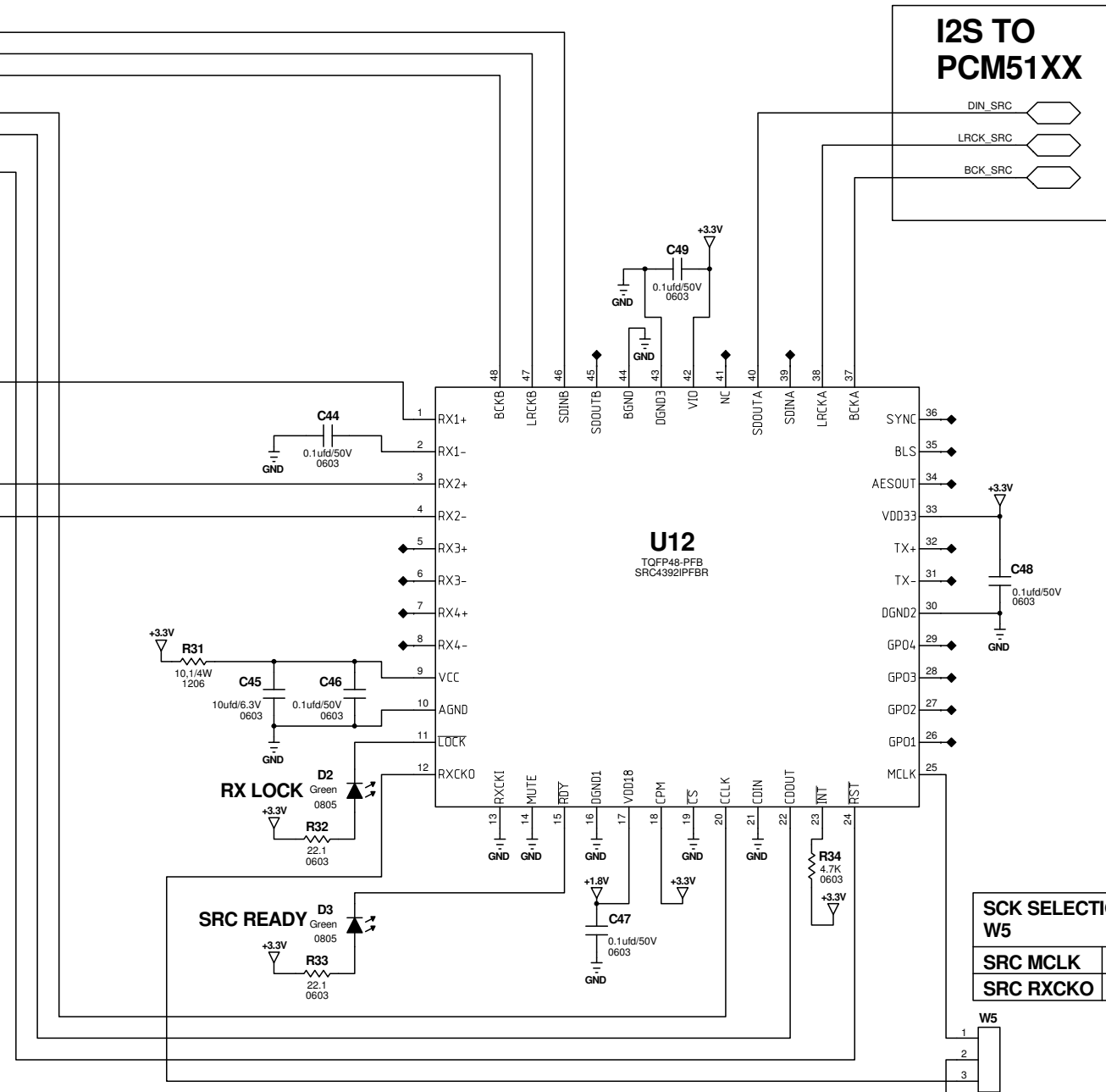
MCLK#1
ONBOARD MCLK
FROM TAS1020B

MCLK#2
EXTERNAL MCLK
+3.3VD BNC INPUT

MCLK#3
22.5792MHz MCLK
FROM OSC Y2

MCLK#4
24.576MHz MCLK
FROM OSC Y2

SAMPLE RATE CONVERTER



I2S TO
PCM51XX



SCK SELECTION W5	
SRC MCLK	1-2
SRC RXCKO	2-3

SCK
TO
PCM51XX

PCM51XXEVM-U

REVISION HISTORY			
REVISION	DESCRIPTION	DATE	APPROVAL
A	RELEASE	OCT 02, 2010	SL

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