

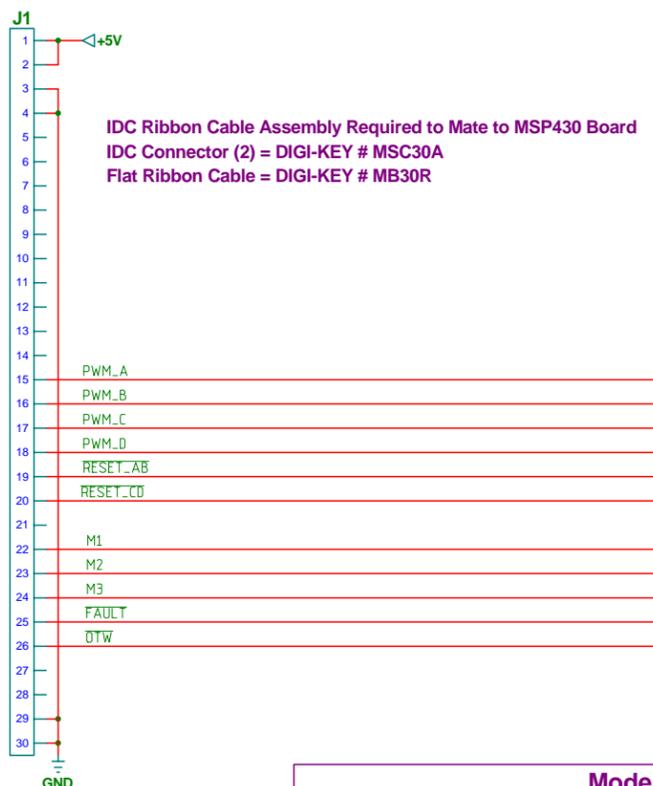
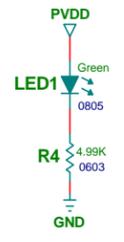
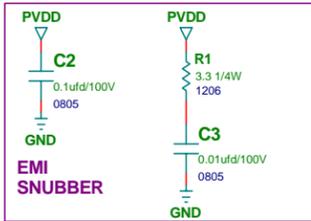
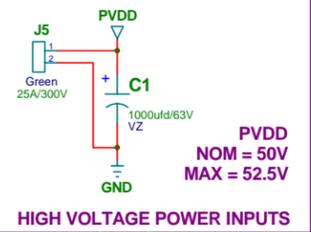
DRV8432DKD EVALUATION BOARD REV A



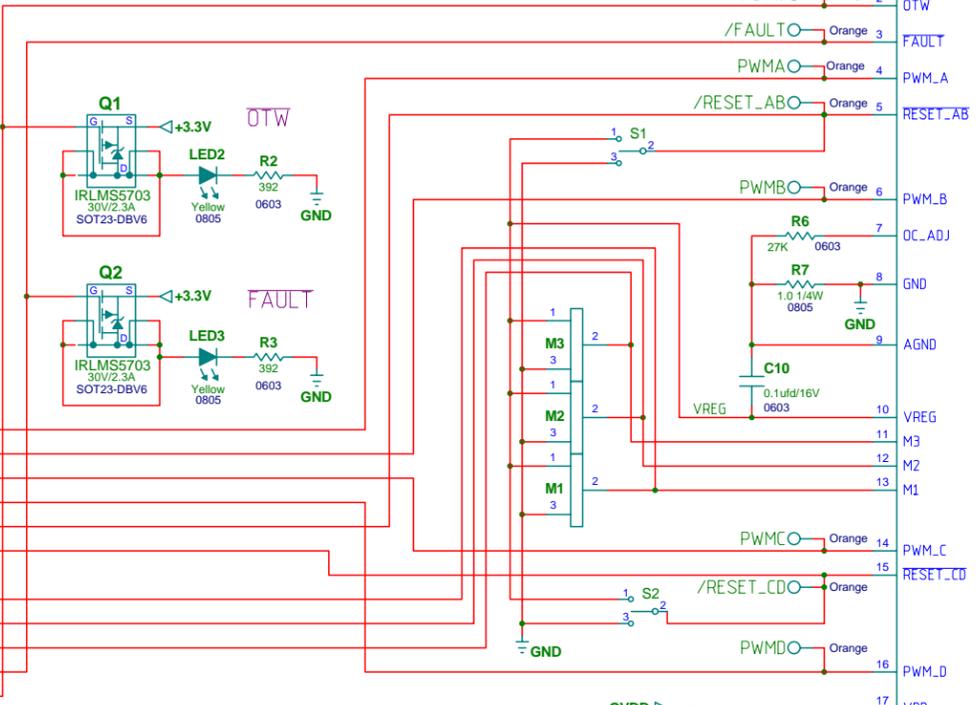
HIGH VOLTAGE WARNING
FOR VOLTAGE POTENTIALS OF 50V OR GREATER



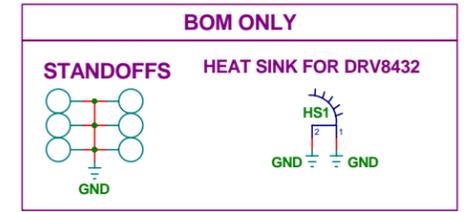
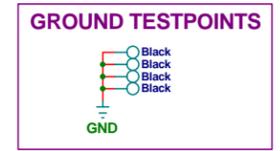
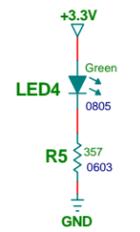
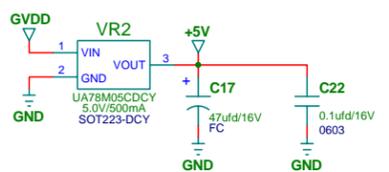
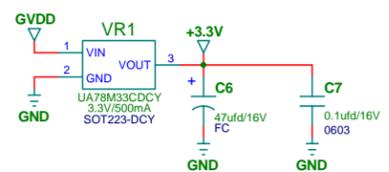
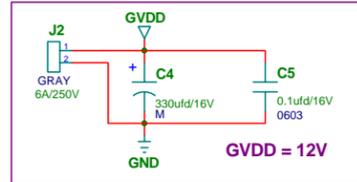
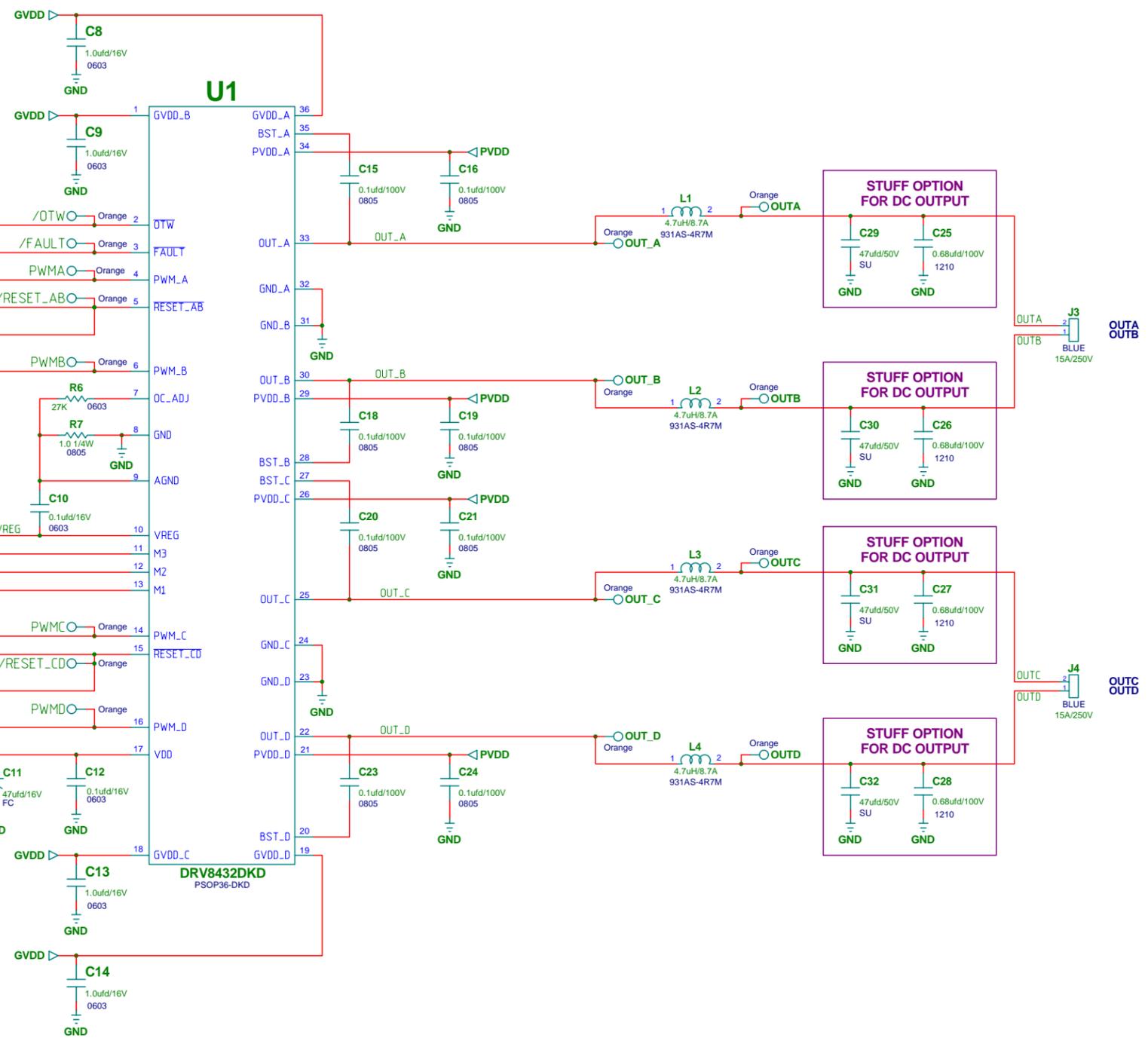
WARNING
HIGH VOLTAGE



ICD Ribbon Cable Assembly Required to Mate to MSP430 Board
ICD Connector (2) = DIGI-KEY # MSC30A
Flat Ribbon Cable = DIGI-KEY # MB30R



Mode Selection Matrix				
M3	M2	M1	OUTPUT	DESCRIPTION
0	0	0	4HB/2FB	HALF/FULL BRIDGE, CYCLE BY CYCLE CURRENT LIMIT (DEFAULT)
0	0	1	4HB/2FB	HALF/FULL BRIDGE, OC LATCHING SHUTDOWN
0	1	0	1PFB	PARALLEL FULL BRIDGE (A+B AND C+D) WITH CYCLE BY CYCLE CURRENT LIMIT
0	1	1	2FB	FULL BRIDGE WITH ONE PWM INPUT AND CYCLE BY CYCLE CURRENT LIMIT
1	X	X	NA	RESERVED FOR TEST MODE



DRV8432DKD EVALUATION BOARD

REVISION HISTORY

REVISION	DESCRIPTION	DATE	APPROVAL
A	RELEASE	OCTOBER 30, 2009	ML

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