


1	2	3	4	5	6
A					
B					
C					
D					
1	2	3	4	5	6

PCB Number: DRV8850EVM
PCB Rev: A

PCB
LOGO
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Designed for: <u>Public Release</u>	Mod. Date: <u>10/10/2013</u>	 TEXAS INSTRUMENTS http://www.ti.com <small>© Texas Instruments 2013</small>
Project Title: <u>DRV8850EVM</u>		
Number: <u>DRV8850EVM</u> Rev: <u>A</u>	Sheet Title:	
SVN Rev: <u>Not in version control</u>	Assembly Variant: <u>[No Variations]</u>	
Drawn By:	File: <u>DRV8850_Customer_EVM_Hardware_ANSI-B 15000-5</u>	Sheet 1 of 1
Engineer: <u>Nick Oborny</u>	Contact: <u>http://www.ti.com/support</u>	

1	2	3	4	5	6
A					
B					
C					
D					

Revision History	
Revision	Notes
E0	Engineering Draft Revision
E1	Toggle ON/OFF Debug
A	Release Revision

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Project Title: DRV8850EVM

Number: **DRV8850EVM** Rev: **A**

SVN Rev: **Not in version control**

Assembly Variant: **[No Variations]**

Drawn By:

File: **DRV8850_Customer_EVM_Cover_Sheet_ANSI_S2D.doc**

Engineer: **Nick Oborny**


Contact: **http://www.ti.com/support**

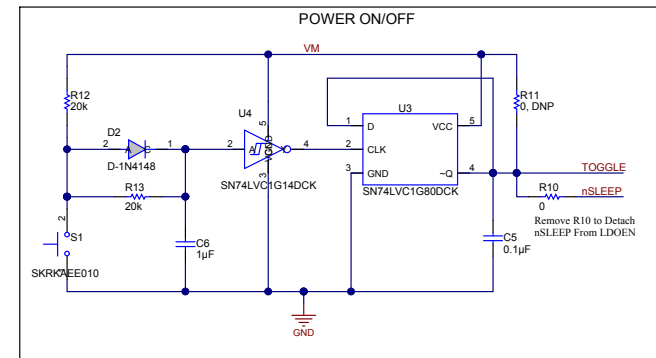
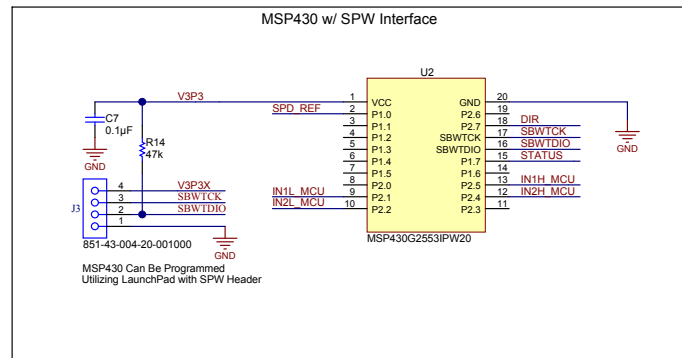
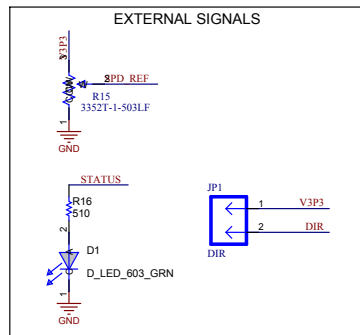
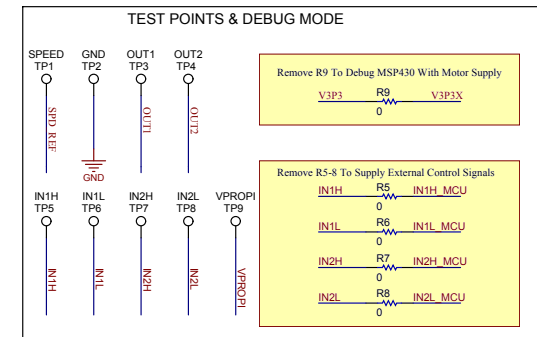
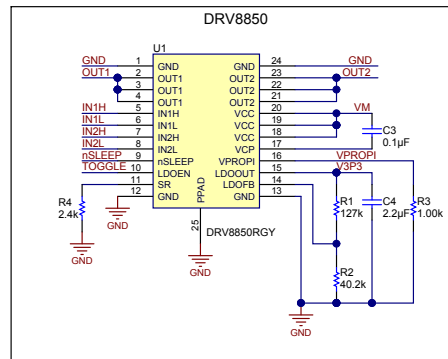
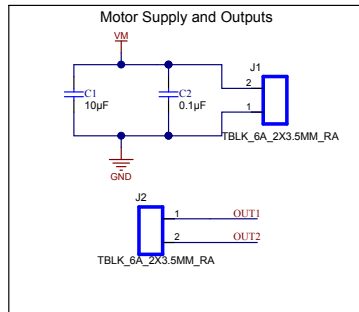
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Project Title: DRV8850EVM	Sheet Title:
Number: DRV8850EVM Rev: A	Assembly Variant: [No Variations]
SVN Rev: Not in version control	File: DRV8850_Customer_EVM_SchDoc
Drawn By: Engineer: Nick Oborny	Contact: http://www.ti.com/support
Sheet 1 of 1	Size: B
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