

Register 2—Reserved (address = 0x02) (Read-Only)

| BIT # | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
|----------|----|----|----|----|----|----|----|----|
| BIT Name | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

Bit Descriptions

Reserved (Bits 7-0)

These bits are reserved and must be set to '0'.