Register 6—PGA Gain (address = 0x06) (Read/Write)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	0	0	0	0	0	G2	G1	G0

This register sets the gain of the programmable-gain amplifier. The unused bits must always be set to '0'. The gain step to register content is given in Table 1.

Bit Descriptions

Reserved (Bits 7-3)
These bits are reserved and must be set to '0'.

G: PGA Gain (Bits 2-0)
These bits set the gain of the programmable-gain amplifier.

G2	G1	G0	PGA VOLTAGE GAIN	SIGNAL PATH TRANSCONDUCTANCE $R_{VI} = 6.34k\Omega$
0	0	0	6.25 V/V	49 mA/V
0	0	1	12.5 V/V	99 mA/V
0	1	0	25 V/V	197 mA/V
0	1	1	50 V/V	394 mA/V
1	0	0	100 V/V	789 mA/V
1	0	1	200 V/V	1577mA/V
1	1	0	400 V/V	3155 mA/V
1	1	1	Reserved	Reserved

Table 1. PGA Gains