Register 9—I$_{REF}$ Output Configuration (address = 0x09) (Read/Write)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	0	IB2	IB1	IB0	0	IA2	IA1	IA0

This register connects the reference currents to the various multiplexed input pins. I$_{REF}$ connection codes are given in Table 1 and Table 2.

Bit Descriptions

Reserved (Bit 7, Bit 3)
These bits are reserved and must be set to '0'.

IB: I$_{REF}$ 2 Input (Bits 6-4)
These bits specify which input channel to connect to the output of current source I$_{REF}$ 2.

IA: I$_{REF}$ 1 Input (Bits 2-0)
These bits specify which input channel to connect to the output of current source I$_{REF}$ 1.

IA2	IA1	IA0	I$_{REF}$ 1 CONNECTION
0	0	0	I$_{REF}$ 1 connected to V/I-0
0	0	1	I$_{REF}$ 1 connected to V/I-1
0	1	0	I$_{REF}$ 1 connected to V/I-2
0	1	1	I$_{REF}$ 1 connected to V/I-3
1	0	0	I$_{REF}$ 1 connected to V/I-4
1	0	1	I$_{REF}$ 1 connected to V/I-5
1	1	0	Reserved
1	1	1	Reserved

Table 1. I$_{REF}$ 1 Output Configuration

IB2	IB1	IB0	I$_{REF}$ 2 CONNECTION
0	0	0	I$_{REF}$ 2 connected to V/I-0
0	0	1	I$_{REF}$ 2 connected to V/I-1
0	1	0	I$_{REF}$ 2 connected to V/I-2
0	1	1	I$_{REF}$ 2 connected to V/I-3
1	0	0	I$_{REF}$ 2 connected to V/I-4
1	0	1	I$_{REF}$ 2 connected to V/I-5
1	1	0	Reserved
1	1	1	Reserved

Table 2. I$_{REF}$ 2 Output Configuration