

### Register 15—Checksum (address = 0x0F) (Read/Write)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	S7	S6	S5	S4	S3	S2	S1	S0

This register contains the checksum byte that is used to validate the data read from the EEPROM. If a write occurs to this register, and the checksum is invalid, an error condition will set (CSE = '1'). If the checksum is valid, the error condition will be cleared (CSE = '0').

If a checksum error is detected, the XTR108 will program itself to the lowest under-scale error level.

### Bit Descriptions

#### C: Checksum (Bits 7-0)

These bits contain the checksum byte that is used to validate the data read from the EEPROM.