Register 7—Alarm Configuration (address = 0x08) (Read/Write)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	AC7	AC6	AC5	AC4	AC4	AC3	AC2	AC1

This register configures whether the XTR108 will go over-scale or under-scale for various detected fault conditions at the input of the PGA. Table 1 defines each of the bits.

If a bit corresponding to the particular error is set to '1', the output will go over-scale when it occurs and if a bit corresponding to the particular error is set to '0', the output will go under-scale.

Bit Descriptions

AC: Alarm Configuration (Bits 7-0)
These bits configure whether the XTR108 will go over-scale or under-scale for various detected fault conditions at the input of the PGA.

	AC7	AC6	AC5	AC4	AC3	AC2	AC1	AC0
V_{INN}	H	L	L	H	N	N	L	H
V_{INP}	L	H	L	H	L	H	N	N

NOTES: 'H' = input exceeds positive common-mode range, 'L' = input exceeds negative common-mode range, and 'N' = input pin is within the common-mode range.

Table 1. Alarm Configuration Register