

#### Register 4—Control Register 2 (address = 0x04) (Read/Write)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	0	0	0	0	0	0	0	RBD

If the RBD bit is set to '1', the automatic read-back from the EEPROM will be disabled after a valid checksum byte is received in Register 15. This bit is read from the EEPROM during a read-back by the XTR108 and allows the user to program the XTR108 to read the EEPROM data once (instead of continuously), and then disables the automatic read-back function. The XTR108 will continuously read the EEPROM if RBD is set to '0'. The remaining bits in this register must be set to '0'.

#### Bit Descriptions

##### Reserved (Bits 7-1)

These bits are reserved and must be set to '0'.

##### RBD: Read-back Disable (Bit 0)

This bit disables automatic read-back from the EEPROM.