

**Register 14—Linearization Adjust (address = 0x0E) (Read/Write)**

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	L7	L6	L5	L4	L3	L2	L1	L0

This register sets the code to the 8-bit DAC that adjust the magnitude of the linearization feedback current. Value is unipolar to 255.

**Bit Descriptions**

**L: Linearization Adjust (Bits 7-0)**

These bits set the code of the DAC that adjusts the magnitude of the linearization feedback current.