

Register 3—Fault Status (address = 0x03) (Read-Only)

BIT #	D7	D6	D5	D4	D3	D2	D1	D0
BIT Name	0	0	0	0	F3	F2	F1	F0

This register is a read-only register. If the input voltage to the PGA exceeds the linear range of operation, the XTR108 will indicate this error condition (typically caused by a sensor fault) by setting the under-scale or over-scale error level depending on the state of the Alarm Configuration Register (address = 0x07). Information on the nature of the fault may be read in digital form with this register, as shown in Table 1. The remaining bits will be set to '0'.

Bit Descriptions

Reserved (Bits 7-4)

These bits are reserved and must be set to '0'.

F: Fault Status (Bits 3-0)

These bits give the fault status of the PGA input as specified in Table 1.

BIT	FAULT MODE
F0	Negative input exceeds positive limit
F1	Negative input exceeds negative limit
F2	Positive input exceeds positive limit
F3	Positive input exceeds negative limit

Table 1. Fault Status Register