PROCESSOR-SDK-RTOS-J721E ES 1.1 Patch

Introduction

This change is applicable for PROCESSOR-SDK-RTOS-J721E_07.02.00.06. This patch/changes enable J721e SR 1.1 GP & HS devices.
These changes will not affect ES 1.0. The archive **psdk_rtos_j721e_es11_src.tar.gz** contains all changes required, changes are limited to PDK & Vision_apps component only

> **TIFS Binary Note**
>
> - TIFS binary is the same for a GP device between ES1.0 and ES1.1.
> - TIFS binary is different for a HS device between ES1.0 and ES1.1.

Contents

Once the contents are unzipped, expect to see folder structure as depicted below:

- pdk_jacinto_07_01_05_14/packages/ti/board/
- pdk_jacinto_07_01_05_14/packages/ti/csl/
- pdk_jacinto_07_01_05_14/packages/ti/drv/csirx/
- pdk_jacinto_07_01_05_14/packages/ti/drv/sciclient/
- vision_apps/makerules/

To apply this patch/changes

1. On a fresh installation of PROCESSOR-SDK-RTOS-J721E_07.02.00.06
2. Copy the archive psdk_rtos_j721e_es11_src.tar.gz to ti-processor-sdk-rtos-j721e-evm-07_02_00_06\
3. Unzip with command "tar -xvf **psdk_rtos_j721e_es11_src.tar.gz**"

Changes

Top level changes are as listed below

Path	Description	Impact
pdk_jacinto_07_01_05_14/packages/ti/board	Details changes related to BOARD PLL	Bug-fix for both ES1.0/ES1.1
pdk_jacinto_07_01_05_14/packages/ti/csl/	Hyperflash demo examples updated	Bug-fix for both ES1.0/ES1.1
pdk_jacinto_07_01_05_14/packages/ti/drv/csirx/	Updated DPHY reset sequence - CSI2RX driver	Enables ES1.1 CSI2RX Bug-fix for CSI2RX shutdown sequence
pdk_jacinto_07_01_05_14/packages/ti/drv/sciclient/	TIFS updated to v2020.08d	Enables ES1.1 HS devices
vision_apps/makerules/	Updated SBL makefile	Required to build SBL for ES1.1 HS devices

Testing Done

These changes have undergone regression test with Vision Apps and PDK demo examples, on J721E ES 1.1 GP and HS devices

Known Issues

In additions to know issues listed in pdk_jacinto_07_01_05_14 release notes, listed below are know issues

- Board API's to configure McASP clock are not functional
- PDK OSPI examples applications do not complete as expected on mcu3_0 and mcu3_1