



## PROCESSOR-SDK-RTOS-J721E ES\_1.1 Patch for 7.1 release

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### Introduction

This change is applicable for PROCESSOR-SDK-RTOS-J721E\_07.01.00.11. This patch/changes enable J721e SR 1.1 GP & HS devices. These changes will not affect ES 1.0. The archive **psdk\_rtos\_j721e\_7.1\_es11\_src.tar.gz** contains all changes required, changes are limited to PDK & Vision\_apps component only

#### TIFS Binary Note

- TIFS binary is the same for a GP device between ES1.0 and ES1.1.
- TIFS binary is different for a HS device between ES1.0 and ES1.1.

### Contents

Once the contents are unzipped, expect to see folder structure as depicted below:

- pdk\_jacinto\_07\_01\_00\_45/packages/ti/board/
- pdk\_jacinto\_07\_01\_00\_45/packages/ti/csl/
- pdk\_jacinto\_07\_01\_00\_45/packages/ti/drv/csirx/
- pdk\_jacinto\_07\_01\_00\_45/packages/ti/drv/sciclient/
- vision\_apps/makerules/

### To apply this patch/changes

1. On a fresh installation of PROCESSOR-SDK-RTOS-J721E\_07.01.00.11
2. Copy the archive **psdk\_rtos\_j721e\_7.1\_es11\_src.tar.gz** to ti-processor-sdk-rtos-j721e-evm-07\_01\_00\_11\
3. Unzip with command "tar -xvf **psdk\_rtos\_j721e\_7.1\_es11\_src.tar.gz**"

### Changes

Top level changes are as listed below

Path	Description	Impact
pdk_jacinto_07_01_00_45/packages/ti/board	Details changes related to BOARD PLL	Bug-fix for both ES1.0/ES1.1
pdk_jacinto_07_01_00_45/packages/ti/csl/	Hyperflash demo examples updated	Bug-fix for both ES1.0/ES1.1
pdk_jacinto_07_01_00_45/packages/ti/drv/csirx/	Updated DPHY reset sequence - CSI2RX driver	Enables ES1.1 CSI2RX Bug-fix for CSI2RX shutdown sequence
pdk_jacinto_07_01_00_45/packages/ti/drv/sciclient/	TIFS updated to v2020.08d	Enables ES1.1 HS devices
vision_apps/makerules/	Updated SBL makefile	Required to build SBL for ES1.1 HS devices

### Testing Done

These changes have undergone regression test with Vision Apps and PDK demo examples, on J721E ES 1.1 GP and HS devices

### Known Issues

In additions to know issues listed in pdk\_jacinto\_07\_01\_00\_45 release notes, listed below are know issues

- Board API's to configure McASP clock are not functional
- PDK OSPI examples applications do not complete as expected on mcu3\_0 and mcu3\_1