

Patch Summary

Patch Target Release Name & Number	BIOS MCSDK 1.00.00.08 Patch 1
Date	December 22, 2010
Patch Installer	bios_mcsdk_1_00_00_08_patch1_setup.exe
Applicability	This Patch applies to the TMDSEVM6474L Platform only.

IR's Fixed:

IR#	State	Description
SDOCM00075656	Closed	Boot loader appears to fail to initialize DDR2 reliably so that start up from power-on or cold reset can fail. This is evidenced by both of the FPGA (yellow) LED's staying lit. If the NAND image was booted successfully they will turn off

Description of Fix for SDOCM00075656:

The TMDSEVM6474L has 3 processor cores. Since the HPDSPUA application is a single core application, the second and third cores must be kept in an idle state until an application is ready to launch them. The boot loader for the platform was not keeping the second and third cores in an idle state and they were, at times, interfering with the DDR2 initialization and operation. The boot loader and the post for the platform have been modified to keep the processors in an idle state to prevent this

List of Files Effected by this Patch

File (Including Patch)	Action
<TI_MCSDK_INSTALL_DIR>\bios_mcsdk_1_00_00_08_patch1_notes.pdf	Added
<TI_MCSDK_INSTALL_DIR>\packages\ti\platform\evm6474\bootloader_package\na ndboot\evmc64x_nand_bootloader.c	Modified
<TI_MCSDK_INSTALL_DIR>\packages\ti\platform\evm6474\bootloader_package\na ndboot\nanBoot.cmd	Modified
<TI_MCSDK_INSTALL_DIR>\packages\ti\platform\evm6474\bootloader_package\ev mc6474!_post\main.c	Modified
<TI_MCSDK_INSTALL_DIR>\packages\ti\platform\evm6474\bootloader_package\ev mc6474!_post\post.cmd	Modified

Special Installation or Use Instructions

You **must re-build** the boot loader and post for the TMDSEVM6474L and re-flash them to the EEPROM to correct this problem. To do so, use the following steps:

1. Apply the patch by running the patch installer, bios_mcsdk_1_00_00_08_patch1_setup.exe.
2. Follow the instructions in the BIOS-MCSDK Users Guide, http://processors.wiki.ti.com/index.php/BIOS_MCSDK_1.0_User_Guide, for Flashing I2C EEPROM (address 0x51) with NAND bootloader and the Post (address 0x50).

Note: This patch will overwrite the files listed as modified. To manually merge, install the patch to a separate directory.