

## **G726 on TMS320C64X/C64X+ Version 2.00.00**

### **Release Notes**

**January 2012**

The ITU G.726 converts A-law or U-law PCM input samples sampled at 8 KHz sampling rate into a 40, 32, 24, or 16 kbps ADPCM bit-stream.

Salient features of this product are described below.

- Bit Compliant with ITU-T G.726 specifications
- Optimized for TI C64x/C64x+ DSP
- C callable interface for encoder and decoder
- Re-entrant multi channel implementation
- Implementation compliant to eXpressDSP™
- Compliant with the eXpressDSP Multimedia (XDM) interface
- Supports RTP Special packing format as defined in RFC3551 and Linear packing format
- Fully interruptible Code
- Efficient Scratch memory management with reduced stack requirements
- The implementation support run time data buffers relocation and constant relocation
- Fully validated on TMS320C6455 DSK using CCS version 4.2 with the code generation tools version 7.2.0A10197
- This release supports both Big and Little Endian Mode of operation
- This release supports both ELF and COFF formats
- This codec can be used on any of the C6x based platforms such as DM644x, DM646x, OMAP3530, DM648, C6455, C674x, C64x+ and C66x.

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## **1 New In This Release**

- This release supports ELF format
- This release is validated for both ELF and COFF formats on TMS320C6455 DSK using Code Composer Studio 4.2 and Code Generation Tools 7.2.0A10197

## **2 System Requirements**

Refer to section 1.1.2 of G726\_XDAIS\_UserManual\_C64x.doc

## **3 Installing G726 on TMS320C64X/C64X+**

Refer to chapter 2 of G726\_XDAIS\_UserManual\_C64x.doc

## **4 Uninstall G726 on TMS320C64X/C64X+**

Refer to chapter 2 of G726\_XDAIS\_UserManual\_C64x.doc

## **5 Release Contents**

Library, User's Guide, Datasheet, Release Notes

## **6 Fixed In This Release**

- None

## **7 Known Issues**

- None