

G722 on TMS320C64X+ Version 2.00.00.000

Release Notes

January 2012

The ITU G.722 converts digitized, linear PCM input signals (15 bits) sampled at 16 KHz sampling rate into a 64Kbps SB-ADPCM bit-stream. Encoder Compresses linear PCM input signals (15 bits) sampled at 16 kHz sampling rate into a 64Kbps SB-ADPCM bit-stream and Decoder expands 48/56/64Kbps bitstream into PCM samples of 15 bits each at 16 KHz.

Salient features of this product are described below.

- Bit Compliant with ITU-T G.722 specifications
- Optimized for TI C64X+ DSP
- C callable interface for encoder and decoder
- Re-entrant multi channel implementation
- eXpressDSP™ compliant
- Fully interruptible Code
- Supports ITU-T G.722 Appendix IV Packet Loss Concealment (PLC)
- Relocatable tables
- The implementation support run time data buffers relocation
- Fully validated on TMS320C6455 DSK, using CCS version 4.2 with the code generation tools version 7.2.0A10197
- This release supports Little Endian and Big Endian Mode of operation
- This release supports both ELF and COFF formats
- This codec can be used on any of the C6x based platforms such as DM644x, DM646x, OMAP3530, DM648, C6455, C674x, C64x+ and C66x.

Copyright © 2012 Texas Instruments Incorporated. All rights reserved.

Information in this document is subject to change without notice. Texas Instruments may have pending patent applications, trademarks, copyrights, or other intellectual property rights covering matter in this document. The furnishing of this documents is given for usage with Texas Instruments products only and does not give you any license to the intellectual property that might be contained within this document. Texas Instruments makes no implied or expressed warranties in this document and is not responsible for the products based from this document.

TABLE OF CONTENTS

1	New In This Release.....	3
2	System Requirements	3
3	Building G722 on TMS320C64X+	3
4	Release Contents.....	3
5	Fixed In This Release	3
6	Known Issues.....	3

1 New In This Release

- This release supports ELF format. Both ELF and COFF Libraries are validated using Code Generations Tools version 7.2.0A10197

2 System Requirements

Refer to section 1.1.2 of G722_XDAIS_UserManual_C64xplus.doc

3 Building G722 on TMS320C64X+

Refer to chapter 2.5 of G722_XDAIS_UserManual_C64Xplus.doc

4 Release Contents

Refer to chapter 2.3 of G722_XDAIS_UserManual_C64Xplus.doc

5 Fixed In This Release

- None

6 Known Issues

- None