Compiler version	20.2.6.LTS
Compiler target	ARM
Date executed	27-Jan-22
Execution machines	Linux, Windows, and MacOSX
Tester	Greg Miller

test	passed	failed	compiler options (use cases)
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv4 --code_state=16 -rtti --exceptions -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv4 -rtti --exceptions -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv5e --code_state=16 -rtti --exceptions -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8611	0	-mv5e -rtti --exceptions -o4 --symdebug:dwarf_version=4
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv6 --code_state=16 -rtti --exceptions -o3 -oi0
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv6 -rtti --exceptions -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv6 -rtti --exceptions -o3 -mf5 -oi20
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv6M0 --code_state=16 -rtti -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv7M3 --code_state=16 -rtti -o3 -mf -oi20
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv7M3 -rtti -o4 -mf5 -me --parallel=2
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv7M4 -rtti --exceptions --code_state=16 -o1
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8624	0	-mv7M4 -rtti --exceptions --code_state=16 -o3 --embedded_constants=off

ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8611	0	-mv7M4 -rtti --exceptions -o4 -mf4 -me
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv7M4 -rtti --code_state=16 -o3 -mf2 --float_support=none -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv7R4 --code_state=16 -rtti -o2 -g
ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3	8255	0	-mv7R4 -rtti -o0 --symdebug:none -s
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -g -o2 -pdse 118
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -g -o2 -pdse 118 --symdebug:dwarf_version=4
ACE C99 SuperTest Mondrian Update 3	888	0	-mv5e -me -o3 --symdebug:dwarf_version=4
ACE C11 SuperTest Mondrian Update 3	245	0	-mv5e -me -o3 --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv4 --code_state=16 -o1 -mf3 -s
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv4 --code_state=16 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv4 --code_state=16 -o3 -mf -oi20 -g --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv4 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv4 -o3 -oi0 -mf1 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --float_support=vfpv2 -me -o0
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --float_support=vfpv2 -me -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --float_support=vfpv2 -me -o3 -mf
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --code_state=16 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --code_state=16 -o3 -me -ms -os
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --code_state=16 -o3 -mf3 --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e -o3

ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --symdebug:none -s
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6 --code_state=16 -o0 -ms -ss
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6 --code_state=16 -o2 -mf2 --symdebug:none
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6 -o3 -mf1 -g --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6M0 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e -g -o2 -pdse 118
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6M0 -o3 -mf0
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf0
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o0 -mf1
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -mf -oi20
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --code_state=16 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf4 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -mf2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf2
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --neon -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --float_support=vfpv2 -me -g -o2 -pdse 118
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me --neon -o2 -o3 -mf -oi20
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --unaligned_access=off -me -o3 -oi0 -mf0 --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M3 --code_state=16 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M3 --code_state=16 -o2 -mf3 -os

ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M3 --code_state=16 -o3 -oi0
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M3 -o4 -mf5 --parallel=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -o1
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv5e --code_state=16 -g -o2 -pdse 118 --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 --code_state=16 -o3 -mf5 -me
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M4 -o4 -mf4 -me
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 --code_state=16 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 --code_state=16 -o2 -mf2
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 --code_state=16 -o3 --symdebug:dwarf_version=4
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -o1
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv6M0 -o2 -pdse 118
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -o2 -g
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -ooff
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7R4 -o3
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7A8 --float_support=vfpv3 -me --unaligned_access=off -g -o2 -pdse 118
ACE C89 SuperTest Mondrian Update 3	2682	0	-mv7M3 --code_state=16 -g -o2 -pdse 118

ACE C89 SuperTest			
Mondrian Update 3	2682	0	-mv7M4 --float_support=fpv4spd16 -o2 -pdse 118
Applications	21	0	-mv7M4 -me -o3 -mf=1
Applications	20	0	-mv7M4 -me -o3 -mf=5
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv5e -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=0
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=1
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=2
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=3
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=4
Applications	25	0	-mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=5
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=0

Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=0
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=1
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=2
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=3
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=4
Applications	25	0	-mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=5
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=0
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=1
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=2
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=3
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=4
Applications	25	0	-mv5e -o4 -DRESTRICT= -mf=5
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv5e --code_state=16 -o3 -DRESTRICT= -mf=5

Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=0
Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=1
Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=2
Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=3
Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=4
Applications	25	0	-mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=5
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=0
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=1
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=2
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=3
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=4
Applications	25	0	-mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=5
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=0
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=1
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=2
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=3
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=4
Applications	25	0	-mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=5
Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=0
Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=1
Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=2
Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=3
Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=4

Applications	25	0	-mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=5
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=0
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=1
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=2
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=3
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=4
Applications	22	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=5
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=0
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=1
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=2
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=3
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=4
Applications	25	0	-mv4 -DRESTRICT= -o1 -os -g -me -mf=5
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv4 -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=0
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=1
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=2
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=3
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=4
Applications	25	0	-mv4 -DRESTRICT= -o3 -g -mf=5
Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=0
Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=1
Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=2

Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=3
Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=4
Applications	25	0	-mv4 -DRESTRICT= -o3 -oi20 -mf=5
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=0
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=1
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=2
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=3
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=4
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=5
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=0
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=1
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=2
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=3
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=4
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=5
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=0
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=1
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=2
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=3
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=4
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=5
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3

Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4
Applications	25	0	-mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=0
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=1
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=2
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=3
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=4
Applications	25	0	-mv5e -DRESTRICT= -o1 --plain_char=signed -mf=5
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv5e -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=0
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=1
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=2
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=3
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=4
Applications	25	0	-mv5e -DRESTRICT= -o2 --symdebug:none -mf=5
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=0
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=1
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=2
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=3
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=4
Applications	25	0	-mv5e -DRESTRICT= -o3 -oi20 -mf=5
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=2

Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=0
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=1
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=2
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=3
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=4
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=5
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=0
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=1
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=2
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=3
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=4
Applications	25	0	-mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=5
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=0
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=1

Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=2
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=3
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=4
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=5
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=0
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=1
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=2
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=3
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=4
Applications	25	0	-mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=5
Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=2

Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv6 -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=0
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=1
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=2
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=3
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=4
Applications	25	0	-mv6 -DRESTRICT= -o2 --symdebug:none -mf=5
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=0
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=1
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=2
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=3
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=4
Applications	25	0	-mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=5
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=0
Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=1

Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=2
Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=3
Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=4
Applications	25	0	-mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=1

Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon -me -DRESTRICT= -o3 -oi0
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o3 -oi20
Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=4

Applications	25	0	-mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=3

Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=0
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=1
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=2
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=3
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=4
Applications	25	0	-mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=5
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=4
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=5
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=0
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=1
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=2

Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=3
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=4
Applications	25	0	-mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=5
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=0
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=1
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=2
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=3
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=4
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=5
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=0
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=1
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=2
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=3
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=4
Applications	19	0	-mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=5
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=0
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=1
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=2
Applications	14	0	-mv7r4 --code_state=16 -o3 -DRESTRICT=
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=3
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=4
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=5
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=0
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=1

Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=2
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=3
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=4
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=5
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=0
Applications	13	0	-mv5e -o3 -k -DRESTRICT=
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=1
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=2
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=3
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=4
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=5
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=0
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=1
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=2
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=3
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=4
Applications	13	0	-mv5e --code_state=16 -o3 -k -DRESTRICT= --symdebug:dwarf_version=4
Applications	25	0	-mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=5
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=0
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=1
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=2
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=3
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=4
Applications	25	0	-mv7M4 -o4 -DRESTRICT= -me -mf=5
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=0
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=1

Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=2
Applications	13	0	-mv7A8 --neon -me --unaligned_access=off -o3 -k -DRESTRICT=
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=3
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=4
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=5
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=0
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=1
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=2
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=3
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=4
Applications	25	0	-mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=5
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=0
Applications	13	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o3 -k -DRESTRICT=
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=1
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=2
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=3
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=4
Applications	25	0	-mv7R4 -DRESTRICT= -o1 -mf=5
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=0
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=1
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=2
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=3
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=4
Applications	13	0	-mv7M3 --code_state=16 -o3 -k -DRESTRICT=
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -g -mf=5

Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=0
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=1
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=2
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=3
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=4
Applications	25	0	-mv7R4 -DRESTRICT= -o2 -os -g -mf=5
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=0
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=1
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=2
Applications	13	0	-mv7m4 --float_support=fpv4spd16 -o3 -k -DRESTRICT=
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=3
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=4
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=5
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=0
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=1
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=2
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=3
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=4
Applications	25	0	-mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=5
Applications	14	0	-mv7M4 --code_state=16 -g --symdebug:dwarf_version=4
Applications	14	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Applications	14	0	-mv7M4 --code_state=16 -o1
Applications	14	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
Applications	14	0	-mv7M4 --code_state=16 -o3 --float_support=none --enum_type=int -g
Applications	14	0	-mv7M4 --code_state=16 -o3 -me
Applications	14	0	-mv7M4 -o4 -me --symdebug:dwarf_version=4
Applications	39	0	-mv5e -o3 -mf=0
Applications	39	0	-mv5e -o3 -mf=1
Applications	39	0	-mv5e -o3 -mf=2
Applications	39	0	-mv5e -o3 -mf=3

Applications	39	0	-mv5e -o3 -mf=4
Applications	39	0	-mv5e -o3 -mf=5
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=0
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=1
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=2
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=3
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=4
Applications	39	0	-mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=5
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=0
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=1
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=2
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=3
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=4
Applications	39	0	-mv7M4 --code_state=16 -o3 -mf=5
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=0
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=1
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=2
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=3
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=4
Applications	39	0	-mv6M0 --code_state=16 -o3 -mf=5
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=0
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=1
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=2
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=3
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=4
Applications	48	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=5
Applications	10	0	-mv7M4 -me -o3 -mf=0
Applications	10	0	-mv7M4 -me -o3 -mf=1
Applications	10	0	-mv7M4 -me -o3 -mf=2

Applications	10	0	-mv7M4 -me -o3 -mf=3
Applications	10	0	-mv7M4 -me -o3 -mf=4
Applications	10	0	-mv7M4 -me -o3 -mf=5
Bugslayer Regression Testing	1727	0	
Bugslayer Regression Testing	1227	0	-mv5e --code_state=16 -o2 -g
Bugslayer Regression Testing	1227	0	-mv5e --code_state=16 -o3 -oi0
Bugslayer Regression Testing	1227	0	-mv5e -o2 -g
Bugslayer Regression Testing	1227	0	-mv5e -o2 -g --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv5e -o3 -oi0
Bugslayer Regression Testing	1227	0	-mv6 --code_state=16 -o2 -g
Bugslayer Regression Testing	1227	0	-mv6 -o1 --symdebug:none
Bugslayer Regression Testing	1227	0	-mv6 -o2 -g
Bugslayer Regression Testing	1227	0	-mv6 -o3 -g -os
Bugslayer Regression Testing	1227	0	-mv6 -o3 -oi0 -mf0
Bugslayer Regression Testing	1227	0	-mv6 -o3 -oi20 -mf3 --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv6M0 --code_state=16 -o1 -mf4
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -mf0 -oi0
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -oi20
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o1
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o0
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o2 -g

Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf5 -oi20
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o1 -mf2 --symdebug:none
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o3 -mf3
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o1
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi0
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf0
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon -o0 -ms -mf0
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me --neon -o2 -mf1
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me -o1 -ms
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me -o2 -g
Bugslayer Regression Testing	1227	0	-mv7A8 --unaligned_access=off -me -o3
Bugslayer Regression Testing	1227	0	-mv7M3 --code_state=16 -o1 -mf4 --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7M3 --code_state=16 -o2
Bugslayer Regression Testing	1227	0	-mv7M3 -o4 -mf2
Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -g
Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -me -o3 -mf2 --float_support=none --enum_type=int -g

Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -o1 -me
Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7M4 --code_state=16 -o3 -mf5 -me
Bugslayer Regression Testing	1227	0	-mv7M4 -o4 -mf4 -me --parallel=2
Bugslayer Regression Testing	1227	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
Bugslayer Regression Testing	1227	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
Bugslayer Regression Testing	1227	0	-mv7R4 --code_state=16 -o2 -g
Bugslayer Regression Testing	1227	0	-mv7R4 --code_state=16 -o3 -mf4 -oi20 -g --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7R4 -o1
Bugslayer Regression Testing	1227	0	-mv7R4 -o2 -g
Bugslayer Regression Testing	1227	0	-mv5e -o3 -mf -oi25 --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv5e --pack_all_structs
Bugslayer Regression Testing	1227	0	-mv6M0 -g --opt_level=off
Bugslayer Regression Testing	1227	0	-mv6M0 -o3 -g
Bugslayer Regression Testing	1227	0	-mv7A8 -o3 -mf -g --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5
Bugslayer Regression Testing	1227	0	-mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 --pack_all_structs --unaligned_access=off
Bugslayer Regression Testing	1227	0	-mv7m4 --float_support=fpv4spd16 -o2 -mf1
Bugslayer Regression Testing	1227	0	-mv7r4 --float_support=vfpv3d16 -mf3
Bugslayer Regression Testing	1227	0	-mv7r4 --float_support=vfpv3d16 -o1 --pack_all_structs --unaligned_access=off
Bugslayer Regression Testing	1227	0	-mv7r4 --float_support=vfpv3d16 -o2 -mf3 --pack_all_structs --unaligned_access=off
Bugslayer Regression Testing	1227	0	-mv7R4 -o3 -mf2

Bugslayer Regression Testing	1227	0	-mv7R4 -o3 -oi0 --code_state=16 --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv7r4 -o4 -mf --parallel=4
Bugslayer Regression Testing	1227	0	-mv4 --code_state=16 -o0
Bugslayer Regression Testing	1227	0	-mv4 --code_state=16 -o2
Bugslayer Regression Testing	1227	0	-mv4 -o1 -ms --symdebug:dwarf_version=4
Bugslayer Regression Testing	1227	0	-mv4 -o2 -g
Bugslayer Regression Testing	1227	0	-mv4 -o2 -mf1 -me -rr5 -rr6
Bugslayer Regression Testing	1227	0	-mv5e --float_support=vfpv2 -me -o1
Bugslayer Regression Testing	1227	0	-mv5e --float_support=vfpv2 -me -o2 -g
Bugslayer Regression Testing	1227	0	-mv5e --float_support=vfpv2 -me -o3
Bugslayer Regression Testing	1227	0	-mv5e --code_state=16 -o0
Code Sourcery	1448	0	-mv7r4 -Ooff
Code Sourcery	1448	0	-mv5e -me
Code Sourcery	1448	0	-mv5e --code_state=16 -me
Code Sourcery	1448	0	-mv6 -me
Code Sourcery	1448	0	-mv6 --code_state=16 -me
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off -me --symdebug:dwarf_version=4
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off --float_support=vfpv3 --neon -me
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off --code_state=16 -me
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off --code_state=16 --float_support=vfpv3 --neon -me
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off --code_state=16 --neon -me
Code Sourcery	1448	0	-mv7A8 --unaligned_access=off --neon -me
Code Sourcery	1448	0	-mv7R4 -me --symdebug:dwarf_version=4
Code Sourcery	1448	0	-mv7R4 --code_state=16 -me
Dinkumware	426	0	-mv5e --code_state=16 -o2 --symdebug:dwarf_version=4
Dinkumware	426	0	-mv7A8 --neon -me --unaligned_access=off -o2
Dinkumware	426	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2
Dinkumware	426	0	-mv7M3 --code_state=16 -o2

Dinkumware	426	0	-mv7m4 --float_support=fpv4spd16 -o2
Dinkumware	426	0	-mv4 --code_state=16 -o1 -mf3 -s
Dinkumware	426	0	-mv4 --code_state=16 -o2 -g
Dinkumware	426	0	-mv4 --code_state=16 -o3 -mf -oi20 -g
Dinkumware	426	0	-mv4 -o2 -g --symdebug:dwarf_version=4
Dinkumware	426	0	-mv4 -o3 -oi0 -mf1 -g
Dinkumware	426	0	-mv5e --float_support=vfpv2 -me -o0
Dinkumware	426	0	-mv5e --float_support=vfpv2 -me -o2 -g
Dinkumware	426	0	-mv5e --float_support=vfpv2 -me -o3 -mf
Dinkumware	426	0	-mv5e --code_state=16 -o2 -g
Dinkumware	426	0	-mv5e --code_state=16 -o2 -mf3
Dinkumware	426	0	-mv5e --code_state=16 -o3 -me -ms -os
Dinkumware	426	0	-mv5e -o2 -g
Dinkumware	426	0	-mv5e -o3
Dinkumware	426	0	-mv5e --symdebug:none -s
Dinkumware	204	0	-mv6 --code_state=16 -o0 -ms -ss --symdebug:dwarf_version=4
Dinkumware	426	0	-mv6 --code_state=16 -o2 -mf2 --symdebug:none
Dinkumware	426	0	-mv6 -o3 -mf1 -g
Dinkumware	426	0	-mv6M0 -o2 -g
Dinkumware	459	0	-mv6M0 -ooff
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf0
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o0 -mf1
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -mf
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf3
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -mf2 -g
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o3 -oi0
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -mf5 -g --symdebug:dwarf_version=4
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf5
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me --neon -o1 -mf2 --symdebug:none
Dinkumware	426	0	-mv7A8 --unaligned_access=off -me -o3 -mf4

Dinkumware	426	0	-mv7M3 --code_state=16 -o2 -mf3 -os
Dinkumware	426	0	-mv7M3 --code_state=16 -o3 -oi0 --symdebug:dwarf_version=4
Dinkumware	366	0	-mv7M3 -o4 -mf1 -me --parallel=2
Dinkumware	459	0	-mv7M4 --code_state=16 -g
Dinkumware	459	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Dinkumware	426	0	-mv7M4 --code_state=16 -o1
Dinkumware	426	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
Dinkumware	426	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none -g
Dinkumware	426	0	-mv7M4 --code_state=16 -o3 -mf5 -me
Dinkumware	366	0	-mv7M4 -o4 -mf4 -me
Dinkumware	426	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
Dinkumware	426	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
Dinkumware	426	0	-mv7R4 --code_state=16 -o2 -g
Dinkumware	426	0	-mv7R4 --code_state=16 -o2 -mf2 --symdebug:dwarf_version=4
Dinkumware	426	0	-mv7R4 --code_state=16 -o3
Dinkumware	426	0	-mv7R4 -o1
Dinkumware	426	0	-mv7R4 -o2 -g
Dinkumware	426	0	-mv7R4 -o3
Embedded Systems Paranoia	1	0	-mv7M3 --code_state=16 -o2 -mf1 --symdebug:dwarf_version=4
Embedded Systems Paranoia	1	0	-mv7M3 --code_state=16 -o3 -mf
GCC Extensions	34	0	-mv4 -o2
GCC Extensions	34	0	-mv5e -o2 --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv6M0 -o2
GCC Extensions	34	0	-mv7A8 --neon -me --unaligned_access=off -o2
GCC Extensions	34	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2
GCC Extensions	34	0	-mv7M3 --code_state=16 -o2
GCC Extensions	34	0	-mv7R4 --code_state=16 -o2
GCC Extensions	34	0	-mv5e --code_state=16 -o2 --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv6 --code_state=16 -o2
GCC Extensions	34	0	-mv7m4 --float_support=fpv4spd16 -o2
GCC Extensions	34	0	-mv4 --code_state=16 -o2 -g
GCC Extensions	34	0	-mv4 --code_state=16 -o2 -mf3 -g
GCC Extensions	34	0	-mv4 -o3 -oi0 -mf0
GCC Extensions	34	0	-mv5e --float_support=vfpv2 -me -o1

GCC Extensions	34	0	-mv5e --float_support=vfpv2 -me -o2 -g --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv5e --code_state=16 -o2 -g
GCC Extensions	34	0	-mv5e --code_state=16 -o3
GCC Extensions	34	0	-mv5e -o2 -g -me
GCC Extensions	34	0	-mv5e -o3 -oi0 -g
GCC Extensions	34	0	-mv6 --code_state=16 -o3 -oi0
GCC Extensions	34	0	-mv6 -o2 -g --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv6 -o3 -mf5 -oi20
GCC Extensions	34	0	-mv6M0 --code_state=16 -o3 -mf -oi20
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf3 -g
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -oi0 -mf0
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -mf3 -g
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi20 -mf5
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf2
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me --neon -o3 -mf5 --plain_char=signed
GCC Extensions	34	0	-mv7A8 --unaligned_access=off -me -o2
GCC Extensions	34	0	-mv7M3 --code_state=16 -o3 -mf -oi20 --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv7M3 -o4 -mf3 -me --parallel=2
GCC Extensions	34	0	-mv7M4 --code_state=16 -g
GCC Extensions	34	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
GCC Extensions	34	0	-mv7M4 --code_state=16 -o1
GCC Extensions	34	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
GCC Extensions	34	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g
GCC Extensions	34	0	-mv7M4 --code_state=16 -o3 -mf5 -me
GCC Extensions	34	0	-mv7M4 -o4 -mf4 -me
GCC Extensions	34	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g

GCC Extensions	34	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
GCC Extensions	34	0	-mv7R4 --code_state=16 -o2 -mf2 -os --symdebug:dwarf_version=4
GCC Extensions	34	0	-mv7R4 -o0 --symdebug:none -s
GCC Extensions	34	0	-mv7R4 -o2 -g
GCC Torture Test	1064	0	-mv4 -o2
GCC Torture Test	1064	0	-mv5e -o2
GCC Torture Test	1064	0	-mv5e --code_state=16 -o2 --symdebug:dwarf_version=4
GCC Torture Test	1064	0	-mv6 --code_state=16 -o2
GCC Torture Test	1064	0	-mv6M0 -o
GCC Torture Test	1064	0	-mv7A8 --neon -me --unaligned_access=off -o2
GCC Torture Test	1064	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2
GCC Torture Test	1064	0	-mv7M3 --code_state=16 -o2
GCC Torture Test	1064	0	-mv7m4 --float_support=fpv4spd16 -o2
GCC Torture Test	1064	0	-mv7R4 --code_state=16 -o2
Kernels	77	0	-mv4 -k -o2
Kernels	77	0	-mv4 --code_state=16 -k -o2
Kernels	77	0	-mv5e --float_support=VFPv2 -me -o2
Kernels	77	0	-mv5e -o1
Kernels	77	0	-mv5e -o2 -me --symdebug:dwarf_version=4
Kernels	77	0	-mv5e --code_state=16 -o0
Kernels	77	0	-mv5e --code_state=16 -o2 -me
Kernels	77	0	-mv6 -o2 -mf -oi20
Kernels	77	0	-mv6 --code_state=16 -o3 -oi0 -me
Kernels	39	0	-mv7a8 --neon --float_support=vfpv3 --code_state=16 -o3 -me --unaligned_access=off -mf -s --symdebug:none
Kernels	39	0	-mv7a8 --neon --float_support=vfpv3 -o3 -me --unaligned_access=off -mf -s --symdebug:none
Kernels	39	0	-mv7a8 --neon --code_state=16 -o3 -me --unaligned_access=off -mf -s --symdebug:none
Kernels	39	0	-mv7a8 --neon -o3 -me --unaligned_access=off -mf -s --symdebug:none
Kernels	77	0	-mv7A8 --unaligned_access=off --neon -me -o2
Kernels	77	0	-mv7A8 --unaligned_access=off --code_state=16 --neon -me -o2
Kernels	77	0	-mv7M3 --code_state=16 -o2 --symdebug:dwarf_version=4
Kernels	77	0	-mv7R4 --code_state=16 -o2
Kernels	77	0	-mv5e -o3 -mf=0
Kernels	77	0	-mv5e -o3 -mf=1
Kernels	77	0	-mv5e -o3 -mf=2
Kernels	77	0	-mv5e -o3 -mf=3

Kernels	77	0	-mv5e -o3 -mf=4
Kernels	77	0	-mv5e -o3 -mf=5
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=0
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=1
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=2
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=3
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=4
Kernels	77	0	-mv7M3 --code_state=16 -o3 -mf=5
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=0
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=1
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=2
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=3
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=4
Kernels	77	0	-mv7M4 --code_state=16 -o3 -mf=5
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=0
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=1
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=2
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=3
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=4
Kernels	77	0	-mv6M0 --code_state=16 -o3 -mf=5
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=0
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=1
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=2
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=3
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=4
Kernels	77	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=5
Nemics R4	1	0	-mv5e --code_state=16 --symdebug:dwarf_version=4
Nemics R4	1	0	-mv7A8 --neon -me --unaligned_access=off
Nemics R4	1	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off
Nemics R4	1	0	-mv7M3 --code_state=16
Nemics R4	1	0	-mv7R4
Nemics R4	1	0	-mv7R4 --code_state=16
Perennial C11	33598	0	-mv5e -g -o2 --symdebug:dwarf_version=4

Perennial C11	33598	0	-mv6M0 --code_state=16 -g -o2
Perennial C11	33596	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off --float_support=vfpv3 -me -g -o2
Perennial C11	33598	0	-mv7M3 --code_state=16 -g -o2
Perennial C11	33598	0	-mv7m4 --float_support=fpv4spd16 -g -o2
Perennial C11	33598	0	-mv7R4 -g -o2
Perennial C11	33598	0	-mv7R4 --code_state=16 -g -o2
Perennial C11	33598	0	-mv4 --code_state=16 -o2 -g
Perennial C11	33598	0	-mv7m4 -o2
Perennial C11	33598	0	-mv7m4 -o2
Perennial C90	9278	0	-mv4 --code_state=16 -o2 -mf3 -g
Perennial C11	33598	0	-mv4 -o2 -g --symdebug:dwarf_version=4
Perennial C11	33598	0	-mv4 -o3 -oi0 -mf0
Perennial C90	9276	0	-mv5e --float_support=vfpv2 -me -o1
Perennial C99	29009	0	-mv5e --float_support=vfpv2 -me -o2 -g
Perennial C11	33598	0	-mv5e --code_state=16 -o2 -g
Perennial C11	33598	0	-mv5e --code_state=16 -o3
Perennial C11	33598	0	-mv5e -o2 -g -me
Perennial C11	33598	0	-mv5e -o3 -oi0 -g
Perennial C90	9278	0	-mv6 --code_state=16 -o3 -oi0 --symdebug:dwarf_version=4
Perennial C11	33598	0	-mv6 -o2 -g
Perennial C11	33598	0	-mv6 -o3 -mf5 -oi20
Perennial C11	33598	0	-mv6M0 --code_state=16 -o3 -mf -oi20
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf3 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -oi0 -mf0
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o2 -mf -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi20 -mf5
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g

Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --neon -o2 -g -mf3
Perennial C11	33577	0	-mv7A8 --unaligned_access=off -me --neon -o3 -mf4 --plain_char=signed
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me -o2
Perennial C11	33598	0	-mv7M3 --code_state=16 -o3 -mf -oi20
Perennial C11	33598	0	-mv7M3 -o4 -mf2 --symdebug:dwarf_version=4 --parallel=2
Perennial C11	33598	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Perennial C11	33598	0	-mv7M4 --code_state=16 -g
Perennial C11	33598	0	-mv7M4 --code_state=16 -o1
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 -mf5 -me
Perennial C11	33598	0	-mv7M4 -o4 -mf4 -me --symdebug:dwarf_version=4
Perennial C11	33596	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
Perennial C11	33596	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
Perennial C11	33598	0	-mv7R4 --code_state=16 -o2 -mf2 -os
Perennial C99	29011	0	-mv7R4 -o0 --symdebug:none -s
Perennial C11	33598	0	-mv5e -g -o2
Perennial C11	33598	0	-mv6M0 --code_state=16 -g -o2
Perennial C11	33596	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off --float_support=vfpv3 -me -g -o2
Perennial C11	33598	0	-mv7M3 --code_state=16 -g -o2
Perennial C11	33598	0	-mv7m4 --float_support=fpv4spd16 -g -o2
Perennial C11	33598	0	-mv7R4 -g -o2
Perennial C11	33598	0	-mv7R4 --code_state=16 -g -o2
Perennial C11	33598	0	-mv4 --code_state=16 -o2 -g
Perennial C11	33598	0	-mv4 --code_state=16 -o2 -mf3 -g --symdebug:dwarf_version=4
Perennial C11	33598	0	-mv4 -o2 -g
Perennial C11	33598	0	-mv4 -o3 -oi0 -mf0
Perennial C11	33596	0	-mv5e --float_support=vfpv2 -me -o1
Perennial C11	33596	0	-mv5e --float_support=vfpv2 -me -o2 -g
Perennial C11	33598	0	-mv5e --code_state=16 -o2 -g
Perennial C11	33598	0	-mv5e --code_state=16 -o3
Perennial C11	33598	0	-mv5e -o2 -g -me
Perennial C11	33598	0	-mv5e -o3 -oi0 -g

Perennial C11	33598	0	-mv6 --code_state=16 -o3 -oi0
Perennial C11	33598	0	-mv6 -o2 -g --symdebug:dwarf_version=4
Perennial C11	33598	0	-mv6 -o3 -mf5 -oi20
Perennial C11	33598	0	-mv6M0 --code_state=16 -o3 -mf -oi20
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf3 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -oi0 -mf0
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o2 -mf -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi20 -mf5
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me --neon -o2 -g -mf3
Perennial C11	33577	0	-mv7A8 --unaligned_access=off -me --neon -o3 -mf4 --plain_char=signed
Perennial C11	33596	0	-mv7A8 --unaligned_access=off -me -o2
Perennial C11	33598	0	-mv7M3 --code_state=16 -o3 -mf -oi20
Perennial C11	33598	0	-mv7M3 -o4 -mf2 --symdebug:dwarf_version=4
Perennial C11	33598	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Perennial C11	33598	0	-mv7M4 --code_state=16 -g
Perennial C11	33598	0	-mv7M4 --code_state=16 -o1
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g
Perennial C11	33598	0	-mv7M4 --code_state=16 -o3 -mf5 -me
Perennial C11	33598	0	-mv7M4 -o4 -mf4 -me --parallel=2
Perennial C11	33596	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g

Perennial C11	33596	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
Perennial C11	33598	0	-mv7R4 --code_state=16 -o2 -mf2 -os
Perennial C11	33598	0	-mv7R4 -o0 --symdebug:none -s
Perennial C++14	242997	0	-mv4 --code_state=16 -rtti --exceptions -o2 -g
Perennial C++14	242997	0	-mv4 --code_state=16 -rtti --exceptions -o2 -mf3 -g --symdebug:dwarf_version=4
Perennial C++14	242997	0	-mv4 -rtti --exceptions -o2 -g
Perennial C++14	242997	0	-mv4 -rtti --exceptions -o3 -oi0 -mf0
Perennial C++14	242997	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions -o1
Perennial C++14	242997	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions -o2 -g
Perennial C++14	242997	0	-mv5e --code_state=16 -rtti --exceptions -o2 -g
Perennial C++14	242997	0	-mv5e --code_state=16 -rtti --exceptions -o3
Perennial C++14	242997	0	-mv5e -rtti --exceptions -o2 -g -me
Perennial C++14	242997	0	-mv5e -rtti --exceptions -o3 -oi0 -g
Perennial C++14	242950	0	-mv5e -rtti --exceptions -o4 --symdebug:dwarf_version=4
Perennial C++14	242997	0	-mv6 --code_state=16 -rtti --exceptions -o3 -oi0
Perennial C++14	242997	0	-mv6 -rtti --exceptions -o2 -g
Perennial C++14	242997	0	-mv6 -rtti --exceptions -o3 -mf5 -oi20
Perennial C++14	213350	0	-mv6M0 --code_state=16 -rtti -o2 -g
Perennial C++14	267446	0	-mv6M0 -rtti -ooff -mf -oi20
Perennial C++14	213350	0	-mv7M3 --code_state=16 -rtti -o2 -g
Perennial C++14	213350	0	-mv7M3 --code_state=16 -rtti -o3 -mf -oi20
Perennial C++14	213303	0	-mv7M3 -rtti -o4 -mf5 -me --parallel=2
Perennial C++14	297109	0	-mv7M4 -rtti --exceptions --code_state=16 -g
Perennial C++14	297109	0	-mv7M4 -rtti --exceptions --code_state=16 -g -me --embedded_constants=off
Perennial C++14	243013	0	-mv7M4 -rtti --exceptions --code_state=16 -o1
Perennial C++14	243013	0	-mv7M4 -rtti --exceptions --code_state=16 -o3 --embedded_constants=off
Perennial C++14	243013	0	-mv7M4 -rtti --exceptions --code_state=16 -o3 -mf5 -me
Perennial C++14	242966	0	-mv7M4 -rtti --exceptions -o4 -mf4 -me
Perennial C++14	213350	0	-mv7M4 -rtti --code_state=16 -o3 -mf2 --float_support=none -g
Perennial C++14	213350	0	-mv7R4 --code_state=16 -rtti -o2 -g
Perennial C++14	213350	0	-mv7R4 --code_state=16 -rtti -o2 -mf2 -os --symdebug:dwarf_version=4
Perennial C++14	49460	0	-mv7m4 -o2
Perennial C++14	49460	0	-mv7m4 -o2 --symdebug:dwarf_version=4
Perennial C++14	213350	0	-mv7R4 -rtti -o0 --symdebug:none -s
Perennial C++14	213350	0	-mv7R4 -rtti -o2 -g
PHIL Regression Testing	2370	0	

PHIL Regression Testing	7274	0	
PHIL Regression Testing	131	0	-mv7m4 --float_support=fpv4spd16 -o2 -mf1
PHIL Regression Testing	128	0	-mv7r4 --float_support=vfpv3d16 -mf3
PHIL Regression Testing	130	0	-mv7R4 -o3 -mf2
PHIL Regression Testing	131	0	-mv7R4 -o3 -oi0 --code_state=16 --symdebug:dwarf_version=4
PHIL Regression Testing	130	0	-mv7M3 --code_state=16 -o2
PHIL Regression Testing	128	0	-mv7M4 --code_state=16 -o1 -me
PHIL Regression Testing	128	0	-mv5e -o3 -mf -oi25 --symdebug:dwarf_version=4
PHIL Regression Testing	129	0	-mv5e --pack_all_structs
PHIL Regression Testing	126	0	-mv6M0 -g --opt_level=off
PHIL Regression Testing	128	0	-mv6M0 -o3 -g
PHIL Regression Testing	126	0	-mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5
PHIL Regression Testing	127	0	-mv7r4 --float_support=vfpv3d16 -o1 --pack_all_structs --unaligned_access=off
PHIL Regression Testing	127	0	-mv7r4 --float_support=vfpv3d16 -o2 -mf3 --pack_all_structs --unaligned_access=off
PHIL Regression Testing	126	0	-mv7r4 -o4 -mf --parallel=4
PHIL Regression Testing	126	0	-mv7A8 -o3 -mf -g --symdebug:dwarf_version=4
PHIL Regression Testing	126	0	-mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 --pack_all_structs --unaligned_access=off
PHIL Regression Testing	127	0	-mv4 --code_state=16 -o0
PHIL Regression Testing	127	0	-mv4 --code_state=16 -o2
PHIL Regression Testing	127	0	-mv4 -o1 -ms --symdebug:dwarf_version=4
PHIL Regression Testing	128	0	-mv4 -o2 -g
PHIL Regression Testing	127	0	-mv4 -o2 -mf1 -me -rr5 -rr6
PHIL Regression Testing	126	0	-mv5e --float_support=vfpv2 -me -o1
PHIL Regression Testing	127	0	-mv5e --float_support=vfpv2 -me -o2 -g
PHIL Regression Testing	128	0	-mv5e --float_support=vfpv2 -me -o3
PHIL Regression Testing	127	0	-mv5e --code_state=16 -o0
PHIL Regression Testing	127	0	-mv5e --code_state=16 -o2 -g
PHIL Regression Testing	128	0	-mv5e --code_state=16 -o3 -oi0
PHIL Regression Testing	128	0	-mv5e -o2 -g
PHIL Regression Testing	128	0	-mv5e -o2 -g --symdebug:dwarf_version=4
PHIL Regression Testing	129	0	-mv5e -o3 -oi0
PHIL Regression Testing	127	0	-mv6 --code_state=16 -o2 -g
PHIL Regression Testing	125	0	-mv6 -o1 --symdebug:none
PHIL Regression Testing	128	0	-mv6 -o2 -g
PHIL Regression Testing	129	0	-mv6 -o3 -g -os
PHIL Regression Testing	129	0	-mv6 -o3 -oi0 -mf0
PHIL Regression Testing	128	0	-mv6 -o3 -oi20 -mf3 --symdebug:dwarf_version=4
PHIL Regression Testing	127	0	-mv6M0 --code_state=16 -o1 -mf4

PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g
PHIL Regression Testing	127	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -mf0 -oi0
PHIL Regression Testing	127	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -oi20
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o1
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o2 -g
PHIL Regression Testing	127	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o0
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o2 -g
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o3 -mf5 -oi20
PHIL Regression Testing	123	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o1 -mf2 --symdebug:none
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o2 -g
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon -o3 -mf3
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o1
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4
PHIL Regression Testing	127	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi0
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf0
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me --neon -o0 -ms -mf0
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --neon -o2 -g
PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me --neon -o2 -mf1
PHIL Regression Testing	125	0	-mv7A8 --unaligned_access=off -me -o1 -ms

PHIL Regression Testing	126	0	-mv7A8 --unaligned_access=off -me -o2 -g
PHIL Regression Testing	127	0	-mv7A8 --unaligned_access=off -me -o3
PHIL Regression Testing	129	0	-mv7M3 --code_state=16 -o1 -mf4 --symdebug:dwarf_version=4
PHIL Regression Testing	129	0	-mv7M3 -o4 -mf2
PHIL Regression Testing	128	0	-mv7M4 --code_state=16 -g
PHIL Regression Testing	126	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
PHIL Regression Testing	129	0	-mv7M4 --code_state=16 -me -o3 -mf2 --float_support=none --enum_type=int -g
PHIL Regression Testing	132	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off --symdebug:dwarf_version=4
PHIL Regression Testing	129	0	-mv7M4 --code_state=16 -o3 -mf5 -me
PHIL Regression Testing	127	0	-mv7M4 -o4 -mf4 -me --parallel=2
PHIL Regression Testing	129	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
PHIL Regression Testing	130	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
PHIL Regression Testing	130	0	-mv7R4 --code_state=16 -o2 -g
PHIL Regression Testing	130	0	-mv7R4 --code_state=16 -o3 -mf4 -oi20 -g --symdebug:dwarf_version=4
PHIL Regression Testing	128	0	-mv7R4 -o1
PHIL Regression Testing	129	0	-mv7R4 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 -o2 -me
Plum Hall ANSI/ISO C Conformance	29446	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off --pack_all_structs -mf5 -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 -o2
Plum Hall ANSI/ISO C Conformance	28871	0	-mv4 --code_state=16 -o0 --symdebug:none
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 --code_state=16 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 --code_state=16 -o2 -g --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 --code_state=16 -o2 -mf2 --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 --code_state=16 -o3 -oi0
Plum Hall ANSI/ISO C Conformance	29446	0	-mv4 -o1 -me -ms --plain_char=signed

Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 -o2 -mf0 --symdebug:none
Plum Hall ANSI/ISO C Conformance	29447	0	-mv4 -o3 -mf -oi20
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --float_support=vfpv2 -me -o2
Plum Hall ANSI/ISO C Conformance	28871	0	-mv5e --float_support=vfpv2 -me -o0
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --float_support=vfpv2 -me -o3 -mf -oi20
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --code_state=16 -o1 -g --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --code_state=16 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --code_state=16 -o2 -mf5
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --code_state=16 -o3 -oi0 -me
Plum Hall ANSI/ISO C Conformance	28870	0	-mv5e -o0 -ms --plain_char=signed
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e -o2 -mf3 --symdebug:none
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e -o3 -mf -oi20
Plum Hall ANSI/ISO C Conformance	29447	0	-mv5e --code_state=16 -o2 --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv6 --code_state=16 -o2 -g
Plum Hall ANSI/ISO C Conformance	29446	0	-mv6 -o1 -me --plain_char=signed
Plum Hall ANSI/ISO C Conformance	29447	0	-mv6 -o3 -mf -oi20
Plum Hall ANSI/ISO C Conformance	29447	0	-mv6M0 -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv6M0 --code_state=16 -o1 -ms -os
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7A8 -me --unaligned_access=off -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 -me --unaligned_access=off -o3 -mf -oi25
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --neon -me --unaligned_access=off -o2 --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2

Plum Hall ANSI/ISO C Conformance	29446	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o1 --plain_char=signed -ms -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -mf5 -oi20
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o0 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o1 --symdebug:none
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o3 -mf -oi20
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o3 -mf2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me --neon --code_state=16 -o2 -mf3
Plum Hall ANSI/ISO C Conformance	29446	0	-mv7A8 --unaligned_access=off -me --neon -o1 --plain_char=signed -ms
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7A8 --unaligned_access=off -me -o0
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7A8 --unaligned_access=off -me -o3 -mf5 --symdebug:none
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M3 --code_state=16 -o1 -ms -os
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M3 --code_state=16 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M3 --code_state=16 -o3 -mf -oi20 --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M3 -o4 -mf2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 --float_support=fpv4spd16 -o2
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7M4 --code_state=16 -g

Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 --code_state=16 -o1
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 --code_state=16 -o3 -mf5 -me
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 -o4 -mf4 -me --symdebug:dwarf_version=4 --parallel=2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 -me --float_support=none -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7M4 -me -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
Plum Hall ANSI/ISO C Conformance	28871	0	-mv7R4 --code_state=16 -o0 -s
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 --code_state=16 -o2 -g -me
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 --code_state=16 -o2
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 --code_state=16 -o3 -oi0
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -o1 -g --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -o2 -g
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -o2 -mf5
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 -o3 -mf3
Plum Hall ANSI/ISO C Conformance	29447	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -mf5 -o2
Plum Hall C++14	2878	0	-mv6 --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv6 --code_state=16 -rtti --exceptions --extern_c_can_throw -o3 -oi0 -mf3
Plum Hall C++14	2878	0	-mv6 -rtti --exceptions --extern_c_can_throw -o1 -mf0
Plum Hall C++14	2841	0	-mv6M0 --code_state=16 -rtti -o1
Plum Hall C++14	2841	0	-mv6M0 --code_state=16 -rtti -o2 -g

Plum Hall C++14	2841	0	-mv6M0 --code_state=16 -rtti -o3 -mf -oi20
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o0 -g
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o3 -mf5 -oi20
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -g
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --code_state=16 --neon --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --code_state=16 -rtti --exceptions --extern_c_can_throw -o1
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --code_state=16 -rtti --exceptions --extern_c_can_throw -o3 -mf -oi20
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o3 -mf4
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --neon --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 --plain_char=signed -ms
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --neon --code_state=16 -rtti --exceptions --extern_c_can_throw -o3 -mf5
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me --neon -rtti --exceptions --extern_c_can_throw -g
Plum Hall C++14	2878	0	-mv7A8 --unaligned_access=off -me -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2841	0	-mv7M3 --code_state=16 -rtti -o1
Plum Hall C++14	2841	0	-mv7M3 --code_state=16 -rtti -o2 -g
Plum Hall C++14	2841	0	-mv7M3 --code_state=16 -rtti -o3 -mf -oi20 --symdebug:dwarf_version=4
Plum Hall C++14	2841	0	-mv7M3 -rtti -o4 -mf2
Plum Hall C++14	2877	0	-mv7M4 --code_state=16 -rtti --exceptions -g -me --embedded_constants=off
Plum Hall C++14	2877	0	-mv7M4 --code_state=16 -rtti --exceptions -g
Plum Hall C++14	2877	0	-mv7M4 --code_state=16 -rtti --exceptions -o1
Plum Hall C++14	2877	0	-mv7M4 --code_state=16 -rtti --exceptions -o3 --embedded_constants=off

Plum Hall C++14	2877	0	-mv7M4 --code_state=16 -rtti --exceptions -o3 -mf5 -me
Plum Hall C++14	2841	0	-mv7M4 --code_state=16 -rtti -o3 -mf2 --float_support=none -g
Plum Hall C++14	2877	0	-mv7M4 -o4 -rtti --exceptions -mf4 -me
Plum Hall C++14	2841	0	-mv7R4 --code_state=16 -rtti -o2 -g
Plum Hall C++14	2841	0	-mv7R4 --code_state=16 -rtti -o3 -mf0 -oi20 --symdebug:dwarf_version=4
Plum Hall C++14	2841	0	-mv7R4 -rtti -o0 -g -os
Plum Hall C++14	2841	0	-mv7R4 -rtti -o2 -g
Plum Hall C++14	2841	0	-mv7R4 -rtti -o2 -mf5
Plum Hall C++14	2841	0	-mv5e -o2
Plum Hall C++14	2841	0	-mv5e --float_support=vfpv2 -me -o2 --symdebug:dwarf_version=4
Plum Hall C++14	2841	0	-mv5e --code_state=16 -o2
Plum Hall C++14	2841	0	-mv6M0 -o2
Plum Hall C++14	2841	0	-mv7A8 -me --unaligned_access=off -o3 -mf -oi25
Plum Hall C++14	2841	0	-mv7A8 --neon -me --unaligned_access=off -o2
Plum Hall C++14	2841	0	-mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2
Plum Hall C++14	2841	0	-mv7M3 --code_state=16 -o2
Plum Hall C++14	2841	0	-mv7M4 -me --float_support=none -o2
Plum Hall C++14	2841	0	-mv7M4 -me -o2
Plum Hall C++14	2841	0	-mv7R4 --code_state=16 -o2
Plum Hall C++14	2841	0	-mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -mf5 -o2
Plum Hall C++14	2841	0	-mv4 -o2
Plum Hall C++14	2841	0	-mv6 --code_state=16 -o2 --symdebug:dwarf_version=4
Plum Hall C++14	2841	0	-mv7A8 -me --unaligned_access=off -g
Plum Hall C++14	2841	0	-mv7M4 --float_support=fpv4spd16 -o2 --symdebug:dwarf_version=4
Plum Hall C++14	2878	0	-mv4 --code_state=16 -rtti --exceptions --extern_c_can_throw -o1
Plum Hall C++14	2878	0	-mv4 --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv4 --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -mf1 --symdebug:dwarf_version=4
Plum Hall C++14	2878	0	-mv4 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv4 -rtti --exceptions --extern_c_can_throw -o3 -mf -oi20
Plum Hall C++14	2878	0	-mv4 -rtti --exceptions --extern_c_can_throw -o3 -mf0 -oi0

Plum Hall C++14	2878	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions --extern_c_can_throw -o1 -os
Plum Hall C++14	2878	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv5e --float_support=vfpv2 -me -rtti --exceptions --extern_c_can_throw -o3 -mf -oi20
Plum Hall C++14	2878	0	-mv5e --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 -g
Plum Hall C++14	2878	0	-mv5e --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv5e --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -mf2 -s
Plum Hall C++14	2878	0	-mv5e -o4 -mf4 -rtti --exceptions --extern_c_can_throw --parallel=2
Plum Hall C++14	2878	0	-mv5e -rtti --exceptions --extern_c_can_throw -o2 -g
Plum Hall C++14	2878	0	-mv5e -rtti --exceptions --extern_c_can_throw -o2 -mf3 --symdebug:none
Plum Hall C++14	2878	0	-mv5e -rtti --exceptions --extern_c_can_throw -o3 -mf1 -me --symdebug:dwarf_version=4
Plum Hall ANSI/ISO C Stress Test	28139	0	-mv7A8 -me --code_state=16 -o3 -mf -oi20
simplelink_sdk	148	0	
simplelink_sdk	305	0	
simplelink_sdk	79	0	
simplelink_sdk	84	0	
simplelink_sdk	276	0	
simplelink_sdk	417	0	
simplelink_sdk	101	0	
simplelink_sdk	111	0	
simplelink_sdk	27	0	
STL Port	197	0	-mv7A8 --neon -me --unaligned_access=off -o2
STL Port	197	0	-mv7M3 --code_state=16 -o2
STL Port	197	0	-mv7m4 --float_support=fpv4spd16 -o2 --symdebug:dwarf_version=4
STL Port	197	0	-mv4 --code_state=16 -o1 -mf3 -s
STL Port	197	0	-mv4 --code_state=16 -o2 -g --symdebug:dwarf_version=4
STL Port	197	0	-mv4 --code_state=16 -o3 -mf -oi20 -g
STL Port	197	0	-mv4 -o2 -g
STL Port	197	0	-mv4 -o3 -oi0 -mf1 -g
STL Port	197	0	-mv5e --float_support=vfpv2 -me -o0
STL Port	197	0	-mv5e --float_support=vfpv2 -me -o2 -g
STL Port	197	0	-mv5e --float_support=vfpv2 -me -o3 -mf
STL Port	197	0	-mv5e --code_state=16 -o2 -g
STL Port	197	0	-mv5e --code_state=16 -o3 -me -ms -os
STL Port	197	0	-mv5e --code_state=16 -o3 -mf3

STL Port	197	0	-mv5e -o2 -g
STL Port	197	0	-mv5e -o3
STL Port	197	0	-mv5e --symdebug:none -s
STL Port	182	0	-mv6 --code_state=16 -o0 -ms -ss
STL Port	197	0	-mv6 -o2 -g --symdebug:dwarf_version=4
STL Port	197	0	-mv6 -o3 -mf1 -g
STL Port	197	0	-mv6M0 --code_state=16 -o2 -mf3 -os
STL Port	197	0	-mv7A8 --unaligned_access=off -me --neon -o2 -o3 -mf -oi20
STL Port	197	0	-mv7A8 --unaligned_access=off -me -o3 -oi0 -mf0
STL Port	197	0	-mv7M3 --code_state=16 -o2 -mf3 -os
STL Port	197	0	-mv7M3 --code_state=16 -o3 -oi0
STL Port	197	0	-mv7M3 -o4 -mf2 --parallel=2
STL Port	197	0	-mv7M4 --code_state=16 -g --symdebug:dwarf_version=4
STL Port	197	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
STL Port	197	0	-mv7M4 --code_state=16 -o1
STL Port	197	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off
STL Port	197	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none -g
STL Port	197	0	-mv7M4 --code_state=16 -o3 -mf5 -me
STL Port	197	0	-mv7M4 -o4 -mf4 -me
STL Port	197	0	-mv7R4 --code_state=16 -o2 -g
STL Port	197	0	-mv7R4 --code_state=16 -o2 -mf2
STL Port	197	0	-mv7R4 --code_state=16 -o3
STL Port	197	0	-mv7R4 -o1 --symdebug:dwarf_version=4
STL Port	197	0	-mv7R4 -o2 -g
STL Port	197	0	-mv7R4 -o3
TUEV	123	0	-mv7M3 --code_state=16 --plain_char=signed
TUEV	123	0	-mv7R4 --plain_char=signed --symdebug:dwarf_version=4
TUEV	123	0	-mv7R4 --code_state=16 --plain_char=signed
TUEV	123	0	-mv4 --plain_char=signed
TUEV	123	0	-mv4 --code_state=16 --plain_char=signed --symdebug:dwarf_version=4
TUEV	123	0	-mv7A8 --neon -me --unaligned_access=off --plain_char=signed
TUEV	123	0	-mv7A8 --code_state=16 --neon --float_support=vfpv3 -me --unaligned_access=off --plain_char=signed
TUEV	123	0	-mv7m4 --float_support=fpv4spd16 --plain_char=signed
TUEV	123	0	-mv4 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv4 -DBIG_ENDIAN -o2 -g -ss -al

TUEV	123	0	-mv4 -DBIG_ENDIAN -o3 -mf -g -ss -al
TUEV	123	0	-mv4 --code_state=16 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv4 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al --symdebug:dwarf_version=4
TUEV	123	0	-mv5e -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv5e -DBIG_ENDIAN -o2 -g -ss -al
TUEV	123	0	-mv5e -DBIG_ENDIAN -o3 -mf -g -ss -al
TUEV	123	0	-mv5e --float_support=vfpv2 -me -o2 -g
TUEV	123	0	-mv5e --float_support=vfpv2 -me -o2 -g -ss -al
TUEV	123	0	-mv5e --float_support=vfpv2 -me -o3 -mf -g -ss -al --symdebug:dwarf_version=4
TUEV	123	0	-mv5e --code_state=16 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv5e --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al
TUEV	123	0	-mv6 -DBIG_ENDIAN -o2 -g -ss -al
TUEV	123	0	-mv6 -DBIG_ENDIAN -o3 -mf -g -ss -al
TUEV	123	0	-mv6 --code_state=16 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv6M0 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 --code_state=16 -o2 -g
TUEV	123	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g
TUEV	123	0	-mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -mf -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 --code_state=16 -o2 -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o3 -mf -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon --code_state=16 -o2 -g --symdebug:dwarf_version=4
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon --code_state=16 -o2 -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me --neon -o3 -mf -g -ss -al
TUEV	123	0	-mv7A8 --unaligned_access=off -me -o2 -g
TUEV	123	0	-mv7A8 --unaligned_access=off -me -o2 -g -ss -al
TUEV	123	0	-mv7M3 --code_state=16 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv7M3 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al

TUEV	123	0	-mv7M4 --code_state=16 -g -me --embedded_constants=off
TUEV	123	0	-mv7M4 --code_state=16 -g -DBIG_ENDIAN
TUEV	123	0	-mv7M4 --code_state=16 -o1 -DBIG_ENDIAN
TUEV	123	0	-mv7M4 --code_state=16 -o3 --embedded_constants=off -DBIG_ENDIAN
TUEV	123	0	-mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g -DBIG_ENDIAN
TUEV	123	0	-mv7M4 --code_state=16 -o3 -mf5 -me --symdebug:dwarf_version=4
TUEV	123	0	-mv7R4 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv7R4 -DBIG_ENDIAN -o2 -g -ss -al
TUEV	123	0	-mv7R4 -DBIG_ENDIAN -o3 -mf -g -ss -al
TUEV	123	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g
TUEV	123	0	-mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0
TUEV	123	0	-mv7R4 --code_state=16 -DBIG_ENDIAN -o2 -g
TUEV	123	0	-mv7R4 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al