Compiler version:	5.1.0.LTS		
Compiler target:	Arm		
Date executed:	3/9/2026		
Execution machines:	Linux, Mac, Windows, Arm Linux		
Tester:	Todd Snider		

NOTE: for future patch releases (eg: ARM-CLANG-QKIT 5.1.1.LTS), download the file, ti_cgt_arm_llvm_5.1.1.LTS_validation_results.pdf, from the same tool folder where the QKIT installer was obtained.

Test	Passed	Failed	Compiler options (use cases)
ACE C++14 SuperTest Vermeer Update 3	17753	0	-mcpu=cortex-m33+nodsp -mfloat-abi=soft -O2 -flto -fexceptions -frtti
ACE C++14 SuperTest Vermeer Update 3	17192	0	-mcpu=cortex-m0plus -mmathacl -fsized-deallocation -O1 -flto
ACE C++14 SuperTest Vermeer Update 3	17212	0	-mcpu=cortex-m3 -fsized-deallocation
ACE C++14 SuperTest Vermeer Update 3	17218	0	-mcpu=cortex-m4 -mfpu=fpv4-sp-d16 -mthumb
ACE C++14 SuperTest Vermeer Update 3	17212	0	-mcpu=cortex-m0 -fsized-deallocation
ACE C++14 SuperTest Vermeer Update 3	17210	0	-mcpu=cortex-m33+nodsp -mfloat-abi=soft
ACE C++14 SuperTest Vermeer Update 3	17247	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard -O3
ACE C++14 SuperTest Vermeer Update 3	17212	0	-mcpu=cortex-m0plus -mmathacl -fsized-deallocation
ACE C++14 SuperTest Vermeer Update 3	17784	0	-mcpu=cortex-m4 -mfpu=fpv4-sp-d16 -fexceptions -frtti -mthumb
ACE C++14 SuperTest Vermeer Update 3	17210	0	-mcpu=cortex-m33 -mfloat-abi=soft
ACE C++14 SuperTest Vermeer Update 3	17210	0	-mcpu=cortex-m0 -mexecute-only
ACE C++14 SuperTest Vermeer Update 3	17753	0	-mcpu=cortex-m33 -mfloat-abi=soft -O2 -flto -fexceptions -frtti
ACE C++14 SuperTest Vermeer Update 3	17813	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard -O3 -fexceptions -frtti
ACE C++14 SuperTest Vermeer Update 3	17218	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16
ACE C++17 SuperTest Vermeer Update 3	18957	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard -O3 -fexceptions -frtti
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-m85 -O3

ACE C++17 SuperTest Vermeer Update 3	18329	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O2
ACE C++17 SuperTest Vermeer Update 3	18328	0	-mcpu=cortex-m0 -O0
ACE C++17 SuperTest Vermeer Update 3	18308	0	-mcpu=cortex-m55 -Oz
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-m55 -O3
ACE C++17 SuperTest Vermeer Update 3	18310	0	-mcpu=cortex-m85 -O1
ACE C++17 SuperTest Vermeer Update 3	18308	0	-mcpu=cortex-r52 -Oz
ACE C++17 SuperTest Vermeer Update 3	18310	0	-mcpu=cortex-m55 -O1
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-r52 -O3
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-m85 -O3 -mfpu=none -mfloat-abi=soft
ACE C++17 SuperTest Vermeer Update 3	18310	0	-mcpu=cortex-r52plus -O1
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-r52 -O3 -mfpu=none -mfloat-abi=soft
ACE C++17 SuperTest Vermeer Update 3	18308	0	-mcpu=cortex-m85 -Oz
ACE C++17 SuperTest Vermeer Update 3	18309	0	-mcpu=cortex-m55 -O3 -mfpu=none -mfloat-abi=soft
ACE C11 SuperTest Vermeer Update 3	5565	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O3
ACE C11 SuperTest Vermeer Update 3	5540	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O1
ACE C11 SuperTest Vermeer Update 3	5555	0	-mcpu=cortex-m3
ACE C11 SuperTest Vermeer Update 3	5555	0	-mcpu=cortex-m0plus -mmathacl
ACE C11 SuperTest Vermeer Update 3	5587	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard
ACE C11 SuperTest Vermeer Update 3	5555	0	-mcpu=cortex-m0
ACE C11 SuperTest Vermeer Update 3	5540	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -O1
ACE C11 SuperTest Vermeer Update 3	5555	0	-mcpu=cortex-r4 -mbig-endian
ACE C11 SuperTest Vermeer Update 3	5555	0	-mcpu=cortex-r4

ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m33+dsp -mfloat-abi=soft
ACE C90 SuperTest Vermeer Update 3	3410	0	-mcpu=cortex-m55 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m0plus -mmathacl
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -Oz -flto
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m85 -mfpu=fp-armv8-fullfp16-d16 -O1
ACE C90 SuperTest Vermeer Update 3	3419	0	-mcpu=cortex-r4 -O2
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m3 -mskip-fault-mitigation -mllvm -verify-machineinstrs
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m33+nodsp -mfloat-abi=soft
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-r52 -O3
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m3
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-r4 -fPIC
ACE C90 SuperTest Vermeer Update 3	3419	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard -Oz -flto
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m0 -mexecute-only
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-r52plus -O2
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m55 -mfpu=fp-armv8-fullfp16-d16 -O1
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -Oz -flto
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m0
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -Oz

ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-r4 -mbig-endian
ACE C90 SuperTest Vermeer Update 3	3408	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -Oz -flto
ACE C90 SuperTest Vermeer Update 3	3421	0	-mcpu=cortex-m33 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
ACE C99 SuperTest Vermeer Update 3	5276	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -O2
ACE C99 SuperTest Vermeer Update 3	5276	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O2
ACE C99 SuperTest Vermeer Update 3	5306	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16
ARM_LLVM Benchmarks	17	0	-mcpu=cortex-m4 -O3 -fno-common -fno-short-enums -mskip-fault-mitigation
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-r5 -O3 -fno-common -fno-inline-functions
ARM_LLVM Benchmarks	2	0	-mcpu=cortex-m33 -O3 -fno-common -fno-short-enums -ffp-exception-behavior=maytrap
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-r5 -O3 -fno-common -finline-functions
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-m4 -O3 -fno-common -fno-short-enums -ffp-exception-behavior=maytrap
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-r5 -O3 -fno-common -ffp-exception-behavior=strict
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-m4 -O3 -fno-common -fno-short-enums
ARM_LLVM Benchmarks	2	0	-mcpu=cortex-m33 -O3 -fno-common -fno-short-enums
ARM_LLVM Benchmarks	18	0	-mcpu=cortex-r5 -O3 -fno-common
LLVM Regression Testing	318697	0	
Perennial C++14	225377	0	-mcpu=cortex-r4 -O1 -mfpu=vfpv3-d16 -flto -fexceptions -frtti
Perennial C++14	181080	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O3
Perennial C++14	181080	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -O3
Perennial C++14	204539	0	-mcpu=cortex-m33 -frtti -mfloat-abi=soft
Perennial C++14	225410	0	-mcpu=cortex-m4 -mfpu=fpv4-sp-d16 -frtti -fexceptions -O3
Perennial C++14	181073	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -O1
Perennial C++14	204553	0	-mcpu=cortex-m4 -frtti -g -pedantic-errors
Perennial C++14	204529	0	-mcpu=cortex-m3 -frtti -O2 -g
Perennial C++14	225410	0	-mcpu=cortex-m4 -mfpu=fpv4-sp-d16 -frtti -fexceptions -O3 -ffp-exception-behavior=maytrap
Perennial C++14	181091	0	-mcpu=cortex-r4 -Os -g
Perennial C++14	181073	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -O1

Perennial C++14	225420	0	-mcpu=cortex-m0plus -mmathacl -frtti -fexceptions -O0
Perennial C++14	204539	0	-mcpu=cortex-m0 -frtti -O0
Perennial C++14	181091	0	-mcpu=cortex-r4 -O1 -mfpu=vfpv3-d16 -ffp-exception-behavior=strict
Perennial C++14	181074	0	-mcpu=cortex-m4 -mfpu=fpv4-sp-d16 -mfloat-abi=hard -flto -O2
Perennial C++14	181098	0	-mcpu=cortex-m4 -O2
Perennial C++14	204522	0	-mcpu=cortex-m4 -frtti -O1
Perennial C++14	181080	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O3
Perennial C++14	181080	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -mthumb -O2
Perennial C++14	204539	0	-mcpu=cortex-m0plus -mmathacl -frtti -O0
Perennial C++14	225410	0	-mcpu=cortex-r4 -O3 -mfpu=vfpv3-d16 -fexceptions -frtti
Perennial C++14	204539	0	-mcpu=cortex-m33+nodsp -frtti -mfloat-abi=soft
Perennial C++14	181098	0	-mcpu=cortex-r4 -O2 -g
Perennial C++14	204522	0	-mcpu=cortex-m4 -frtti -Os -mfloat-abi=soft
Perennial C++14	181108	0	-mcpu=cortex-r4 -mthumb
Perennial C++14	204529	0	-mcpu=cortex-m0 -frtti -Oz
Perennial C++14	181091	0	-mcpu=cortex-r5 -O1 -mfpu=vfpv3-d16
Perennial C++14	204529	0	-mcpu=cortex-m3 -frtti -O3
Perennial C++14	204529	0	-mcpu=cortex-m0plus -mmathacl -frtti -O2
Perennial C++14	204499	0	-mcpu=cortex-m0 -frtti -Oz -flto
Perennial C++14	181098	0	-mcpu=cortex-r4 -O3 -mfpu=vfpv3-d16
Perennial C++14	181080	0	-mcpu=cortex-r52 -O3
Perennial C++14	204515	0	-mcpu=cortex-m3 -frtti -flto
Perennial C++17	263832	0	-mcpu=cortex-m33 -O0 -frtti -mfloat-abi=soft
Perennial C++17	228031	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Perennial C++17	263717	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard -O3 -frtti
Perennial C++17	285945	0	-mcpu=cortex-m4 -frtti -fexceptions -O2
Perennial C++17	228146	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mthumb
Perennial C++17	263687	0	-mcpu=cortex-r4 -Oz -frtti -flto
Perennial C++17	228042	0	-mcpu=cortex-m4 -O1
Perennial C++17	228031	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Perennial C11	36446	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -O1
Perennial C11	38560	0	-mcpu=cortex-r4
Perennial C11	36434	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -mthumb -O2
Perennial C11	38541	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O2
Perennial C11	36469	0	-mcpu=cortex-m0
Perennial C11	38567	0	-mcpu=cortex-r5 -mfpu=vfpv3-d16 -mfloat-abi=hard -mbig-endian -O1
Perennial C11	38543	0	-mcpu=cortex-m3 -O1

Perennial C11	38571	0	-mcpu=cortex-r5 -mfpu=vfpv3-d16 -ffp-exception-behavior=maytrap -O3
Perennial C11	36450	0	-mcpu=cortex-r52 -O3
Perennial C11	38560	0	-mcpu=cortex-r4 -fPIC
Perennial C11	36450	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O3
Perennial C11	36450	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -O3
Perennial C11	38571	0	-mcpu=cortex-r5 -mfpu=vfpv3-d16 -mfloat-abi=hard -O3
Perennial C11	36456	0	-mcpu=cortex-m55 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
Perennial C11	36469	0	-mcpu=cortex-m0plus -mmathacl
Perennial C11	38555	0	-mcpu=cortex-r5 -mfpu=vfpv3-d16 -mfloat-abi=hard -mbig-endian -mthumb -O2
Perennial C11	36446	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mbig-endian -O1
Perennial C11	36446	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O3
Perennial C90	9229	0	-mcpu=cortex-m33 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
Perennial C90	9229	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard
Perennial C90	9209	0	-mcpu=cortex-r52 -O3
Perennial C90	9229	0	-mcpu=cortex-m3 -mskip-fault-mitigation -mllvm -verify-machineinstrs
Perennial C99	34413	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Perennial C99	34422	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -mthumb
Perennial C99	34453	0	-mcpu=cortex-r5 -mfpu=vfpv3-d16 -mfloat-abi=hard -mthumb
Perennial C99	34417	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Perennial C99	34426	0	-mcpu=cortex-r4 -mbig-endian -Oz
Performance Reporting	1	0	
Performance Reporting	1	0	
Performance Reporting	1	0	
Performance Reporting	1	0	
PHIL Regression Testing	447	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O1
PHIL Regression Testing	447	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -g -mexecute-only
PHIL Regression Testing	421	0	-mcpu=cortex-r52 -O3
PHIL Regression Testing	423	0	-mcpu=cortex-m55 -mfpu=fp-armv8-fullfp16-d16 -O1

PHIL Regression Testing	424	0	-mcpu=cortex-r4 -mfloat-abi=hard -mfpu=vfpv3-d16 -O2 -flto
PHIL Regression Testing	453	0	-mcpu=cortex-r4 -mbig-endian -Os
PHIL Regression Testing	445	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O3 -ffast-math
PHIL Regression Testing	434	0	-mcpu=cortex-m33+nodsp -mfloat-abi=soft
PHIL Regression Testing	447	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16
PHIL Regression Testing	424	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft
PHIL Regression Testing	454	0	-mcpu=cortex-r4 -mbig-endian -O2 -g
PHIL Regression Testing	446	0	-mcpu=cortex-r4 -mthumb -mbig-endian -O3 -finline-functions -gdwarf-3
PHIL Regression Testing	446	0	-mcpu=cortex-r4 -mthumb -mfloat-abi=hard -mfpu=vfpv3-d16 -mno-unaligned-access -Oz -finline-functions
PHIL Regression Testing	425	0	-mcpu=cortex-r4 -mfpu=none -mfloat-abi=soft -O2 -flto
PHIL Regression Testing	445	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -O3 -ffast-math
PHIL Regression Testing	434	0	-mcpu=cortex-m33 -mfloat-abi=soft
PHIL Regression Testing	439	0	-mcpu=cortex-m0plus -mmathacl -Oz
PHIL Regression Testing	424	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft
PHIL Regression Testing	441	0	-mcpu=cortex-r4 -mfloat-abi=hard -mfpu=vfpv3-d16 -O2
PHIL Regression Testing	409	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O3 -flto
PHIL Regression Testing	10113	0	
PHIL Regression Testing	424	0	-mcpu=cortex-r4 -mfloat-abi=hard -mfpu=vfpv3-d16 -O2 -flto -ffp-exception-behavior=maytrap
PHIL Regression Testing	422	0	-mcpu=cortex-r52
PHIL Regression Testing	423	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O3
PHIL Regression Testing	12429	0	
PHIL Regression Testing	449	0	-mcpu=cortex-m3

PHIL Regression Testing	411	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -Oz -flto
PHIL Regression Testing	423	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -O3
PHIL Regression Testing	443	0	-mcpu=cortex-r4
PHIL Regression Testing	453	0	-mcpu=cortex-r4 -mbig-endian -O1
PHIL Regression Testing	451	0	-mcpu=cortex-r4 -mbig-endian -O3 -ffast-math
PHIL Regression Testing	439	0	-mcpu=cortex-r4 -mfloat-abi=hard -mfpu=vfpv3-d16 -O2 -fpack-struct -mno-unaligned-access
PHIL Regression Testing	447	0	-mcpu=cortex-r4 -mthumb -mbig-endian -Og
PHIL Regression Testing	438	0	-mcpu=cortex-m0plus -mmathacl -O3
PHIL Regression Testing	422	0	-mcpu=cortex-r52plus -O2
PHIL Regression Testing	411	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O1 -flto
PHIL Regression Testing	439	0	-mcpu=cortex-r4 -mfloat-abi=hard -mfpu=vfpv3-d16 -O1 -fpack-struct -mno-unaligned-access
PHIL Regression Testing	445	0	-mcpu=cortex-m0 -mexecute-only
PHIL Regression Testing	445	0	-mcpu=cortex-m0
PHIL Regression Testing	423	0	-mcpu=cortex-m85 -mfpu=fp-armv8-fullfp16-d16 -O1
PHIL Regression Testing	446	0	-mcpu=cortex-r4 -mthumb -mfloat-abi=hard -mfpu=vfpv3-d16 -mno-unaligned-access -Og
PHIL Regression Testing	445	0	-mcpu=cortex-r4 -mthumb -mbig-endian -O3 -ffast-math -g -gdwarf-3
PHIL Regression Testing	423	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O2
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-r4
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-r4 -mbig-endian
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -Oz -flto
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m55 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -O1 -flto

Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-r4 -fPIC
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m3
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m3 -mskip-fault-mitigation -mllvm -verify-machineinstrs
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m33 -mfloat-abi=soft -mskip-fault-mitigation -mllvm -verify-machineinstrs
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-r52plus -Oz
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O3
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m0
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O2 -flto
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m4 -mfloat-abi=hard -mfpu=fpv4-sp-d16 -Os -flto
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m33+nodsp -mfloat-abi=soft
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m0plus -mmathacl
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-r4 -mfpu=vfpv3-d16 -mfloat-abi=hard
Plum Hall ANSI/ISO C Conformance	28869	0	-mcpu=cortex-m33 -mfloat-abi=soft
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-r52 -O1
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O3
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m85 -mfloat-abi=soft -mfpu=none -Oz
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -O3
Plum Hall ANSI/ISO C Conformance	28866	0	-mcpu=cortex-m55 -mfloat-abi=soft -mfpu=none -Oz
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -frtti -O2 -g
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -O4 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m3 -O2
Plum Hall C++14	2828	0	-mcpu=cortex-m4 -mfloat-abi=soft -O3 -flto
Plum Hall C++14	2836	0	-mcpu=cortex-m0plus -mmathacl -O1 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-r52 -O2

Plum Hall C++14	2836	0	-mcpu=cortex-m0 -O1 -frtti
Plum Hall C++14	2837	0	-mcpu=cortex-m0 -frtti
Plum Hall C++14	2828	0	-mcpu=cortex-m3 -frtti -flto -O3
Plum Hall C++14	2836	0	-mcpu=cortex-m33+nodsp -frtti -O2
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -frtti -O1
Plum Hall C++14	2924	0	-mcpu=cortex-m0 -O1 -frtti -fexceptions -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m33 -frtti -O2
Plum Hall C++14	2836	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -O2
Plum Hall C++14	2837	0	-mcpu=cortex-m33 -g
Plum Hall C++14	2836	0	-mcpu=cortex-m0 -O3 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -frtti -Os -g -mthumb
Plum Hall C++14	2836	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O2
Plum Hall C++14	2836	0	-mcpu=cortex-r52 -mfpu=none -mfloat-abi=soft -Oz
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -O2 -mfloat-abi=hard -mfpu=vfpv3-d16
Plum Hall C++14	2836	0	-mcpu=cortex-r52plus -O1
Plum Hall C++14	2836	0	-mcpu=cortex-m3 -O3 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m55 -mfpu=none -mfloat-abi=soft -Oz
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -frtti -O2 -g -mthumb
Plum Hall C++14	2924	0	-mcpu=cortex-r4 -O2 -fexceptions -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -mfloat-abi=soft -O2
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -O2 -mfloat-abi=hard
Plum Hall C++14	2837	0	-mcpu=cortex-m33+nodsp -g
Plum Hall C++14	2863	0	-mcpu=cortex-m4 -O3 -mfpu=fpv4-sp-d16 -flto -fexceptions
Plum Hall C++14	2836	0	-mcpu=cortex-m55 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O1
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -O3 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m0plus -mmathacl -O2
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -frtti -Oz -g -mthumb
Plum Hall C++14	2836	0	-mcpu=cortex-m3 -O2 -g -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m4 -O2
Plum Hall C++14	2836	0	-mcpu=cortex-r4 -O2 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m85 -mfloat-abi=hard -mfpu=fp-armv8-fullfp16-d16 -O1
Plum Hall C++14	2836	0	-mcpu=cortex-m85 -mfpu=none -mfloat-abi=soft -Oz
Plum Hall C++14	2836	0	-mcpu=cortex-m3 -O1 -frtti
Plum Hall C++14	2836	0	-mcpu=cortex-m0 -O2
SDK Code Size	23	0	
SDK Code Size	12	0	
SDK Code Size	10	0	
SDK Code Size	130	0	
SDK Code Size	130	0	

| SDK Code Size | 12 | 0 |