

NOTE: for future patch releases (eg: C2000/CLA 6.4.4), download the file, validation\_results.C2000\_CLA.6.4.4.pdf , from mysecuresoftware tool folder where QKIT installer was obtained.

Compiler version 6.4.9  
 Compiler target C2000 and CLA  
 Date executed November 20 2015  
 Execution machines Linux, Windows, and MacOSX  
 Tester Anna Youssefi

Test	Passed	Failed	Compiler options (use cases)
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o4
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o4 -ms
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o2 -ms
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o3 -mf5
ACE C89 SuperTest 2012	2517	0	-v28 -ml -pdw -o2 -pdse 118
ACE C89 SuperTest 2012	2517	0	-v28 -ml -o3 -mf
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o2 -mi
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o3 --symdebug:none
ACE C89 SuperTest 2012	2517	0	-v28 -ml --float_support=fpu32 -pdw -o2 -pdse 118
ACE C89 SuperTest 2012	2517	0	-v28 -ml -o2 -g
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o2
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o3 -mf1
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o3 -mf3
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o2 -mi
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o3 -pdw -pdse 118
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -o3 -ms
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -o3 -mo
ACE C89 SuperTest 2012	2517	0	-v28 -ml -o3 -mf3
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -pdw -o3 -pdse 118
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o0
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -o1
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o3 -mf5
ACE C89 SuperTest 2012	2517	0	-v28 --float_support=fpu32 -ml -mt -o3 -mf3
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o1
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o2 --rpt_threshold=80
ACE C89 SuperTest 2012	2517	0	-v28 -ml -o1
ACE C89 SuperTest 2012	2517	0	-v28 -ml -mt -o2 -ms
Applications	1	0	-v28 -ml -DRESTRICT=
Applications	10	0	-v28 --float_support=fpu32 -ml -mt -o3 -mi -DRESTRICT=
Applications	2	0	-v28 -ml -mt -o2 -ms
Applications	13	0	-v28 -ml -mt -o3 -pm
Applications	2	0	-v28 -ml -o3
Applications	10	0	-v28 -ml -o4 -ms -DRESTRICT=
Applications	10	0	-v28 -ml -mt -o2 -DRESTRICT=

		-v28 --float_support=fpu32 -ml -mt -o3 --rpt_threshold=20 -
Applications	10	0 DRESTRICT=
Applications	10	0 -v28 --float_support=fpu32 -ml -mt -o4 -ms -DRESTRICT=
Applications	10	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms -DRESTRICT=
Applications	3	0 -v28 -ml -DRESTRICT=
Applications	13	0 -v28 --float_support=fpu32 -ml -o3 -pm -mf
Applications	10	0 -v28 --float_support=fpu32 -ml -g -DRESTRICT=
Applications	2	0 -v28 -ml -mt -o3
Applications	10	0 -v28 -ml -o3 -mf -DRESTRICT=
Applications	2	0 -v28 -ml -o4
Applications	2	0 -v28 -ml -mt -o2
Applications	10	0 -v28 -ml -o3 -mi -DRESTRICT=
Applications	2	0 -v28 -ml -mt -o3 -mf
Applications	10	0 -v28 --float_support=fpu32 -ml -mt -o1 -DRESTRICT=
Applications	2	0 -v28 -ml -mt -o2 -mi
Applications	13	0 -v28 -ml -mt -o3 -pm --rpt_threshold=80
Applications	2	0 -v28 -ml -o1
Applications	10	0 -v28 -ml -o2 -ms -DRESTRICT=
Applications	18	0 -v28 -ml -ms -o3 -k -DRESTRICT=
Applications	10	0 -v28 --float_support=fpu32 -ml -o3 -mf -DRESTRICT=
Applications	10	0 -v28 -ml -mt -o1 -DRESTRICT=
Applications	13	0 -v28 --float_support=fpu32 -ml -o3 -pm -mf3
Applications	10	0 -v28 -ml -o2 -ms -DRESTRICT=
Applications	10	0 -v28 -o2 -ml -mt -DRESTRICT=
Applications	10	0 -v28 -ml -mt -o4 -mf2 -DRESTRICT=
Applications	10	0 -v28 -ml -mt -o0 -DRESTRICT=
Applications	1	0 -v28 -ml -pm -o3 -DRESTRICT=
Applications	2	0 -v28 -ml -mt --symdebug:none
Applications	2	0 -v28 -ml -mt -o4 -ms
Applications	10	0 -v28 -ml -mt -o2 -ms -DRESTRICT=
Applications	18	0 -v28 -ml -mt --float_support=fpu32 -ms -o3 -k -DRESTRICT=
Applications	10	0 -v28 -ml -mt -DRESTRICT=
Applications	10	0 -v28 -ml -mt -o3 --rpt_threshold=80 -DRESTRICT=
Applications	2	0 -v28 -ml -o2
Applications	3	0 -v28 -ml -mt -pm -o3 -v28 -DRESTRICT=
Applications	18	0 -v28 -ml -ms -o3 -k -DRESTRICT=
Applications	2	0 -v28 -ml -o3 -mf
Applications	10	0 -v28 -ml -o2 -DRESTRICT=
Applications	10	0 -v28 -ml -o3 -mf -DRESTRICT=
Applications	10	0 -v28 -ml -o1 -DRESTRICT=
Applications	10	0 -v28 -ml -mt -o2 -mi -DRESTRICT=
Applications	10	0 -v28 -ml -DRESTRICT=
Applications	3	0 -v28 -ml -pm -o3 -DRESTRICT=
Applications	10	0 -v28 --float_support=fpu32 -ml -mt -o2 -DRESTRICT=
Applications	10	0 -v28 -ml -DRESTRICT=
Applications	10	0 -v28 -ml -o3 -ms -DRESTRICT=

Applications	10	0 -v28 -ml -o3 -DRESTRICT=
Applications	3	0 -v28 -ml -mt -DRESTRICT=
Applications	13	0 -v28 -ml -mt -o3 -pm -mf
Applications	12	0 -v28 --float_support=fpu32 -ml -o3 -pm
Applications	2	0 -v28 -ml -mt -o3 -mf3 -pm
Applications	2	0 -v28 -ml -mt -o1 --symdebug:coff
Applications	10	0 -v28 -ml -pm -o3 -DRESTRICT=
Applications	10	0 -v28 -ml -o4 -mf -DRESTRICT=
Applications	13	0 -v28 --float_support=fpu32 -ml -o3 -pm -ms
Applications	18	0 -v28 -ml -mt -ms -o3 -k -DRESTRICT=
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -o2 -mt -mf5
Bugslayer Regression Testing	47	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1
Bugslayer Regression Testing	910	0 -v28 -ml -mt -o1
Bugslayer Regression Testing	560	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0
Bugslayer Regression Testing	910	0 -v28 --tmu_support=tmu0 -mt -o3 --fp_mode=relaxed
Bugslayer Regression Testing	910	0 -v28 -ml
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -ml -mt
Bugslayer Regression Testing	121	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1
Bugslayer Regression Testing	121	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0
Bugslayer Regression Testing	1188	0
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -o0 -mt -g
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -ml -mt -mf3 -o2
Bugslayer Regression Testing	121	0 -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2
Bugslayer Regression Testing	560	0 -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -ml -mt -o3
Bugslayer Regression Testing	47	0 -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2
Bugslayer Regression Testing	910	0 -v28 -ml -mt -o0
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -o3 -mt --rpt_threshold=20
Bugslayer Regression Testing	910	0 -v28 -ml -o2
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -o3 -mt -mf -mi
Bugslayer Regression Testing	910	0 -v28 -ml -mt -o2 -mf3
Bugslayer Regression Testing	47	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0
Bugslayer Regression Testing	560	0 -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1
Bugslayer Regression Testing	910	0 -v28 -ml -mt -o3
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -ml -mt -o4
Bugslayer Regression Testing	910	0 -v28 -ml -o3 --symdebug:coff
Bugslayer Regression Testing	910	0 -v28 --float_support=fpu32 -ml -mt -o1
Dinkumware	1551	0 -v28 -ml -o1

Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o2 -g
Dinkumware	1551	0 -v28 -ml -mt -o2 --rpt_threshold=80
Dinkumware	1551	0 -v28 -ml -mt -o4 -ms
Dinkumware	1551	0 -v28 -ml -o3 --symdebug:none
Dinkumware	1551	0 -v28 -ml -mt -o3 -mf
Dinkumware	1551	0 -v28 -ml -mt -o3 -mf3
Dinkumware	1551	0 -v28 -ml -mt -o2 -g
Dinkumware	1551	0 -v28 -ml -mt -o2 -mi
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o0
Dinkumware	1551	0 -v28 -ml -mt -o2 -ms
Dinkumware	1551	0 -v28 -ml -o2 -mt --rpt_threshold=80
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o3 -mf3
Dinkumware	1551	0 -v28 -ml -mt --float_support=fpu32 -pdw -o2
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o3 -ms
Dinkumware	1551	0 -v28 -ml -mt -o3 --symdebug:none
Dinkumware	1551	0 -v28 -ml -mt -o3 -mf1
Dinkumware	1551	0 -v28 -ml -mt -o4 -mf2
Dinkumware	1551	0 -v28 -ml -mt -pdw -o2
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o2 -mi
Dinkumware	1551	0 -v28 -ml -o2 -g
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o2 -mf
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -o2 -ms
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o3 -g -mo=off
Dinkumware	1551	0 -v28 -ml -mt -o1
Dinkumware	1551	0 -v28 -ml -pdw -o2
Dinkumware	1551	0 -v28 -ml -o3 -mf
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -mt -o4 -mf2
Dinkumware	1551	0 -v28 --float_support=fpu32 -ml -o3 -mf
GCC Extensions	33	0 -v28 -ml -mt -o3 -mf3
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms
GCC Extensions	33	0 -v28 -ml
GCC Extensions	33	0 -v28 -ml -mt -pdw -o2 --gcc
GCC Extensions	33	0 -v28 -ml -o3
GCC Extensions	33	0 -v28 -ml -mt -o2
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -mt -o3 -mf3
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -o3 -mf
GCC Extensions	33	0 -v28 -ml -o1
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -o2 -mf0
GCC Extensions	33	0 -v28 -ml -o2
GCC Extensions	33	0 -v28 -ml -mt -o3
GCC Extensions	33	0 -v28 -ml -pdw -o2 --gcc
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -mt -o4 -ms
GCC Extensions	33	0 -v28 -ml -mt -o4
GCC Extensions	33	0 -v28 --float_support=fpu32 -ml -mt -pdw -o2 --gcc
GCC Extensions	33	0 -v28 -ml -mt -pdw -o2 --gcc
GCC Extensions	33	0 -v28 -ml -mt -o2 -ms
GCC Torture Test	947	0 -v28 -ml -mt -pdw -o2

GCC Torture Test	947	0 -v28 -ml -pdw -o2
MFTEST	16	0 -v28 -ml -mt -o3
MFTEST	18	0 -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -v28 -ml -mt --float_support=fpu32 -o3 -
MFTEST	18	0 DMATH_TYPE=IQ_MATH
MFTEST	9	0 -v28 --float_support=fpu32 -ml -mt -o3
MFTEST	9	0 -v28 --float_support=fpu32 -ml -mt -o3
MFTEST	14	0 -v28 -ml -mt -o3 -DRESTRICT=
MFTEST	16	0 -v28 --float_support=fpu32 -ml -mt -o3
MFTEST	14	0 -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -
MFTEST	14	0 o3 -DRESTRICT= -v28 -ml -mt --float_support=fpu32 -o3 -
MFTEST	26	0 DMATH_TYPE=FLOAT_MATH
Perennial C (C90)	9138	0 -v28 -ml -mt -o2 -mi
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o0 -g
Perennial C (C90)	9138	0 -v28 -ml -o2 -g
Perennial C (C90)	9138	0 -v28 -ml -g -o2 -pdw
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o3 -mi
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o2 -g
Perennial C (C90)	9138	0 -v28 -ml -mt -o3 --rpt_threshold=80
Perennial C (C90)	9138	0 -v28 -ml -mt -g
Perennial C (C90)	9138	0 -v28 -ml -mt -o1
Perennial C (C90)	9138	0 -v28 -ml -mt -o3 -mf -g -mn
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -o3 -mf -ps
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -ooff
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o2 --rpt_threshold=80
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -o3
Perennial C (C90)	9138	0 -v28 -ml -mt -o2 -g
Perennial C (C90)	9138	0 -v28 -ml -o3 -mf -ps
Perennial C (C90)	9138	0 -v28 -ml -mt -g -o2 -pdw
Perennial C (C90)	9138	0 -v28 -ml -mt -o0
Perennial C (C90)	9138	0 -v28 -ml -mt -o3 -ps
Perennial C (C90)	9138	0 -v28 -ml -g -o2 -pdw
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml --symdebug:none
Perennial C (C90)	9138	0 -v28 -ml -mt -o2 -ms
Perennial C (C90)	9138	0 -v28 -ml -o1
Perennial C (C90)	9138	0 -v28 --float_support=fpu32 -ml -mt -o1
Perennial C (C90)	9138	0 -v28 -ml -o2
Perennial C++03	64760	0 -v28 -ml -mt --exceptions -rtti -o2 -g
Perennial C++03	64543	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2
Perennial C++03	64555	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o3
Perennial C++03	64742	0 -v28 -ml --exceptions -rtti -o3 -mf
Perennial C++03	64742	0 -v28 -ml -mt --exceptions -rtti -o3 -mf3

Perennial C++03	64566	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -ooff
Perennial C++03	64760	0 -v28 -ml --exceptions -rtti -o2 -ms
Perennial C++03	64539	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o1
Perennial C++03	64760	0 -v28 -ml -mt --exceptions -rtti -o2 -v28 --float_support=fpu32 -ml --exceptions -rtti -ps -o3 -mf -
Perennial C++03	64539	0 pdb
Perennial C++03	64700	0 -v28 -ml -mt --exceptions -rtti
Perennial C++03	64674	0 -v28 -ml -mt --exceptions -rtti -o0 --symdebug:none
Perennial C++03	64543	0 -v28 --float_support=fpu32 -ml --exceptions -rtti -o2
Perennial C++03	64760	0 -v28 -ml -mt --exceptions -rtti -o2 -ms
Perennial C++03	64475	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o3 -
Perennial C++03	64531	0 mf3
Perennial C++03	64760	0 -v28 -ml --exceptions -rtti -o2
Perennial C++03	64760	0 -v28 -ml -mt --exceptions -rtti -o3
Perennial C++03	64742	0 -v28 -ml -mt --exceptions -rtti -o3 -mf
Perennial C++03	64760	0 -v28 -ml -mt --exceptions -rtti -o1
Perennial C++03	64594	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2 -ms
Perennial C++03	64742	0 -v28 -ml --exceptions -rtti -o2 -mf
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -ooff
Perennial C99	14684	0 -v28 -ml -mt -o0
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml --symdebug:none
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o3 -mi
Perennial C99	14684	0 -v28 -ml -mt -o2 -ms
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o2 -g
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms
Perennial C99	14684	0 -v28 -ml -mt -o3 --rpt_threshold=80
Perennial C99	14684	0 -v28 -ml -mt -o2 -g
Perennial C99	14684	0 -v28 -ml -mt -g
Perennial C99	14684	0 -v28 -ml -mt -o1
Perennial C99	14684	0 -v28 -ml -g -o2 -pdw
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o0 -g
Perennial C99	14684	0 -v28 -ml -o2 -g
Perennial C99	14684	0 -v28 -ml -mt -o3 -mf -g -mn
Perennial C99	14684	0 -v28 -ml -o2
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o1
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -o3
Perennial C99	14684	0 -v28 -ml -mt -o2 -mi
Perennial C99	14684	0 -v28 -ml -mt -g -o2 -pdw
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o2 --rpt_threshold=80
Perennial C99	14684	0 -v28 -ml -o1
Perennial C99	14684	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms
Plum Hall ANSI/ISO C Conformance	29438	0 -v28 --float_support=fpu32 -ml -mt -o3 --fp_reassoc=off
Plum Hall ANSI/ISO C Conformance	29438	0 -v28 --float_support=fpu32 -ml -mt

Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o0 --symdebug:none
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o2 -mf5
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -o3 -ml -mf -mt -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pdw -
Plum Hall ANSI/ISO C Conformer	29438	0 o3
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o2 -mf1
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o3 --rpt_threshold=20
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o2 -mf0
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o3 -mf3
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o4 -mf -mt
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt --float_support=fpu32 -pdw -o2
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o3 -mf
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -o3
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o2 -mi
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o3 -ms
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o3 --rpt_threshold=20
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o4
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o3 -g
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -o2
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o2 -mi
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o1 --symdebug:coff
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o0
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o3 -mf -pm
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -mt -o3 -pm
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o2 -ms -g
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o2 -ms
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o1 -mt
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o3 -mf3 -pdb
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o2 -mi
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o1 -g
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o2
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -pdw -o3
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 --float_support=fpu32 -ml -o2 -mf
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt -o4
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o0 --symdebug:coff
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o2 -g
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -pdw -o2
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -mt
Plum Hall ANSI/ISO C Conformer	29438	0 -v28 -ml -o2 -ms
Plum Hall ANSI/ISO C Stress Test	29230	0 -v28 -ml -mt -o3 -mf3 -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pdw -
Plum Hall ANSI/ISO C Stress Test	29230	0 o2
Plum Hall ANSI/ISO C Stress Test	29230	0 -v28 --float_support=fpu32 -ml -mt -o3 -mf3
STL Port	217	0 -v28 -ml -mt -o2 --rpt_threshold=80
STL Port	217	0 -v28 -ml -pdw -o2

STL Port	217	0 -v28 -ml -pdw -o3 -mf3
STL Port	217	0 -v28 -ml -mt -o4 -ms
STL Port	217	0 -v28 -ml -o2
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -ooff
STL Port	217	0 -v28 -ml -mt -pdw -o2
STL Port	217	0 -v28 -ml -mt -o3 --symdebug:none
STL Port	217	0 -v28 -ml -mt -o2 -ms
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o3 -ms
STL Port	217	0 -v28 -ml -mt -o3 -mf
STL Port	217	0 -v28 -ml -mt -o2 -mi
STL Port	217	0 -v28 --float_support=fpu32 -ml -o2
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o2 -mf
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o3 -mi
STL Port	217	0 -v28 -ml -o3
STL Port	217	0 -v28 -ml -mt -o1
STL Port	217	0 -v28 -ml -mt -o2
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o4
STL Port	217	0 -v28 -ml -mt --float_support=fpu32 -pdw -o2
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o0
STL Port	217	0 -v28 -ml -mt -o4
STL Port	217	0 -v28 -ml -mt -o3 -mf3
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o2 -ms
STL Port	217	0 -v28 -ml -mt -o2 --rpt_threshold=80
STL Port	217	0 -v28 -ml -o1
STL Port	217	0 -v28 --float_support=fpu32 -ml -mt -o3 -mf
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o2
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o0 -g
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml --exceptions -rtti -o2
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o3 -mf2 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2 -
Suite++ Plum Hall C++ Conformal	5118	0 mf0
Suite++ Plum Hall C++ Conformal	5117	0 -v28 -ml --exceptions -rtti -o4 -mf
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o3 -mf -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o3 -
Suite++ Plum Hall C++ Conformal	5118	0 mf3
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o3 -g -me
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o1
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o2
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o2 -g
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o1
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o2 -ms
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o3
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o2 -ms
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o2 -mi -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2 -
Suite++ Plum Hall C++ Conformal	5118	0 mf5
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o3 -mi -mo



Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o1
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o3 -ms
Suite++ Plum Hall C++ Conformal	5117	0 -v28 -ml -mt --exceptions -rtti -o4 -mf
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o3
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -g
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -g
Suite++ Plum Hall C++ Conformal	5117	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o4
		-v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o3 --
Suite++ Plum Hall C++ Conformal	5118	0 rpt_threshold=40
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2 -ms
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o0 -pdb
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o3 -mf
Suite++ Plum Hall C++ Conformal	5117	0 -v28 -ml -mt --exceptions -rtti -o4 -ms
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml -mt --exceptions -rtti -o2
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml --exceptions -rtti -o3 -mf3
Suite++ Plum Hall C++ Conformal	5117	0 -v28 -ml --exceptions -rtti -o4
Suite++ Plum Hall C++ Conformal	5118	0 -v28 --float_support=fpu32 -ml --exceptions -rtti -o0
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -o2 -g --rpt_threshold=80
Suite++ Plum Hall C++ Conformal	5118	0 -v28 -ml -mt --exceptions -rtti -g