

NOTE: for obtaining validation results for future patch releases, request using below contact
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Compiler version 20.2.5.LTS
Compiler target C2000/CLA
Date executed 13-Apr-21
Execution machines Linux, Windows, and MacOSX
Tester Anna Youssefi

| Test | Passed | Failed | Compiler options (use cases) |
|---|--------|--------|---|
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv4 --code_state=16 -rtti --exceptions -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv6M0 --code_state=16 -rtti -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8611 | 0 | -mv7M4 -rtti --exceptions -o4 -mf4 -me |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv5e --code_state=16 -rtti --exceptions -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8194 | 0 | -vmbsp -o4 -mf5 --parallel=4 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8611 | 0 | -mv5e -rtti --exceptions -o4 --symdebug:dwarf_version=4 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv7M3 -rtti -o4 -mf5 -me --parallel=2 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv7M3 --code_state=16 -rtti -o3 -mf -oi20 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv6 --code_state=16 -rtti --exceptions -o3 -oi0 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv7R4 -rtti -o0 --symdebug:none -s |

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| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv7R4 --code_state=16 -rtti -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8255 | 0 | -mv7M4 -rtti --code_state=16 -o3 -mf2 -- float_support=none -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -o3 -- embedded_constants=off |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -o1 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv4 -rtti --exceptions -o2 -g |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv6 -rtti --exceptions -o3 -mf5 -oi20 |
| ACE C++11 and C++14 (for C++14) SuperTest Mondrian Update 3 | 8624 | 0 | -mv6 -rtti --exceptions -o2 -g |
| ACE C11 SuperTest Mondrian Update 3 | 241 | 0 | -vmssp -ml -o3 |
| ACE C11 SuperTest Mondrian Update 3 | 245 | 0 | -mv5e -me -o3 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp -ml -o2 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -o3 -ms |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp -ml -o1 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2655 | 0 | -vmssp -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv4 --code_state=16 -o3 -mf -oi20 -g -- symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 -ml -mt -o3 -mf5 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -mi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --float_support=vfpv2 -me -o3 -mf |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -pdse 118 -- abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp --data_model=restricted -o2 -mf0 |

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| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 --code_state=16 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --code_state=16 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 --code_state=16 -o2 -mf2 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --float_support=vfpv2 -me -o0 |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmSPX -o3 -mf0 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf0 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmSPX --data_model=restricted -o4 -mf0 --parallel=2 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv4 -o3 -oi0 -mf1 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -o3 -mo |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmSPX -o2 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -o3 -mo --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf2 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -o3 -mf -oi20 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 --parallel=2 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmSPX --data_model=restricted -o3 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmSPX -ml -o3 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6 -o3 -mf1 -g --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o0 -mf1 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 -ml -mt -o4 -ms --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmSPX -ml -o3 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --float_support=vfpv2 -me -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o2 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o3 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |

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| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2678 | 0 | -vmsex -ml -o2 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest | | | -mv5e --code_state=16 -o3 -mf3 -- |
| Mondrian Update 3 | 2682 | 0 | symdebug:dwarf_version=4 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv5e -o3 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2678 | 0 | -vmsex --data_model=restricted -g -o2 -pdse 118 |
| ACE C89 SuperTest | | | -v28 -ml --float_support=fpu64 -o2 -pdse 118 -- |
| Mondrian Update 3 | 2653 | 0 | symdebug:dwarf_version=4 --abi=eabi |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2677 | 0 | -vmsex --data_model=small -g -o2 -pdse 118 |
| ACE C89 SuperTest | | | -v28 --float_support=fpu64 -ml -o1 -- |
| Mondrian Update 3 | 2653 | 0 | symdebug:dwarf_version=4 --abi=eabi |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2677 | 0 | -vmsex -o1 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -o3 -ms --abi=eabi |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -o2 -pdse 118 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2678 | 0 | -vmsex -ml -o4 -mf0 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o4 -ms |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv7M4 -o4 -mf4 -me |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv7M3 --code_state=16 -o2 -mf3 -os |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o1 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o3 -mf3 |
| ACE C89 SuperTest | | | -mv7R4 --code_state=16 -o3 -- |
| Mondrian Update 3 | 2682 | 0 | symdebug:dwarf_version=4 |
| ACE C89 SuperTest | | | -v28 --float_support=fpu64 -ml -mt -o2 -ms -- |
| Mondrian Update 3 | 2653 | 0 | abi=eabi |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me -o2 -g |
| ACE C89 SuperTest | | | -vmsex --data_model=restricted -o2 -- |
| Mondrian Update 3 | 2678 | 0 | align_for_power |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -o3 -mf3 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2682 | 0 | -mv7M3 -o4 -mf5 --parallel=4 |
| ACE C89 SuperTest | | | -mv5e --code_state=16 -g -o2 -pdse 118 -- |
| Mondrian Update 3 | 2682 | 0 | symdebug:dwarf_version=4 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o2 --rpt_threshold=80 |
| ACE C89 SuperTest | | | |
| Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf3 |
| ACE C89 SuperTest | | | -mv7A8 --unaligned_access=off -me --neon -- |
| Mondrian Update 3 | 2682 | 0 | float_support=vfpv3 -o1 |

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| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -o1 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -o3 -mf |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o3 -mf -oi20 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmssp -o0 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv4 --code_state=16 -o1 -mf3 -s |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -ms |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -o1 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o2 -mf2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M3 --code_state=16 -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6M0 -o3 -mf0 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp --data_model=restricted -o3 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6M0 -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv4 --code_state=16 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml --float_support=fpu32 -o2 -pdse 118 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -o2 -g --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -o1 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv4 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp --data_model=restricted -o1 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmssp --data_model=restricted -o0 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -g -o2 -pdse 118 --symdebug:dwarf_version=4 |

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| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -o3 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmosp --data_model=restricted -o2 -mf5 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6 --code_state=16 -o2 -mf2 --symdebug:none |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmosp -o2 -mf0 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf3 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --symdebug:none -s |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o3 -mf4 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmosp -o3 |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmosp -ml -o2 -mf0 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmosp -ml -o0 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --float_support=fpv4spd16 -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M4 --code_state=16 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 -ooff |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv5e --code_state=16 -o3 -me -ms -os |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -mi --abi=eabi -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf5 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -o4 -mf --parallel=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --float_support=vfpv3 -me --unaligned_access=off -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7A8 --unaligned_access=off -me -o3 -oi0 -mf0 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2655 | 0 | -vmosp --code_model=small --data_model=small -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o3 -mf1 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 --float_support=fpu32 -ml -o1 --symdebug:dwarf_version=4 |

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| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M3 --code_state=16 -o3 -oi0 |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6M0 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv6 --code_state=16 -o0 -ms -ss |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmbsp --data_model=large -g -o2 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2653 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 --parallel=2 --abi=eabi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7M3 --code_state=16 -o2 -g |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o3 -pdse 118 |
| ACE C89 SuperTest Mondrian Update 3 | 2677 | 0 | -vmbsp -o2 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o2 -ms |
| ACE C89 SuperTest Mondrian Update 3 | 2678 | 0 | -vmbsp --data_model=restricted -o2 --symdebug:dwarf_version=4 |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o3 --symdebug:none |
| ACE C89 SuperTest Mondrian Update 3 | 2650 | 0 | -v28 -ml -mt -o2 -mi |
| ACE C89 SuperTest Mondrian Update 3 | 2682 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -g -o2 -pdse 118 |
| ACE C99 SuperTest Mondrian Update 3 | 873 | 0 | -v28 -ml -o2 |
| ACE C99 SuperTest Mondrian Update 3 | 874 | 0 | -v28 -ml -o2 --abi=eabi |
| ACE C99 SuperTest Mondrian Update 3 | 888 | 0 | -mv5e -me -o3 --symdebug:dwarf_version=4 |
| ACE C99 SuperTest Mondrian Update 3 | 877 | 0 | -vmbsp -ml -o3 |
| Applications | 11 | 0 | -vmbsp -ml -DRESTRICT= -o3 -mf=0 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=None --enum_type=int -g -mf=4 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=5 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=4 |
| Applications | 9 | 0 | -vmbsp -pm -op2 -o3 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=0 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=5 |

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| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=0 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=0 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o4 -mf=0 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=4 |
| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=5 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=0 |
| Applications | 10 | 0 | -v28 -ml -mt -DRESTRICT= |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o1 -mf=5 |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=1 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o1 -DRESTRICT= |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=2 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -g -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=3 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=0 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o3 -mf=5 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o3 -mf=0 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=3 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o1 -mf=1 |
| Applications | 9 | 0 | -vmspx --code_model=small --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=2 |
| Applications | 10 | 0 | -v28 -ml -mt -o1 -DRESTRICT= |
| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=3 |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=4 |

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| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 --symdebug:none -mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=5 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=5 |
| Applications | 2 | 0 | -v28 -ml -mt -o2 -mi |
| Applications | 39 | 0 | -mv5e -o3 -mf=4 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=4 |
| Applications | 11 | 0 | -vmSPX -DRESTRICT= -o1 -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=1 |
| Applications | 11 | 0 | -vmSPX --data_model=restricted -DRESTRICT= -o0 -mf=1 |
| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=4 |
| Applications | 10 | 0 | -v28 -ml -mt -o3 --rpt_threshold=80 -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5 |
| Applications | 11 | 0 | -vmSPX --code_model=small --data_model=small -DRESTRICT= -o4 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=5 |
| Applications | 11 | 0 | -vmSPX --code_model=small --data_model=small -DRESTRICT= -o4 -mf=5 |
| Applications | 9 | 0 | -vmSPX --data_model=large -pm -op2 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=5 |
| Applications | 11 | 0 | -vmSPX -ml -DRESTRICT= -o3 -mf=5 |
| Applications | 10 | 0 | -v28 -ml -o3 -ms -DRESTRICT= |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=1 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=3 |
| Applications | 11 | 0 | -vmSPX --code_model=small --data_model=small -DRESTRICT= -o3 -mf=1 |
| Applications | 11 | 0 | -vmSPX --code_model=small --data_model=small -DRESTRICT= -o2 -mf=1 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -DRESTRICT= --abi=eabi |

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| Applications | 13 | 0 | -mv7m4 --float_support=fpv4spd16 -o3 -k -DRESTRICT= |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o4 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=5 |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=2 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o1 -mf=1 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o1 -mf=0 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o3 -oi20 -mf=4 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=4 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=1 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 -ms -DRESTRICT= --parallel=2 |
| Applications | 9 | 0 | -vmspx --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=0 |
| Applications | 1 | 0 | -v28 -ml -pm -o3 -DRESTRICT= |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=5 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=4 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=1 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=1 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o2 -mf=3 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRICT= -o1 -mf=5 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=1 |
| Applications | 9 | 0 | -vmspx -pm -op2 -o3 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=1 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o1 -mf=3 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o1 -DRESTRICT= --abi=eabi -mf=2 |

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| Applications | 11 | 0 | -vmosp -DRESTRICT= -o3 -mf=5 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o2 -mf=4 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRICT= -o2 -mf=1 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=1 |
| Applications | 10 | 0 | -v28 -ml -o3 -DRESTRICT= |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=1 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o3 -mf=1 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRICT= -o1 -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=2 |
| Applications | 11 | 0 | -vmosp --data_model=restricted -DRESTRICT= -o2 -mf=3 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRICT= -o4 -mf=2 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=3 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -pm -o3 -DRESTRICT= --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=2 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= --opt_level=2 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o1 -mf=2 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRICT= -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o4 --parallel=2 -mf=5 |
| Applications | 11 | 0 | -vmosp --code_model=small --data_model=small -DRESTRICT= -o1 -mf=2 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o0 -mf=3 |
| Applications | 9 | 0 | -vmosp --code_model=small --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=1 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRICT= -o0 -mf=0 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=4 |

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| Applications | 25 | 0 | -mv5e -DRESTRICT= -o1 --plain_char=signed -mf=0 -v28 -ml -mt --div_support=div0 -- float_support=fpu32 -pm -o1 -DRESTRICT= --abi=eabi - |
| Applications | 14 | 0 | mf=5 -mv7R4 -DRESTRICT= -me --code_state=16 -- float_support=vfpv3d16 --unaligned_access=off -o3 - |
| Applications | 25 | 0 | oi0 -mf=3 -v28 --float_support=fpu64 -ml -o3 -pm -ms -- symdebug:dwarf_version=4 --abi=eabi |
| Applications | 13 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mi - DRESTRICT= |
| Applications | 10 | 0 | -mv7A8 --unaligned_access=off -- float_support=vfpv3 --neon --code_state=16 -me - DRESTRICT= -o2 -g -mf=1 |
| Applications | 25 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi - mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -g -DRESTRICT= |
| Applications | 10 | 0 | -mv7A8 --unaligned_access=off -- float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=5 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=1 -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 - |
| Applications | 25 | 0 | oi20 -pm -op2 -mf=3 |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o3 -mf=3 |
| Applications | 11 | 0 | -vmisp -ml -DRESTRICT= -o2 -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 -g -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=5 -v28 -ml -mt --float_support=fpu32 -ms -o3 -k - DRESTRICT= |
| Applications | 20 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 - oi20 -mf=0 |
| Applications | 25 | 0 | -vmisp -DRESTRICT= -o1 -mf=1 |
| Applications | 11 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= - -abi=eabi -mf=2 |
| Applications | 14 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=2 -mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm - op2 -mf=2 |
| Applications | 25 | 0 | -vmisp -ml -DRESTRICT= -o4 -mf=1 |
| Applications | 11 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g - mf=1 |
| Applications | 25 | 0 | -vmisp -pm -op2 -o3 --pack_all_structs -- diag_suppress=169 -DRESTRICT= -mf=3 |
| Applications | 9 | 0 | -v28 -ml -mt --tmu_support=tmu1 -- fp_mode=relaxed -pm -o3 -DRESTRICT= --abi=eabi - mf=5 |
| Applications | 14 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -oi20 -mf=4 |
| Applications | 25 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -DRESTRICT= |
| Applications | 10 | 0 | |

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| Applications | 25 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRICT= -me -mf=2 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=0 |
| Applications | 9 | 0 | -vmbsp --data_model=restricted -pm -op2 -o3 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRICT= -mf=1 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o1 -mf=4 |
| Applications | 11 | 0 | -vmbsp --data_model=restricted -DRESTRICT= -o3 -mf=3 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=0 |
| Applications | 3 | 0 | -v28 -ml -mt -DRESTRICT= |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=5 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o4 --parallel=2 -mf=1 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=4 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o1 -os -g -me -mf=0 |
| Applications | 2 | 0 | -v28 -ml -mt -o2 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 --symdebug:none -mf=0 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=0 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o4 --parallel=2 -mf=0 |
| Applications | 2 | 0 | -v28 -ml -o4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=2 |
| Applications | 11 | 0 | -vmbsp --data_model=restricted -DRESTRICT= -o2 -mf=0 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=3 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o3 -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=3 |
| Applications | 21 | 0 | -mv7M4 -me -o3 -mf=5 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -g -mf=3 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=4 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=5 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -pm -o3 -DRESTRICT= --abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRICT= -me -mf=3 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=4 |

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| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5 |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o2 -mf=3 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0 |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o4 --parallel=2 -mf=3 |
| Applications | 9 | 0 | -vmisp -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=2 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 -ms -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=3 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=4 |
| Applications | 9 | 0 | -vmisp --data_model=restricted -pm -op2 -o3 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=1 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -g -mf=4 |
| Applications | 9 | 0 | -vmisp --data_model=restricted -pm -op2 -o3 -DRESTRICT= -mf=1 |
| Applications | 10 | 0 | -v28 -ml -o3 -mi -DRESTRICT= |
| Applications | 2 | 0 | -v28 -ml -o2 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=5 |
| Applications | 13 | 0 | -v28 --float_support=fpu32 -ml -o3 -pm -ms --symdebug:dwarf_version=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=3 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=0 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o1 -mf=2 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=2 |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=1 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=0 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=1 |
| Applications | 11 | 0 | -vmisp --data_model=restricted -DRESTRICT= -o2 -mf=2 |
| Applications | 11 | 0 | -vmisp -ml -DRESTRICT= -o2 -mf=5 |
| Applications | 11 | 0 | -vmisp -ml -DRESTRICT= -o4 -mf=5 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o1 --plain_char=signed -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=1 |

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| Applications | 9 | 0 | -vmspx --data_model=restricted -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRICT= -mf=5 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o3 -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=2 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=2 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRICT= -mf=5 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=1 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o2 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=2 |
| Applications | 2 | 0 | -v28 -ml -mt --symdebug:none |
| Applications | 2 | 0 | -v28 -ml -mt -o2 --abi=eabi |
| Applications | 10 | 0 | -v28 -ml -pm -o3 -DRESTRICT= |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=0 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=0 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 -ms -DRESTRICT= --abi=eabi |
| Applications | 2 | 0 | -v28 -ml -mt -o2 -ms |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o4 -mf=2 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRICT= -me -mf=1 |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o4 -mf=0 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o3 -mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=4 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 --symdebug:none -mf=5 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o1 -mf=5 |
| Applications | 13 | 0 | -v28 -ml -mt -o3 -pm |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=4 |
| Applications | 11 | 0 | -vmisp -DRESTRICT= -o3 -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=3 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 -g -mf=4 |
| Applications | 10 | 0 | -v28 -ml -mt -o3 --rpt_threshold=80 -DRESTRICT= |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=1 |

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| Applications | 20 | 0 | -v28 -ml -mt -ms -o3 -k -DRESTRICT= --symdebug:dwarf_version=4 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=0 |
| Applications | 9 | 0 | -vmssp -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=1 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o2 -mf=4 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRICT= -mf=4 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o4 -mf=5 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 --symdebug:none -mf=3 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o1 -mf=1 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRICT= -me -mf=4 |
| Applications | 14 | 0 | -mv7M4 -o4 -me --symdebug:dwarf_version=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=2 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=3 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o0 -mf=5 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --rpt_threshold=20 -DRESTRICT= |
| Applications | 13 | 0 | -v28 --float_support=fpu32 -ml -o3 -pm -mf |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o2 -g -mf=4 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -ms -DRESTRICT= |
| Applications | 2 | 0 | -v28 -ml -o3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=0 |
| Applications | 11 | 0 | -vmssp --data_model=restricted -DRESTRICT= -o0 -mf=0 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=5 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o1 -mf=3 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=2 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o4 --parallel=2 -mf=2 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=3 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=2 |

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| Applications | 9 | 0 | -vmbsp --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=0 |
| Applications | 11 | 0 | -vmbsp --code_model=small --data_model=small -DRESTRICT= -o4 -mf=1 |
| Applications | 11 | 0 | -vmbsp --code_model=small --data_model=small -DRESTRICT= -o2 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -g -mf=4 |
| Applications | 11 | 0 | -vmbsp -ml -DRESTRICT= -o0 -mf=2 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -g -mf=5 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -pm -o3 -DRESTRICT= --abi=eabi -mf=2 |
| Applications | 9 | 0 | -vmbsp --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=1 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o1 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -me -mf=3 |
| Applications | 2 | 0 | -v28 -ml -o3 -mf |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -os -g -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=3 |
| Applications | 11 | 0 | -vmbsp --code_model=small --data_model=small -DRESTRICT= -o3 -mf=0 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=2 |
| Applications | 2 | 0 | -v28 -ml -mt -o3 |
| Applications | 13 | 0 | -v28 --float_support=fpu32 -ml -o3 -pm |
| Applications | 11 | 0 | -vmbsp --data_model=restricted -DRESTRICT= -o3 -mf=5 |

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| Applications | 9 | 0 | -vmsox --code_model=small --data_model=small -pm -op2 -o3 -DRESTRIC= -mf=1 |
| Applications | 11 | 0 | -vmsox -DRESTRIC= -o2 -mf=0 |
| Applications | 11 | 0 | -vmsox --data_model=restricted -DRESTRIC= -o3 -mf=4 |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=2 |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRIC= -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o1 -DRESTRIC= --abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o1 -mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRIC= -o3 -oi0 -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=3 |
| Applications | 11 | 0 | -vmsox --code_model=small --data_model=small -DRESTRIC= -o1 -mf=3 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=5 |
| Applications | 9 | 0 | -vmsox --data_model=large -pm -op2 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=0 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRIC= -mf=2 |
| Applications | 11 | 0 | -vmsox -ml -DRESTRIC= -o0 -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRIC= -o3 -oi0 -mf=4 |
| Applications | 9 | 0 | -vmsox --data_model=large -pm -op2 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=3 |
| Applications | 25 | 0 | -mv5e -DRESTRIC= -o1 --plain_char=signed -mf=2 |
| Applications | 11 | 0 | -vmsox --data_model=restricted -DRESTRIC= -o1 -mf=1 |
| Applications | 9 | 0 | -vmsox --code_model=small --data_model=small -pm -op2 -o3 -DRESTRIC= -mf=4 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRIC= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=5 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=0 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -ms -DRESTRIC= --abi=eabi |
| Applications | 11 | 0 | -vmsox -DRESTRIC= -o0 -mf=2 |
| Applications | 11 | 0 | -vmsox -DRESTRIC= -o3 -mf=3 |

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| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o2 -g -mf=5 |
| Applications | 9 | 0 | -vmisp -pm -op2 -o3 -DRESTRIC= -mf=0 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -g -DRESTRIC= --abi=eabi |
| Applications | 11 | 0 | -vmisp --data_model=restricted -DRESTRIC= -o4 -mf=5 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o3 --float_support=none --enum_type=int -g -mf=5 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o3 -oi0 -mf=1 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=5 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small -DRESTRIC= -o1 -mf=5 |
| Applications | 10 | 0 | -v28 -ml -o1 -DRESTRIC= |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRIC= -o3 -oi20 -mf=5 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=3 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o3 -me -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRIC= -o3 -g -mf=1 |
| Applications | 3 | 0 | -v28 -ml -pm -o3 -DRESTRIC= |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -g --symdebug:dwarf_version=4 -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRIC= -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -pm -o3 -DRESTRIC= --abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o3 -oi0 -mf=1 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRIC= -mf=3 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o3 -oi0 -mf=0 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o3 --embedded_constants=off -mf=1 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o2 -os -g -mf=2 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRIC= -mf=3 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRIC= --abi=eabi -mf=3 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRIC= --abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRIC= -o3 -oi20 -mf=4 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=1 |

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| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o1 -DRESTRICT= --abi=eabi -mf=0 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -g -mf=5 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o1 -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=4 |
| Applications | 11 | 0 | -vmssp --data_model=restricted -DRESTRICT= -o1 -mf=0 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o1 --plain_char=signed -mf=1 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o1 -mf=2 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= -mf=3 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o2 -mf=5 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o4 -mf=3 |
| Applications | 11 | 0 | -vmssp --data_model=restricted -DRESTRICT= -o0 -mf=4 |
| Applications | 13 | 0 | -mv5e -o3 -k -DRESTRICT= |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o3 -mf=2 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=4 |
| Applications | 11 | 0 | -vmssp --data_model=restricted -DRESTRICT= -o1 -mf=2 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=1 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --rpt_threshold=20 -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=1 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=3 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=5 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=5 |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=2 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -oi20 -mf=2 |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -os -g -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=5 |

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| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRIC= -o3 -oi0 -mf=5 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o2 -g -os -mf=3 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o3 -oi20 -pm -op2 -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o3 --embedded_constants=off -mf=3 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRIC= -mf=0 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o3 -oi20 -pm -op2 -mf=1 |
| Applications | 11 | 0 | -vmisp -DRESTRIC= -o4 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRIC= -mf=2 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRIC= --abi=eabi -mf=2 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRIC= -mf=2 |
| Applications | 25 | 0 | -mv4 -DRESTRIC= -o3 -oi20 -mf=5 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=1 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=1 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small -DRESTRIC= -o4 -mf=4 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRIC= -mf=2 |
| Applications | 2 | 0 | -v28 -ml -mt -o3 -mf3 -pm |
| Applications | 9 | 0 | -vmisp -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRIC= -mf=5 |
| Applications | 9 | 0 | -vmisp --data_model=large -pm -op2 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -o3 --float_support=none --enum_type=int -g -mf=0 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRIC= -mf=4 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRIC= -me -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=5 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRIC= -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRIC= --abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=5 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRIC= -mf=3 |
| Applications | 11 | 0 | -vmisp --data_model=restricted -DRESTRIC= -o4 -mf=3 |
| Applications | 11 | 0 | -vmisp --data_model=restricted -DRESTRIC= -o2 -mf=1 |
| Applications | 11 | 0 | -vmisp -ml -DRESTRIC= -o0 -mf=1 |
| Applications | 11 | 0 | -vmisp --data_model=restricted -DRESTRIC= -o0 -mf=3 |

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| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o3 -oi0 -mf=1 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=5 |
| Applications | 2 | 0 | -v28 -ml -mt -o3 -mf |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o4 --parallel=2 -mf=4 |
| Applications | 2 | 0 | -v28 -ml -mt -o1 |
| Applications | 9 | 0 | -vmspx --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o3 -oi20 -mf=1 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g --symdebug:dwarf_version=4 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRICT= -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=4 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=1 |
| Applications | 9 | 0 | -vmssp -pm -op2 -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o1 -os -g -me -mf=2 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=2 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -g --symdebug:dwarf_version=4 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=4 |
| Applications | 9 | 0 | -vmspx --data_model=large -pm -op2 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=2 |
| Applications | 9 | 0 | -vmspx --data_model=restricted -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=3 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o0 -mf=4 |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=0 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o3 -oi20 -mf=5 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o1 -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -mf=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=4 |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=0 |

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| Applications | 11 | 0 | -vmisp -DRESTRIC= -o4 -mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me - DRESTRIC= -o3 -pm -op2 -mf=3 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o1 -mf=4 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o3 - oi20 -mf=1 |
| Applications | 20 | 0 | -v28 -ml -mt --float_support=fpu32 -ms -o3 -k - DRESTRIC= --abi=eabi |
| Applications | 9 | 0 | -vmisp -pm -op2 -o3 -DRESTRIC= -mf=1 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRIC= -o3 -oi0 -g - mf=2 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu1 -- fp_mode=relaxed -pm -o3 -DRESTRIC= --abi=eabi - mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o2 -g -mf=5 |
| Applications | 10 | 0 | -v28 -ml -mt -o2 -ms -DRESTRIC= |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -- code_state=16 -me -DRESTRIC= -o2 -g -mf=5 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small - DRESTRIC= -o2 -mf=5 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small - DRESTRIC= -o1 -mf=4 |
| Applications | 9 | 0 | -vmisp --data_model=restricted -pm -op2 -o3 -- pack_all_structs --diag_suppress=169 -DRESTRIC= - mf=2 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRIC= - abi=eabi -mf=3 |
| Applications | 39 | 0 | -mv6M0 --code_state=16 -o3 -mf=4 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRIC= -mf=1 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRIC= -o3 -oi20 -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi - mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -- float_support=vfpv3 --neon --code_state=16 -me - DRESTRIC= -o3 -oi20 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small - DRESTRIC= -o0 -mf=5 |
| Applications | 39 | 0 | -mv5e -o3 -mf=3 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi - mf=3 |
| Applications | 11 | 0 | -vmisp --code_model=small --data_model=small - DRESTRIC= -o0 -mf=2 |
| Applications | 9 | 0 | -vmisp --data_model=restricted -pm -op2 -o3 -- pack_all_structs --diag_suppress=169 -DRESTRIC= - mf=0 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRIC= - abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRIC= -o3 -oi0 -g -mf=4 |
| Applications | 11 | 0 | -vmisp -DRESTRIC= -o1 -mf=0 |

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| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4 |
| Applications | 13 | 0 | -mv5e --code_state=16 -o3 -k -DRESTRICT= --symdebug:dwarf_version=4 |
| Applications | 1 | 0 | -vmbsp -o4 -mf0 --data_model=restricted -DRESTRICT= |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o3 -mf=4 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi0 -g --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=4 |
| Applications | 11 | 0 | -vmbsp --data_model=restricted -DRESTRICT= -o4 -mf=0 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -os -g -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=5 |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o0 -mf=2 |
| Applications | 9 | 0 | -vmbsp -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=5 |
| Applications | 11 | 0 | -vmbsp -ml -DRESTRICT= -o2 -mf=3 |
| Applications | 13 | 0 | -v28 -ml -mt -o3 -pm -mf |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRICT= --abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o1 -os -g -me -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -pm -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=2 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -o3 -me |
| Applications | 11 | 0 | -vmbsp -DRESTRICT= -o0 -mf=1 |

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| Applications | 11 | 0 | -vmosp -DRESTRIC= -o3 -mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o2 -g -me -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRIC= --opt_level=3 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRIC= -o2 -mf=0 |
| Applications | 11 | 0 | -vmosp --data_model=restricted -DRESTRIC= -o4 -mf=2 |
| Applications | 20 | 0 | -v28 -ml -ms -o3 -k -DRESTRIC= |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRIC= -mf=2 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRIC= -o3 -oi0 -g -mf=5 |
| Applications | 10 | 0 | -v28 -ml -mt -o4 -mf2 -DRESTRIC= |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o2 -g --symdebug:dwarf_version=4 -mf=5 |
| Applications | 11 | 0 | -vmosp --code_model=small --data_model=small -DRESTRIC= -o3 -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRIC= -abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o2 -os -g -mf=5 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=4 |
| Applications | 25 | 0 | -mv5e -DRESTRIC= -o3 -oi20 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRIC= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRIC= -mf=3 |
| Applications | 25 | 0 | -mv5e -DRESTRIC= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -DRESTRIC= -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRIC= -o3 -oi20 -mf=3 |
| Applications | 39 | 0 | -mv5e -o3 -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=1 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRIC= -mf=2 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=1 |
| Applications | 25 | 0 | -mv6 -DRESTRIC= -o2 -g -mf=1 |
| Applications | 13 | 0 | -v28 -ml -mt -o3 -pm --rpt_threshold=80 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRIC= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o2 -g -os -mf=4 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=2 |
| Applications | 11 | 0 | -vmosp -DRESTRIC= -o2 -mf=4 |

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| Applications | 11 | 0 | -vmosp -DRESTRICT= -o0 -mf=0 |
| Applications | 11 | 0 | -vmosp --data_model=restricted -DRESTRICT= -o2 -mf=4 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi0 -mf=2 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=2 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=5 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=4 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 --symdebug:none -mf=5 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -mf=0 |
| Applications | 9 | 0 | -vmosp --data_model=restricted -pm -op2 -o3 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o3 -oi20 -mf=3 |
| Applications | 2 | 0 | -v28 -ml -mt -o4 -ms |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=3 |
| Applications | 25 | 0 | -mv6 -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o1 --plain_char=signed -mf=5 |
| Applications | 2 | 0 | -vmosp -o4 -mf0 -DRESTRICT= |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --float_support=none --enum_type=int -g -mf=3 |
| Applications | 10 | 0 | -v28 -ml -o2 -ms -DRESTRICT= |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=0 |
| Applications | 11 | 0 | -vmosp -ml -DRESTRICT= -o1 -mf=4 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o1 -mf=2 |
| Applications | 11 | 0 | -vmosp -DRESTRICT= -o0 -mf=5 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=3 |
| Applications | 10 | 0 | -v28 -ml -mt -o0 -DRESTRICT= |
| Applications | 11 | 0 | -vmosp --code_model=small --data_model=small -DRESTRICT= -o0 -mf=0 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -mf=2 |
| Applications | 1 | 0 | -vmosp -o4 -mf0 -DRESTRICT= --parallel=4 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 -g -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRICT= --abi=eabi -mf=3 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRICT= -mf=4 |
| Applications | 11 | 0 | -vmosp --code_model=small --data_model=small -DRESTRICT= -o4 -mf=0 |

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| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRIC= -o3 -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -pm -o3 -DRESTRIC= --abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv6 -DRESTRIC= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o3 -oi0 -mf=3 |
| Applications | 3 | 0 | -v28 -ml -DRESTRIC= |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon -me -DRESTRIC= -o3 -oi0 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRIC= --parallel=2 -mf=3 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=3 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRIC= --parallel=2 -mf=0 |
| Applications | 11 | 0 | -vmssp -DRESTRIC= -o2 -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRIC= -o2 -g -mf=4 |
| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=1 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=5 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRIC= -o0 -mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRIC= -o2 -g -mf=5 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRIC= -g -me --embedded_constants=off -mf=1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRIC= -o3 -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRIC= -mf=3 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRIC= -o2 -g -mf=3 |
| Applications | 11 | 0 | -vmssp -DRESTRIC= -o2 -mf=1 |
| Applications | 9 | 0 | -vmspx --data_model=large -pm -op2 -o3 -DRESTRIC= --symdebug:dwarf_version=4 -mf=1 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRIC= -o4 -mf=4 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRIC= -o2 -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRIC= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRIC= -o3 -oi20 -mf=1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRIC= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRIC= -o3 -mf=0 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRIC= -o1 -mf=3 |

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| Applications | 25 | 0 | -mv7M3 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o0 -mf=1 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o1 -mf=5 |
| Applications | 14 | 0 | -mv7r4 --code_state=16 -o3 -DRESTRICT= |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o3 -oi20 -mf=3 |
| Applications | 1 | 0 | -v28 -ml -DRESTRICT= --symdebug:dwarf_version=4 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi0 -g -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 9 | 0 | -vmssp -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=2 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o2 -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=3 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o3 -mf=2 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mi -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --code_state=16 -me -DRESTRICT= -o2 -g -mf=3 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=0 |
| Applications | 9 | 0 | -vmssp --data_model=restricted -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=5 |
| Applications | 11 | 0 | -vmssp --code_model=small --data_model=small -DRESTRICT= -o3 -mf=5 |
| Applications | 20 | 0 | -v28 -ml -mt -ms -o3 -k -DRESTRICT= --symdebug:dwarf_version=4 --abi=eabi |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=0 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o1 -os -g -me -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o2 -g -mf=5 |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=1 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o2 -mf=1 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -pm -o3 -DRESTRICT= --abi=eabi -mf=3 |
| Applications | 22 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=2 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 -me -mf=5 |

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| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRICT= -o3 -mf=1 |
| Applications | 9 | 0 | -vmspx --data_model=restricted -pm -op2 -o3 --pack_all_structs --diag_suppress=169 -DRESTRICT= -mf=1 |
| Applications | 2 | 0 | -v28 -ml -o1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=1 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o2 -g -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o2 -g -mf=1 |
| Applications | 11 | 0 | -vmspx -DRESTRICT= -o0 -mf=4 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o2 -g -mf=2 |
| Applications | 39 | 0 | -mv7M3 --code_state=16 -o3 --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=5 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -pm -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRICT= -abi=eabi -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICT= -o2 -os -g -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -pm -o3 -DRESTRICT= -mf=4 |
| Applications | 9 | 0 | -vmspx --code_model=small --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=0 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRICT= -o0 -mf=2 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRICT= -o2 -g -mf=2 |
| Applications | 39 | 0 | -mv5e -o3 -mf=2 |
| Applications | 10 | 0 | -v28 -ml -o3 -mf -DRESTRICT= --symdebug:dwarf_version=4 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRICT= -o3 -mf=2 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o1 -mf=0 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o3 -mf=4 |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=2 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o2 -mf=3 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -mf=5 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o1 -os -g -me -mf=1 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRICT= --abi=eabi -mf=4 |

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| Applications | 11 | 0 | -vmSPX --data_model=restricted -DRESTRICt= -o4 -mf=1 |
| Applications | 25 | 0 | -mv6 -DRESTRICt= -o2 -g -mf=5 |
| Applications | 11 | 0 | -vmSPX -DRESTRICt= -o0 -mf=5 |
| Applications | 3 | 0 | -v28 -ml -mt -pm -o3 -v28 -DRESTRICt= |
| Applications | 13 | 0 | -mv7A8 --neon -me --unaligned_access=off -o3 -k -DRESTRICt= |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRICt= -mf=0 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICt= -o2 -g -mf=2 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o3 -DRESTRICt= --abi=eabi -mf=5 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 -mf=2 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -o1 |
| Applications | 11 | 0 | -vmSP -DRESTRICt= -o0 -mf=4 |
| Applications | 13 | 0 | -v28 --float_support=fpu64 -ml -o3 -pm -mf3 --abi=eabi |
| Applications | 25 | 0 | -mv6 --code_state=16 -DRESTRICt= -o3 -oi0 -g -mf=5 |
| Applications | 25 | 0 | -mv4 -DRESTRICt= -o3 -g -mf=2 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -DRESTRICt= -o3 -oi0 -g -mf=4 |
| Applications | 9 | 0 | -vmSPX --data_model=restricted -pm -op2 -o3 -DRESTRICt= -mf=5 |
| Applications | 11 | 0 | -vmSPX --data_model=restricted -DRESTRICt= -o2 -mf=5 |
| Applications | 10 | 0 | -mv7M4 -me -o3 -mf=2 |
| Applications | 25 | 0 | -mv5e -o4 -DRESTRICt= -mf=2 |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICt= -mf=5 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICt= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -pm -o3 -DRESTRICt= --abi=eabi -mf=5 |
| Applications | 11 | 0 | -vmSPX -DRESTRICt= -o2 -mf=5 |
| Applications | 25 | 0 | -mv6 -DRESTRICt= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv7R4 --float_support=vfpv3d16 -me --unaligned_access=off -o4 -DRESTRICt= -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICt= -o2 -g -mf=1 |
| Applications | 25 | 0 | -mv7R4 -DRESTRICt= -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -oi0 -mf=0 |
| Applications | 39 | 0 | -mv5e -o3 -mf=5 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICt= -g --symdebug:dwarf_version=4 -mf=0 |
| Applications | 25 | 0 | -mv5e -DRESTRICt= -o2 --symdebug:none -mf=2 |
| Applications | 11 | 0 | -vmSP -DRESTRICt= -o4 -mf=4 |
| Applications | 10 | 0 | -v28 -ml -mt -o2 -mi -DRESTRICt= --symdebug:dwarf_version=4 |
| Applications | 11 | 0 | -vmSP -DRESTRICt= -o0 -mf=3 |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICt= --parallel=2 -mf=4 |
| Applications | 11 | 0 | -vmSPX --code_model=small --data_model=small -DRESTRICt= -o3 -mf=2 |

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| Applications | 13 | 0 | -v28 --float_support=fpu64 -ml -o3 -pm --abi=eabi |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o3 -oi20 -pm -op2 -mf=0 |
| Applications | 14 | 0 | -mv7M4 --code_state=16 -o3 --float_support=none --enum_type=int -g |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -mt -o1 -DRESTRICT= -abi=eabi |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRICT= -mf=3 |
| Applications | 25 | 0 | -mv4 -DRESTRICT= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o3 -oi20 -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=4 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -o3 -DRESTRICT= -mf=5 |
| Applications | 10 | 0 | -v28 -ml -mt -o2 -DRESTRICT= |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=4 |
| Applications | 10 | 0 | -v28 --float_support=fpu64 -ml -o3 -mf -DRESTRICT= -abi=eabi |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi0 -mf=5 |
| Applications | 14 | 0 | -v28 -ml -mt -o3 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o2 -g -mf=0 |
| Applications | 25 | 0 | -mv5e -o3 -DRESTRICT= -mf=4 |
| Applications | 39 | 0 | -mv5e -o3 -mf=1 |
| Applications | 25 | 0 | -mv6M0 -o4 -DRESTRICT= --parallel=2 -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me -DRESTRICT= -o3 -pm -op2 -mf=0 |
| Applications | 25 | 0 | -mv7M4 -o4 -DRESTRICT= -me -mf=5 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRICT= -o4 -mf=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=1 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=5 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g --symdebug:dwarf_version=4 -mf=0 |
| Applications | 11 | 0 | -vmspx -ml -DRESTRICT= -o3 -mf=3 |
| Applications | 14 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu32 -pm -o1 -DRESTRICT= --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o3 --embedded_constants=off -mf=2 |
| Applications | 9 | 0 | -vmspx --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=3 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -mf=2 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRICT= -o2 -mf=0 |

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| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o1 -mf=3 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 -me -DRESTRIC= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me -DRESTRIC= -o3 -pm -op2 -mf=1 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=3 |
| Applications | 13 | 0 | -mv7M3 --code_state=16 -o3 -k -DRESTRIC= |
| Applications | 9 | 0 | -vmspx --data_model=restricted -pm -op2 -o3 -DRESTRIC= -mf=3 |
| Applications | 16 | 0 | -v28 -ml -mt -o3 --abi=eabi -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon -me -DRESTRIC= -o3 -pm -op2 -mf=2 |
| Applications | 21 | 0 | -mv7M4 -me -o3 -mf=1 |
| Applications | 13 | 0 | -v28 --float_support=fpu32 -ml -o3 -pm -mf3 |
| Applications | 10 | 0 | -v28 -ml -DRESTRIC= --symdebug:dwarf_version=4 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRIC= -o2 -g -mf=4 |
| Applications | 25 | 0 | -mv6M0 --code_state=16 -DRESTRIC= -o2 -g -mf=1 |
| Applications | 9 | 0 | -vmspx --code_model=small --data_model=small -pm -op2 -o3 -DRESTRIC= -mf=5 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off -me -DRESTRIC= -o3 -mf=2 |
| Applications | 9 | 0 | -vmisp -pm -op2 -o3 -DRESTRIC= -mf=2 |
| Applications | 25 | 0 | -mv5e -DRESTRIC= -o2 --symdebug:none -mf=1 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRIC= -mf=4 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRIC= -o0 -mf=5 |
| Applications | 10 | 0 | -v28 -ml -o4 -mf -DRESTRIC= --parallel=2 |
| Applications | 25 | 0 | -mv7R4 -DRESTRIC= -o2 -g -mf=0 |
| Applications | 10 | 0 | -v28 -ml -o4 -ms -DRESTRIC= |
| Applications | 10 | 0 | -v28 -ml -o2 -DRESTRIC= |
| Applications | 25 | 0 | -mv4 -DRESTRIC= -o1 -os -g -me -mf=5 |
| Applications | 10 | 0 | -v28 --float_support=fpu32 -ml -o3 -mf -DRESTRIC= |
| Applications | 14 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -DRESTRIC= -abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRIC= -o3 -oi20 -pm -op2 -mf=2 |
| Applications | 11 | 0 | -vmisp -DRESTRIC= -o3 -mf=1 |
| Applications | 11 | 0 | -vmisp -DRESTRIC= -o0 -mf=0 |
| Applications | 11 | 0 | -vmspx --data_model=restricted -DRESTRIC= -o3 -mf=1 |
| Applications | 11 | 0 | -vmspx --code_model=small --data_model=small -DRESTRIC= -o0 -mf=3 |
| Applications | 25 | 0 | -mv7M3 --code_state=16 -o4 -DRESTRIC= -mf=1 |
| Applications | 11 | 0 | -vmspx -DRESTRIC= -o1 -mf=4 |

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| Applications | 20 | 0 | -v28 -ml -mt --float_support=fpu64 -ms -o3 -k -DRESTRICT= --abi=eabi |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o4 -DRESTRICT= --parallel=2 -mf=1 |
| Applications | 11 | 0 | -vmssp --data_model=restricted -DRESTRICT= -o1 -mf=4 |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=5 |
| Applications | 48 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=2 |
| Applications | 25 | 0 | -mv5e --float_support=vfpv2 -me -DRESTRICT= -o2 -g -mf=2 |
| Applications | 39 | 0 | -mv7M4 --code_state=16 -o3 -mf=4 |
| Applications | 13 | 0 | -mv7A8 --code_state=16 --neon -me --unaligned_access=off -o3 -k -DRESTRICT= |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= --abi=eabi -mf=1 |
| Applications | 14 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -DRESTRICT= -mf=0 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --neon --code_state=16 -me -DRESTRICT= -o2 -g -mf=4 |
| Applications | 11 | 0 | -vmssp -ml -DRESTRICT= -o0 -mf=3 |
| Applications | 11 | 0 | -vmssp --code_model=small --data_model=small -DRESTRICT= -o1 -mf=0 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o1 --plain_char=signed -mf=4 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=3 |
| Applications | 25 | 0 | -mv4 -o3 -DRESTRICT= --symdebug:dwarf_version=4 -mf=2 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -o1 -mf=0 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| Applications | 25 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me -DRESTRICT= -o3 -oi0 -mf=2 |
| Applications | 19 | 0 | -mv7M4 --code_state=16 -DRESTRICT= -g -me --embedded_constants=off -mf=0 |
| Applications | 25 | 0 | -mv7M4 --code_state=16 -o3 -DRESTRICT= -mf=4 |
| Applications | 16 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| Applications | 25 | 0 | -mv5e --code_state=16 -DRESTRICT= -o3 -oi20 -pm -op2 -mf=3 |
| Applications | 25 | 0 | -mv5e -DRESTRICT= -o3 -oi20 -mf=0 |
| Applications | 11 | 0 | -vmssp -DRESTRICT= -o1 -mf=0 |
| Applications | 25 | 0 | -mv5e --code_state=16 -o3 -DRESTRICT= -mf=0 |
| Applications | 9 | 0 | -vmssp --data_model=small -pm -op2 -o3 -DRESTRICT= -mf=1 |
| Applications | 25 | 0 | -mv7R4 --code_state=16 -o3 -DRESTRICT= -mf=4 |
| Applications | 14 | 0 | -v28 -ml -mt --div_support=div0 --float_support=fpu64 -pm -o3 -DRESTRICT= --abi=eabi -mf=0 |

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| Applications | 13 | 0 | -v28 --float_support=fpu64 -ml -o3 -pm -mf --abi=eabi |
| Applications | 25 | 0 | -mv4 --code_state=16 -DRESTRICT= -o2 -g -os -mf=1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -o3 -mf2 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o2 -mt -mf5 --abi=eabi |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp -ml -o2 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e -o3 -oi0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e -o3 -mf -oi25 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt -mf3 -o2 --abi=coffabi |
| Bugslayer Regression Testing | 121 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmbsp |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp --code_model=small --data_model=small -o0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me -o2 -g |
| Bugslayer Regression Testing | 121 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp -o3 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp -ml -o0 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp -ml -o3 -mf0 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --opt_level=3 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me -o3 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 --code_state=16 -o2 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 -o3 -g -os |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o0 |

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| Bugslayer Regression Testing | 47 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 --abi=eabi |
| Bugslayer Regression Testing | 47 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv4 --code_state=16 -o0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7r4 --float_support=vfpv3d16 -o2 -mf3 --pack_all_structs --unaligned_access=off |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon -o0 -ms -mf0 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o0 -mt -g --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o3 -mf5 -oi20 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt -o3 --symdebug:dwarf_version=4 --abi=coffabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --opt_level=2 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi0 |
| Bugslayer Regression Testing | 150 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmssp --code_model=small --data_model=small -o2 -mf5 |
| Bugslayer Regression Testing | 931 | 0 | -vmssp --code_model=small --data_model=small -o3 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6M0 -g --opt_level=off |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=coffabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me -o1 -ms |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 --abi=eabi -DDOUBLE_IS_64BITS |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o2 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 --code_state=16 -o2 -g |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu32 -ooff -mf3 |
| Bugslayer Regression Testing | 121 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -o2 -mt -mf5 --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt --abi=eabi --opt_level=0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e -o2 -g --symdebug:dwarf_version=4 |

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| Bugslayer Regression Testing | 121 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7r4 --float_support=vfpv3d16 -o1 --pack_all_structs --unaligned_access=off |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi |
| Bugslayer Regression Testing | 150 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx -o2 -mf0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o1 |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --pack_all_structs |
| Bugslayer Regression Testing | 931 | 0 | -vmspx -o1 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --tmu_support=tmu0 -mt -o3 --fp_mode=relaxed |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --tmu_support=tmu1 -mt -o3 --fp_mode=relaxed --abi=eabi |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 -o3 -oi20 -mf3 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv4 --code_state=16 -o2 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --code_state=16 -o3 -oi0 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt -o2 -mf3 --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M3 --code_state=16 -o1 -mf4 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o3 -mt -mf -mi --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o3 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=4 |
| Bugslayer Regression Testing | 1388 | 0 | |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt --opt_level=0 |
| Bugslayer Regression Testing | 121 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 -o4 -mf4 -me --parallel=2 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt --abi=eabi --opt_level=1 |

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| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -o1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 -o2 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o2 -g |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 --abi=eabi -DDOUBLE_IS_64BITS |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o2 -mt -mf5 --abi=coffabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 --code_state=16 -o3 -mf4 -oi20 -g --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o3 -mf3 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu32 -mt -o1 -mf1 |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 --abi=eabi |
| Bugslayer Regression Testing | 1728 | 0 | |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --code_state=16 -o2 -g |
| Bugslayer Regression Testing | 931 | 0 | -vmsex -ml -o2 -mf0 |
| Bugslayer Regression Testing | 931 | 0 | -vmsex --data_model=restricted -o3 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6M0 --code_state=16 -o1 -mf4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt -mf3 -o2 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmsex -ml -o3 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt -o2 -mf3 --symdebug:dwarf_version=4 --abi=coffabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o1 -mf2 --symdebug:none |
| Bugslayer Regression Testing | 931 | 0 | -vmsex --code_model=small --data_model=small -o2 -mf0 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=3 |
| Bugslayer Regression Testing | 931 | 0 | -vmsex -o2 -mf5 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi --opt_level=2 |

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| Bugslayer Regression Testing | 1200 | 0 | |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e -o2 -g |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o3 -mt --rpt_threshold=20 --abi=coffabi |
| Bugslayer Regression Testing | 121 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 --abi=eabi |
| Bugslayer Regression Testing | 150 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 -o3 -mf -g --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 -mf1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -o1 -me |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -mf0 -oi0 |
| Bugslayer Regression Testing | 931 | 0 | -vmssp --data_model=restricted -o2 -mf0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --float_support=vfpv2 -me -o1 |
| Bugslayer Regression Testing | 931 | 0 | -vmssp -o3 -mf0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --code_state=16 -o0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -oi20 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 --pack_all_structs --unaligned_access=off |
| Bugslayer Regression Testing | 931 | 0 | -vmssp --code_model=small --data_model=small -o1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv4 -o2 -mf1 -me -rr5 -rr6 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -o2 -g |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmssp --data_model=small |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -o3 -oi0 --code_state=16 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi --opt_level=3 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --abi=eabi --opt_level=3 |
| Bugslayer Regression Testing | 931 | 0 | -vmssp -o0 |

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| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o3 |
| Bugslayer Regression Testing | 1227 | 0 | -mv4 -o2 -g |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --code_model=small --data_model=small -o3 -mf0 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx -o3 --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 47 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o1 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o2 -mf5 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=3 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o3 -mt -mf -mi --abi=coffabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o2 --align_for_power |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -mf1 |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx --data_model=restricted --pack_all_structs |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 -o1 --symdebug:none |
| Bugslayer Regression Testing | 47 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla0 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6 -o3 -oi0 -mf0 |
| Bugslayer Regression Testing | 931 | 0 | -o3 -g -vmspx --code_model=small --data_model=small --pack_all_structs |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx --data_model=small --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M3 --code_state=16 -o2 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --code_model=small --data_model=small -o3 -symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt --opt_level=1 |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx -ml --pack_all_structs |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --opt_level=1 |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx -ml |

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| Bugslayer Regression Testing | 1023 | 0 | -v28 --opt_level=3 |
| Bugslayer Regression Testing | 1227 | 0 | -mv4 -o1 -ms --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o2 |
| Bugslayer Regression Testing | 1227 | 0 | -mv5e --float_support=vfpv2 -me -o3 |
| Bugslayer Regression Testing | 931 | 0 | -o1 -g -vmspx --pack_all_structs |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --abi=eabi --opt_level=4 |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o0 |
| Bugslayer Regression Testing | 1227 | 0 | -mv6M0 -o3 -g |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M4 --code_state=16 -me -o3 -mf2 --float_support=none --enum_type=int -g |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -g |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt -mf3 -o2 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx --data_model=restricted -o3 -mf0 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --opt_level=4 |
| Bugslayer Regression Testing | 560 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 --abi=eabi -DDOUBLE_IS_64BITS |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx -ml --symdebug:dwarf_version=4 |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx --data_model=small --pack_all_structs |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -o3 -mt -mf -mi --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi --opt_level=4 |
| Bugslayer Regression Testing | 931 | 0 | -o2 -g -vmspx --code_model=small --data_model=small |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o3 -mt --rpt_threshold=20 --abi=eabi |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -o3 -mt --rpt_threshold=20 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx -g -o2 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --abi=eabi --opt_level=1 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -mt --symdebug:dwarf_version=4 --abi=coffabi |
| Bugslayer Regression Testing | 931 | 0 | -vmspx -ml -o3 -mf5 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7M3 -o4 -mf2 |

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| Bugslayer Regression Testing | 1227 | 0 | -mv7r4 -o4 -mf --parallel=4 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu32 -o0 -mt -g --symdebug:dwarf_version=4 --abi=coffabi |
| Bugslayer Regression Testing | 1227 | 0 | -mv7r4 --float_support=vfpv3d16 -mf3 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| Bugslayer Regression Testing | 47 | 0 | -v28 -o3 -ml -mt --float_support=fpu32 --cla_support=cla2 |
| Bugslayer Regression Testing | 1227 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o2 -g |
| Bugslayer Regression Testing | 47 | 0 | -v28 -o2 -ml -mt --float_support=fpu32 --cla_support=cla1 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu64 -o3 -mf5 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 --float_support=fpu64 -o0 -mt -g --symdebug:dwarf_version=4 --abi=eabi |
| Bugslayer Regression Testing | 931 | 0 | -vmbsp -ml -o1 |
| Bugslayer Regression Testing | 1023 | 0 | -v28 -mt -o3 --symdebug:dwarf_version=4 --abi=eabi |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=1 |

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| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=4 |

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| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu64 -ml -mt -o2 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --idiv_support=idiv0 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=1 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=4 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu1 --fp_mode=relaxed -ml -mt -o3 --abi=eabi -mf=2 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=2 |

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| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --abi=eabi -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --tmu_support=tmu0 --fp_mode=relaxed -ml -mt -o3 -mf=0 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf=3 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=5 |
| C28x Benchmarks | 9 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --abi=eabi -mf=0 |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off --float_support=vfpv3 --neon -me |
| Code Sourcery | 1448 | 0 | -mv7R4 --code_state=16 -me |
| Code Sourcery | 1448 | 0 | -mv7r4 -Ooff |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off --code_state=16 -me |
| Code Sourcery | 1448 | 0 | -mv6 -me |
| Code Sourcery | 1448 | 0 | -mv5e -me |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off --code_state=16 --float_support=vfpv3 --neon -me |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off -me --symdebug:dwarf_version=4 |
| Code Sourcery | 1448 | 0 | -mv7R4 -me --symdebug:dwarf_version=4 |
| Code Sourcery | 1448 | 0 | -mv6 --code_state=16 -me |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off --neon -me |
| Code Sourcery | 1448 | 0 | -mv5e --code_state=16 -me |
| Code Sourcery | 1448 | 0 | -mv7A8 --unaligned_access=off --code_state=16 --neon -me |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o2 --rpt_threshold=80 |
| Dinkumware | 366 | 0 | -mv7M4 -o4 -mf4 -me |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -ms --abi=eabi |
| Dinkumware | 426 | 0 | -mv6M0 -o2 -g |
| Dinkumware | 426 | 0 | -mv5e -o2 -g |
| Dinkumware | 426 | 0 | -mv5e --code_state=16 -o3 -me -ms -os |
| Dinkumware | 428 | 0 | -vmspx --data_model=restricted -o2 -mf0 |
| Dinkumware | 426 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -mi |
| Dinkumware | 428 | 0 | -vmspx --data_model=restricted -o2 -mf5 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o4 -ms |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -mf5 -g --symdebug:dwarf_version=4 |
| Dinkumware | 459 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |
| Dinkumware | 820 | 0 | -v28 -ml -mt --float_support=fpu64 -o2 --symdebug:dwarf_version=4 --abi=eabi |
| Dinkumware | 426 | 0 | -mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2 |
| Dinkumware | 819 | 0 | -v28 -ml -mt --float_support=fpu32 -o2 --symdebug:dwarf_version=4 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o3 -mf3 |
| Dinkumware | 426 | 0 | -mv5e -o3 |

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| | | | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| Dinkumware | 426 | 0 | |
| Dinkumware | 426 | 0 | -mv7R4 -o3 |
| Dinkumware | 428 | 0 | -vmspx --data_model=restricted -o0 |
| Dinkumware | 426 | 0 | -mv7R4 --code_state=16 -o3 |
| Dinkumware | 426 | 0 | -mv5e --code_state=16 -o2 -mf3 |
| | | | -mv7M4 --code_state=16 -o3 -mf2 -- |
| Dinkumware | 426 | 0 | float_support=none -g |
| Dinkumware | 819 | 0 | -v28 -ml -o1 |
| Dinkumware | 426 | 0 | -mv7M4 --code_state=16 -o1 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o2 -ms |
| Dinkumware | 338 | 0 | -vmspx -ml -o4 |
| Dinkumware | 459 | 0 | -mv6M0 -ooff |
| Dinkumware | 428 | 0 | -vmspx -ml -o3 |
| Dinkumware | 426 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| | | | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| Dinkumware | 426 | 0 | -neon --float_support=vfpv3 -o3 -mf3 |
| Dinkumware | 428 | 0 | -vmspx --data_model=large -o2 |
| | | | -v28 --float_support=fpu64 -ml -mt -o2 -mf -- |
| Dinkumware | 820 | 0 | symdebug:dwarf_version=4 --abi=eabi |
| | | | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| Dinkumware | 426 | 0 | -neon -o3 -oi0 |
| Dinkumware | 428 | 0 | -vmspx -o2 -mf5 --symdebug:dwarf_version=4 |
| Dinkumware | 426 | 0 | -mv6 -o3 -mf1 -g |
| | | | -mv7A8 --unaligned_access=off -me --neon -- |
| Dinkumware | 426 | 0 | float_support=vfpv3 -o2 -mf5 |
| Dinkumware | 428 | 0 | -vmspx -ml -o3 -mf0 -g |
| | | | -v28 --float_support=fpu64 -ml -mt -o2 -mi --abi=eabi |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -g -mo=off --abi=eabi |
| Dinkumware | 366 | 0 | -mv7M3 -o4 -mf1 -me --parallel=2 |
| Dinkumware | 428 | 0 | -vmspx -ml -o0 |
| Dinkumware | 819 | 0 | -v28 -ml -o3 -mf --symdebug:dwarf_version=4 |
| Dinkumware | 428 | 0 | -vmspx -o3 -mf5 -g |
| Dinkumware | 426 | 0 | -mv4 -o2 -g --symdebug:dwarf_version=4 |
| Dinkumware | 426 | 0 | -mv5e --symdebug:none -s |
| Dinkumware | 459 | 0 | -mv7M4 --code_state=16 -g |
| Dinkumware | 426 | 0 | -mv7R4 -o2 -g |
| | | | -mv7A8 --unaligned_access=off -me --neon -- |
| Dinkumware | 426 | 0 | float_support=vfpv3 -o1 |
| Dinkumware | 820 | 0 | -v28 -ml -mt -o4 -ms --abi=eabi |
| Dinkumware | 426 | 0 | -mv5e --float_support=vfpv2 -me -o0 |
| Dinkumware | 820 | 0 | -v28 -ml -mt -o2 --rpt_threshold=80 --abi=eabi |
| Dinkumware | 338 | 0 | -vmspx -o4 --parallel=2 |
| | | | -v28 --float_support=fpu32 -ml -mt -o2 -mf -- |
| Dinkumware | 819 | 0 | symdebug:dwarf_version=4 |
| | | | -v28 --float_support=fpu32 -ml -mt -o3 -g -mo=off |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o3 -mf |
| Dinkumware | 428 | 0 | -vmspx -ml -o2 |
| | | | -mv7A8 --unaligned_access=off -me --neon -- |
| Dinkumware | 338 | 0 | -vmspx --data_model=restricted -o4 --parallel=4 |

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| Dinkumware | 426 | 0 | -mv7R4 -o1 |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g |
| Dinkumware | 426 | 0 | -mv5e --code_state=16 -o2 --symdebug:dwarf_version=4 |
| Dinkumware | 426 | 0 | -mv7M3 --code_state=16 -o2 |
| Dinkumware | 426 | 0 | -mv4 -o3 -oi0 -mf1 -g |
| Dinkumware | 428 | 0 | -vmSPX -ml -o1 --symdebug:dwarf_version=4 |
| Dinkumware | 426 | 0 | -mv7M3 --code_state=16 -o3 -oi0 --symdebug:dwarf_version=4 |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 -mf2 --parallel=2 --abi=eabi |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 --abi=eabi |
| Dinkumware | 428 | 0 | -vmSPX -o2 -mf0 |
| Dinkumware | 428 | 0 | -vmSPX --data_model=restricted -o3 -mf0 |
| Dinkumware | 426 | 0 | -mv5e --float_support=vfpv2 -me -o3 -mf |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o0 -mf1 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o3 --symdebug:none |
| Dinkumware | 428 | 0 | -vmSPX -o1 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o4 -mf2 |
| Dinkumware | 426 | 0 | -mv7M3 --code_state=16 -o2 -mf3 -os |
| Dinkumware | 428 | 0 | -vmSPX --data_model=restricted -o1 |
| Dinkumware | 315 | 0 | -vmSP -o2 --symdebug:dwarf_version=4 |
| Dinkumware | 428 | 0 | -vmSPX --data_model=restricted -o3 -mf5 -g |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -g |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -o2 -ms |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -o3 -mf --abi=eabi |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf3 |
| Dinkumware | 428 | 0 | -vmSPX -o3 |
| Dinkumware | 204 | 0 | -mv6 --code_state=16 -o0 -ms -ss --symdebug:dwarf_version=4 |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 -mf2 --parallel=2 |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --neon -o1 -mf2 --symdebug:none |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -g -mo=off --abi=eabi |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o2 |
| Dinkumware | 426 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -o3 -mf |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o1 |
| Dinkumware | 819 | 0 | -v28 -ml -o3 --symdebug:none |
| Dinkumware | 426 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -o2 -ms --abi=eabi |
| Dinkumware | 426 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| Dinkumware | 426 | 0 | -mv4 --code_state=16 -o3 -mf -oi20 -g |
| Dinkumware | 428 | 0 | -vmSPX -ml -o2 -mf5 |

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| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - -neon -o2 -mf2 -g |
| Dinkumware | 426 | 0 | -mv7R4 --code_state=16 -o2 -mf2 -- symdebug:dwarf_version=4 |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o2 -mi --symdebug:dwarf_version=4 |
| Dinkumware | 819 | 0 | -v28 -ml -o2 |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -o2 -mf0 |
| Dinkumware | 428 | 0 | -vmssp --data_model=restricted -o3 |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me -o3 -mf4 |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -ms |
| Dinkumware | 426 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - -float_support=vfpv3 -o3 -mf |
| Dinkumware | 428 | 0 | -vmssp --data_model=restricted -o2 |
| Dinkumware | 428 | 0 | -vmssp -ml -o3 -mf5 |
| Dinkumware | 426 | 0 | -mv4 --code_state=16 -o2 -g |
| Dinkumware | 819 | 0 | -v28 -ml -mt -o3 -mf1 |
| Dinkumware | 426 | 0 | -mv5e --code_state=16 -o2 -g |
| Dinkumware | 426 | 0 | -mv6 --code_state=16 -o2 -mf2 --symdebug:none |
| Dinkumware | 426 | 0 | -mv7R4 --code_state=16 -o2 -g |
| Dinkumware | 426 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| Dinkumware | 426 | 0 | -mv4 --code_state=16 -o1 -mf3 -s |
| Dinkumware | 428 | 0 | -vmssp -o2 |
| Dinkumware | 819 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -g --abi=eabi |
| Dinkumware | 428 | 0 | -vmssp -ml -o2 -mf0 |
| Dinkumware | 428 | 0 | -vmssp -o3 -mf0 |
| Dinkumware | 428 | 0 | -vmssp -o0 |
| Dinkumware | 820 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf3 -- abi=eabi |
| Embedded Systems Paranoia | 1 | 0 | -mv7M3 --code_state=16 -o3 -mf |
| Embedded Systems Paranoia | 1 | 0 | -mv7M3 --code_state=16 -o2 -mf1 -- symdebug:dwarf_version=4 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=5 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=4 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=2 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=1 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=5 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=1 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=2 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=5 |

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| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=3 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=0 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=5 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=1 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=3 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=0 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=5 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=2 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=5 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=0 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=5 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=1 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=0 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=1 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=0 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=4 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=0 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=3 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=3 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=3 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=0 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=4 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=4 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=5 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=3 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=1 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=4 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH -mf=0 |

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| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=4 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=4 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=3 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=4 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=FLOAT_MATH -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH --abi=eabi -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt -o3 -DMATH_TYPE=IQ_MATH -mf=1 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=2 |
| FP Kernels | 18 | 0 | -v28 -ml -mt --float_support=fpu32 -o3 - DMATH_TYPE=IQ_MATH --abi=eabi -mf=5 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=3 |
| FP Kernels | 26 | 0 | -v28 -ml -mt --float_support=fpu64 -o3 - DMATH_TYPE=FLOAT_MATH --abi=eabi -mf=1 |
| GCC Extensions | 34 | 0 | -vmssp --code_model=small --data_model=small -o3 |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - -neon --float_support=vfpv3 -o3 -mf |
| GCC Extensions | 34 | 0 | -vmssp -ml -o2 |
| GCC Extensions | 34 | 0 | -vmssp --data_model=restricted -o2 |
| GCC Extensions | 34 | 0 | -mv7M3 -o4 -mf3 -me --parallel=2 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -o3 -mf |
| GCC Extensions | 34 | 0 | -mv5e --float_support=vfpv2 -me -o1 |
| GCC Extensions | 34 | 0 | -v28 -ml -o1 --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -vmssp -o2 --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -mv4 --code_state=16 -o2 -mf3 -g |
| GCC Extensions | 34 | 0 | -mv6M0 --code_state=16 -o3 -mf -oi20 |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -o3 -- embedded_constants=off |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --neon -- float_support=vfpv3 -o2 -g |
| GCC Extensions | 34 | 0 | -vmssp -o2 |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --neon -- float_support=vfpv3 -o2 -mf2 |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -o3 -mf2 -- float_support=none --enum_type=int -g |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -o3 -mf --abi=eabi |
| GCC Extensions | 34 | 0 | -vmssp -ml -o3 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o2 -ms --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf3 -- abi=eabi |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -mv6 --code_state=16 -o2 |

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| GCC Extensions | 34 | 0 | -mv5e -o2 -g -me |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -o2 -mf0 -- symdebug:dwarf_version=4 --abi=eabi |
| GCC Extensions | 34 | 0 | -mv7A8 --code_state=16 --neon -me -- unaligned_access=off -o2 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o3 |
| GCC Extensions | 34 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 |
| GCC Extensions | 34 | 0 | -mv7M4 -o4 -mf4 -me |
| GCC Extensions | 34 | 0 | -mv6 -o3 -mf5 -oi20 |
| GCC Extensions | 34 | 0 | -mv6M0 -o2 |
| GCC Extensions | 34 | 0 | -mv7R4 -me --code_state=16 -- float_support=vfpv3d16 --unaligned_access=off -o3 - mf0 -oi0 |
| GCC Extensions | 34 | 0 | -mv5e --code_state=16 -o3 |
| GCC Extensions | 34 | 0 | -v28 -ml -o2 --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -vmbsp -o2 |
| GCC Extensions | 34 | 0 | -mv7M3 --code_state=16 -o3 -mf -oi20 -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -vmbsp -o3 --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -v28 -ml |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -g |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - neon -o2 -mf3 -g |
| GCC Extensions | 34 | 0 | -vmbsp --code_model=small --data_model=small -o1 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o2 |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -g -me -- embedded_constants=off |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -ms -- abi=eabi |
| GCC Extensions | 34 | 0 | -mv7R4 -me --code_state=16 -- float_support=vfpv3d16 --unaligned_access=off -o2 - g |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me -o2 |
| GCC Extensions | 34 | 0 | -v28 -ml -o2 |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -o2 -g |
| GCC Extensions | 34 | 0 | -mv7R4 -o0 --symdebug:none -s |
| GCC Extensions | 34 | 0 | -vmbsp -o4 --parallel=2 |
| GCC Extensions | 34 | 0 | -mv7R4 --code_state=16 -o2 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf3 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -o2 -mf0 -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -mv6 --code_state=16 -o3 -oi0 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 -ms |
| GCC Extensions | 34 | 0 | -mv4 --code_state=16 -o2 -g |
| GCC Extensions | 34 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 |
| GCC Extensions | 34 | 0 | -mv7M4 --code_state=16 -o1 |
| GCC Extensions | 34 | 0 | -mv7M3 --code_state=16 -o2 |
| GCC Extensions | 34 | 0 | -mv5e -o2 --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - o3 -oi20 -mf5 |
| GCC Extensions | 34 | 0 | -vmbsp --data_model=small -o2 |
| GCC Extensions | 34 | 0 | -vmbsp -o3 |

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| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -o2 -mf0 -- symdebug:dwarf_version=4 --abi=eabi |
| GCC Extensions | 34 | 0 | -mv5e --code_state=16 -o2 -g |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -- symdebug:dwarf_version=4 --abi=eabi |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -o2 -mf3 -g |
| GCC Extensions | 34 | 0 | -mv4 -o2 |
| GCC Extensions | 34 | 0 | -vmssp --data_model=large -o2 |
| GCC Extensions | 34 | 0 | -vmssp --data_model=restricted -o3 |
| GCC Extensions | 34 | 0 | -vmssp -o1 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o3 --abi=eabi |
| GCC Extensions | 34 | 0 | -vmssp --code_model=small --data_model=small -o2 |
| GCC Extensions | 34 | 0 | -mv5e -o3 -oi0 -g |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --neon -o3 -mf5 -- plain_char=signed |
| GCC Extensions | 34 | 0 | -mv7R4 -o2 -g |
| GCC Extensions | 34 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -ms |
| GCC Extensions | 34 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - float_support=vfpv3 -o3 -oi0 -mf0 |
| GCC Extensions | 34 | 0 | -v28 -ml -o3 |
| GCC Extensions | 34 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 -ms -- abi=eabi |
| GCC Extensions | 34 | 0 | -mv7R4 --code_state=16 -o2 -mf2 -os -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o3 -mf3 |
| GCC Extensions | 34 | 0 | -mv4 -o3 -oi0 -mf0 |
| GCC Extensions | 34 | 0 | -vmssp -o4 |
| GCC Extensions | 34 | 0 | -v28 -ml -mt -o4 --parallel=2 |
| GCC Extensions | 34 | 0 | -vmssp --data_model=restricted -o1 |
| GCC Extensions | 34 | 0 | -mv6 -o2 -g --symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -vmssp -ml -o1 |
| GCC Extensions | 34 | 0 | -mv5e --code_state=16 -o2 -- symdebug:dwarf_version=4 |
| GCC Extensions | 34 | 0 | -vmssp -o1 |
| GCC Torture Test | 1064 | 0 | -mv7R4 --code_state=16 -o2 |
| GCC Torture Test | 1064 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 |
| GCC Torture Test | 1003 | 0 | -v28 -ml -mt -o2 |
| GCC Torture Test | 1064 | 0 | -mv6 --code_state=16 -o2 |
| GCC Torture Test | 1064 | 0 | -mv7A8 --code_state=16 --neon -me -- unaligned_access=off -o2 |
| GCC Torture Test | 1064 | 0 | -mv4 -o2 |
| GCC Torture Test | 1064 | 0 | -mv7M3 --code_state=16 -o2 |
| GCC Torture Test | 1064 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 |
| GCC Torture Test | 1064 | 0 | -mv5e -o2 |
| GCC Torture Test | 1001 | 0 | -v28 -ml -mt -o2 --abi=eabi |
| GCC Torture Test | 932 | 0 | -vmssp --data_model=large -o2 |
| GCC Torture Test | 1064 | 0 | -mv6M0 -o |
| GCC Torture Test | 932 | 0 | -vmssp --data_model=restricted -o2 |
| GCC Torture Test | 1064 | 0 | -mv5e --code_state=16 -o2 -- symdebug:dwarf_version=4 |

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| GCC Torture Test | 1003 | 0 | -v28 -ml -o2 --symdebug:dwarf_version=4 |
| GCC Torture Test | 932 | 0 | -vmisp -o2 --symdebug:dwarf_version=4 |
| Kernels | 77 | 0 | -mv6 --code_state=16 -o3 -oi0 -me |
| Kernels | 36 | 0 | -vmisp -ml -o2 |
| | | | |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=3 |
| Kernels | 36 | 0 | -vmisp -ml -o0 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=5 |
| Kernels | 53 | 0 | -vmisp -o2 -DRESTRICT= |
| Kernels | 36 | 0 | -vmisp -o2 --symdebug:dwarf_version=4 |
| | | | |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=0 |
| Kernels | 77 | 0 | -mv5e -o3 -mf=3 |
| Kernels | 36 | 0 | -vmisp -o2 -mf5 |
| Kernels | 77 | 0 | -mv5e --float_support=VFPv2 -me -o2 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=3 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=2 |
| Kernels | 77 | 0 | -mv5e --code_state=16 -o0 |
| | | | |
| Kernels | 53 | 0 | -vmisp --data_model=restricted -o2 -DRESTRICT= |
| Kernels | 36 | 0 | -vmisp -o4 -mf0 |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 -o2 |
| | | | |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=4 |
| Kernels | 36 | 0 | -vmisp -o3 |
| Kernels | 36 | 0 | -vmisp -o4 -mf0 |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=0 |
| Kernels | 36 | 0 | -vmisp -k -DRESTRICT= |
| Kernels | 77 | 0 | -mv5e -o3 -mf=2 |
| Kernels | 36 | 0 | -vmisp -o4 -mf5 --parallel=2 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=4 |
| Kernels | 36 | 0 | -vmisp -o2 |
| | | | |
| Kernels | 39 | 0 | -mv7a8 --neon --code_state=16 -o3 -me --unaligned_access=off -mf -s --symdebug:none |
| Kernels | 36 | 0 | -vmisp -o0 |
| Kernels | 36 | 0 | -vmisp -ml -o3 -mf5 |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=1 |
| Kernels | 36 | 0 | -vmisp -ml -o3 -mf0 |
| Kernels | 36 | 0 | -vmisp -ml -o4 -mf5 |
| Kernels | 53 | 0 | -vmisp -ml |
| Kernels | 36 | 0 | -vmisp -ml -o3 |
| Kernels | 36 | 0 | -vmisp -o3 -mf0 |
| Kernels | 36 | 0 | -vmisp -o3 |
| | | | |
| Kernels | 39 | 0 | -mv7a8 --neon --float_support=vfpv3 --code_state=16 -o3 -me --unaligned_access=off -mf -s --symdebug:none |
| Kernels | 77 | 0 | -mv5e --code_state=16 -o2 -me |
| Kernels | 53 | 0 | -vmisp |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=0 |
| Kernels | 77 | 0 | -mv4 --code_state=16 -k -o2 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=1 |

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| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=5 |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=2 |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=4 |
| Kernels | 77 | 0 | -mv5e -o3 -mf=1 |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=5 |
| Kernels | 36 | 0 | -vmssp -ml -o2 -mf5 |
| Kernels | 77 | 0 | -mv5e -o2 -me --symdebug:dwarf_version=4 |
| Kernels | 39 | 0 | -mv7a8 --neon --float_support=vfpv3 -o3 -me --unaligned_access=off -mf -s --symdebug:none |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=2 |
| Kernels | 39 | 0 | -mv7a8 --neon -o3 -me --unaligned_access=off -mf -s --symdebug:none |
| Kernels | 36 | 0 | -vmssp --data_model=restricted -k -DRESTRICT= |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=3 |
| Kernels | 36 | 0 | -vmssp -ml -o4 -mf0 |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=2 |
| Kernels | 77 | 0 | -mv5e -o3 -mf=5 |
| Kernels | 36 | 0 | -vmssp -o4 -mf5 --parallel=2 |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=4 |
| Kernels | 36 | 0 | -vmssp -o3 -mf5 |
| Kernels | 77 | 0 | -mv4 -k -o2 |
| Kernels | 77 | 0 | -mv5e -o1 |
| Kernels | 36 | 0 | -vmssp -o2 -mf0 |
| Kernels | 77 | 0 | -mv7A8 --unaligned_access=off --code_state=16 --neon -me -o2 |
| Kernels | 36 | 0 | -vmssp -o1 |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=1 |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o3 -mf=5 |
| Kernels | 77 | 0 | -mv6 -o2 -mf -oi20 |
| Kernels | 77 | 0 | -mv5e -o3 -mf=0 |
| Kernels | 77 | 0 | -mv5e -o3 -mf=4 |
| Kernels | 53 | 0 | -vmssp |
| Kernels | 36 | 0 | -vmssp -o3 -mf5 |
| Kernels | 77 | 0 | -mv7M4 --code_state=16 -o3 -mf=0 |
| Kernels | 36 | 0 | -vmssp -o1 |
| Kernels | 36 | 0 | -vmssp -o2 -mf0 |
| Kernels | 53 | 0 | -vmssp --data_model=large -o2 -DRESTRICT= --symdebug:dwarf_version=4 |
| Kernels | 77 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -o3 -mf=1 |
| Kernels | 36 | 0 | -vmssp -o2 -mf5 --symdebug:dwarf_version=4 |
| Kernels | 36 | 0 | -vmssp -ml -o1 --symdebug:dwarf_version=4 |
| Kernels | 36 | 0 | -vmssp -o0 |
| Kernels | 77 | 0 | -mv7A8 --unaligned_access=off --neon -me -o2 |
| Kernels | 36 | 0 | -vmssp -o3 -mf0 |
| Kernels | 36 | 0 | -vmssp -ml -o2 -mf0 |
| Kernels | 77 | 0 | -mv7M3 --code_state=16 -o2 --symdebug:dwarf_version=4 |
| Kernels | 77 | 0 | -mv6M0 --code_state=16 -o3 -mf=3 |
| Kernels | 36 | 0 | -vmssp --data_model=large -k -DRESTRICT= |

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| Nemics R4 | 1 | | -mv5e --code_state=16 --symdebug:dwarf_version=4 |
| Nemics R4 | 1 | | -mv7M3 --code_state=16 |
| Nemics R4 | 1 | | -mv7R4 --code_state=16 |
| Nemics R4 | 1 | | -mv7A8 --neon -me --unaligned_access=off |
| Nemics R4 | 1 | | -mv7R4 |
| Nemics R4 | 1 | | -mv7A8 --code_state=16 --neon -me --unaligned_access=off |
| Perennial C++03 | 67833 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -ooff |
| Perennial C++03 | 67833 | 0 | -v28 -ml --exceptions --rtti -o2 -mf |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o0 --symdebug:none |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -mf3 |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -mf |
| Perennial C++03 | 67829 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o3 |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -mf3 |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o3 |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o3 |
| Perennial C++03 | 66593 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -ms |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Perennial C++03 | 67829 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -ms |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti |
| Perennial C++03 | 61574 | 0 | -ml -mt -o2 |
| Perennial C++03 | 66593 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -mf |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -ooff |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Perennial C++03 | 61574 | 0 | -v28 --abi=eabi -ml -mt -o2 |
| Perennial C++03 | 66593 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o0 |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o3 -mf3 |
| Perennial C++03 | 67829 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o3 -mf3 |
| Perennial C++03 | 67829 | 0 | -v28 --float_support=fpu32 -ml --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Perennial C++03 | 67833 | 0 | -v28 -ml --exceptions --rtti -o3 -mf |
| Perennial C++03 | 67833 | 0 | -v28 -ml --exceptions --rtti -o2 |
| Perennial C++03 | 67833 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o0 |
| Perennial C++03 | 66593 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o3 |

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| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o2 -ms -- symdebug:dwarf_version=4 |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt -- exceptions --rtti -o2 -ms |
| Perennial C++03 | 67829 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt -- exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Perennial C++03 | 64457 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt -- exceptions --rtti -o0 |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt -- exceptions --rtti -ooff |
| Perennial C++03 | 67833 | 0 | -v28 -ml --exceptions --rtti -o2 -ms |
| Perennial C++03 | 66593 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt -- exceptions --rtti -o3 -mf3 |
| Perennial C++03 | 66597 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o2 -g |
| Perennial C++03 | 67833 | 0 | -v28 -ml -mt --exceptions --rtti -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o0 -g |
| Perennial C++14 | 243017 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -o1 |
| Perennial C++14 | 234078 | 0 | -vmSPX -ml --rtti --exceptions -o4 --parallel=4 |
| Perennial C++14 | 234772 | 0 | -vmSPX --data_model=restricted --rtti --exceptions - o1 -g -mf0 |
| Perennial C++14 | 243017 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -o3 -- embedded_constants=off |
| Perennial C++14 | 243001 | 0 | -mv5e -rtti --exceptions -o2 -g -me |
| Perennial C++14 | 213354 | 0 | -mv7R4 --code_state=16 -rtti -o2 -mf2 -os -- symdebug:dwarf_version=4 |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o3 -mf0 |
| Perennial C++14 | 234772 | 0 | -vmSPX --data_model=restricted --rtti --exceptions - o3 -g -mf5 |
| Perennial C++14 | 213354 | 0 | -mv7R4 -rtti -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o0 -mf5 |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o3 -g -mf5 |
| Perennial C++14 | 297113 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -g -me -- embedded_constants=off |
| Perennial C++14 | 234078 | 0 | -vmSPX --data_model=restricted --rtti --exceptions - o4 --symdebug:dwarf_version=4 |
| Perennial C++14 | 243001 | 0 | -mv5e -rtti --exceptions -o3 -oi0 -g |
| Perennial C++14 | 243001 | 0 | -mv6 -rtti --exceptions -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmSPX --data_model=restricted --rtti --exceptions - o2 -mf0 |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o1 -mf5 |
| Perennial C++14 | 297113 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -g |
| Perennial C++14 | 243001 | 0 | -mv6 --code_state=16 -rtti --exceptions -o3 -oi0 |
| Perennial C++14 | 234772 | 0 | -vmSPX -ml --rtti --exceptions -o0 -mf0 -- symdebug:dwarf_version=4 |
| Perennial C++14 | 213354 | 0 | -mv7R4 --code_state=16 -rtti -o2 -g |
| Perennial C++14 | 213307 | 0 | -mv7M3 -rtti -o4 -mf5 -me --parallel=2 |
| Perennial C++14 | 234772 | 0 | -vmSPX --data_model=restricted --rtti --exceptions - o2 -mf5 |
| Perennial C++14 | 180914 | 0 | -vmSPX -ml -o2 -mf4 --symdebug:dwarf_version=4 |

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| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o0 -g |
| Perennial C++14 | 242954 | 0 | -mv5e -rtti --exceptions -o4 --symdebug:dwarf_version=4 |
| Perennial C++14 | 188292 | 0 | -mv7m4 -o2 --symdebug:dwarf_version=4 |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o2 -mf0 --symdebug:dwarf_version=4 |
| Perennial C++14 | 243001 | 0 | -mv6 -rtti --exceptions -o3 -mf5 -oi20 |
| Perennial C++14 | 267450 | 0 | -mv6M0 -rtti -ooff -mf -oi20 |
| Perennial C++14 | 213354 | 0 | -mv7M3 --code_state=16 -rtti -o2 -g |
| Perennial C++14 | 243001 | 0 | -mv4 -rtti --exceptions -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o2 |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o1 |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o1 -g -mf0 |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o2 |
| Perennial C++14 | 213354 | 0 | -mv7R4 -rtti -o0 --symdebug:none -s |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o3 -mf0 |
| Perennial C++14 | 188292 | 0 | -mv7m4 -o2 |
| Perennial C++14 | 213354 | 0 | -mv7M3 --code_state=16 -rtti -o3 -mf -oi20 |
| Perennial C++14 | 243001 | 0 | -mv5e --code_state=16 -rtti --exceptions -o2 -g |
| Perennial C++14 | 243017 | 0 | -mv7M4 -rtti --exceptions --code_state=16 -o3 -mf5 -me |
| Perennial C++14 | 213354 | 0 | -mv7M4 -rtti --code_state=16 -o3 -mf2 --float_support=none -g |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o2 -mf5 |
| Perennial C++14 | 243001 | 0 | -mv4 --code_state=16 -rtti --exceptions -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o3 -mf0 |
| Perennial C++14 | 213354 | 0 | -mv6M0 --code_state=16 -rtti -o2 -g |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o3 |
| Perennial C++14 | 243001 | 0 | -mv4 --code_state=16 -rtti --exceptions -o2 -mf3 -g --symdebug:dwarf_version=4 |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o0 -mf5 |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o1 |
| Perennial C++14 | 243001 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -o1 |
| Perennial C++14 | 243001 | 0 | -mv5e --code_state=16 -rtti --exceptions -o3 |
| Perennial C++14 | 234772 | 0 | -vmspx -ml --rtti --exceptions -o2 -g -mf5 --symdebug:dwarf_version=4 |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o3 |
| Perennial C++14 | 242970 | 0 | -mv7M4 -rtti --exceptions -o4 -mf4 -me |
| Perennial C++14 | 243001 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -o2 -g |
| Perennial C++14 | 243001 | 0 | -mv4 -rtti --exceptions -o3 -oi0 -mf0 |
| Perennial C++14 | 234772 | 0 | -vmspx --data_model=restricted --rtti --exceptions -o1 -mf5 --symdebug:dwarf_version=4 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml --symdebug:none --abi=eabi |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o2 -g |

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| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o1 |
| Perennial C11 | 33017 | 0 | -ml -mt --float_support=fpu64 -o2 --abi=eabi |
| Perennial C11 | 33017 | 0 | -v28 -ml -mt -o3 --rpt_threshold=80 --abi=eabi |
| Perennial C11 | 33429 | 0 | -vmssp -ml -o1 --symdebug:dwarf_version=4 |
| Perennial C11 | 33418 | 0 | -vmssp -o2 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -g -me -- embedded_constants=off |
| Perennial C11 | 33418 | 0 | -vmssp -o1 -mf0 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - -neon --float_support=vfpv3 -o3 -mf |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - o2 -g |
| Perennial C11 | 33017 | 0 | -ml -mt --float_support=fpu64 -o2 --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmssp -o1 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7M4 -o4 -mf4 -me --symdebug:dwarf_version=4 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me -o2 |
| Perennial C11 | 33418 | 0 | -vmssp -o2 -mf0 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o2 - mf5 |
| Perennial C11 | 33600 | 0 | -mv7R4 --code_state=16 -g -o2 |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -mt -ooff -- symdebug:dwarf_version=4 |
| Perennial C11 | 33418 | 0 | -vmssp -o1 -mf0 |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml --symdebug:none |
| Perennial C11 | 33429 | 0 | -vmssp -ml -o3 -mf0 |
| Perennial C11 | 33598 | 0 | -mv5e --float_support=vfpv2 -me -o1 |
| Perennial C11 | 33418 | 0 | -vmssp -o2 -mf4 |
| Perennial C11 | 33600 | 0 | -mv5e --code_state=16 -o3 |
| Perennial C11 | 33600 | 0 | -mv4 -o2 -g |
| Perennial C11 | 29793 | 0 | -ml -mt --float_support=fpu32 -o2 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o1 - mf5 |
| Perennial C11 | 33418 | 0 | -vmssp -o3 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -ooff -- symdebug:dwarf_version=4 --abi=eabi |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mi --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o2 - mf0 |
| Perennial C11 | 33429 | 0 | -vmssp -ml -o3 -mf5 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -g --abi=eabi |
| Perennial C11 | 33429 | 0 | -vmssp -ml -o3 --symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv7M3 --code_state=16 -o3 -mf -oi20 |
| Perennial C11 | 33418 | 0 | -vmssp -o3 -mf5 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o2 -ms |
| Perennial C11 | 33428 | 0 | -vmssp --data_model=restricted -o2 -mf5 |
| Perennial C11 | 33428 | 0 | -vmssp --data_model=restricted -o2 -mf0 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -g |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - -float_support=vfpv3 -o3 -oi0 -mf0 |

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| Perennial C11 | 33418 | 0 | -vmosp -o2 -mf5 --symdebug:dwarf_version=4 |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o4 -parallel=4 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g |
| Perennial C11 | 33598 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| Perennial C11 | 33429 | 0 | -vmosp -ml -o1 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7m4 -o2 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 -g --abi=eabi |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o0 |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o1 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -ms --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmosp -g -o2 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -g |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml --symdebug:none --abi=eabi |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -g -o2 |
| Perennial C11 | 33600 | 0 | -mv7m4 --float_support=fpv4spd16 -g -o2 |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o1 -mf0 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o3 -mf -g |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 --rpt_threshold=80 --abi=eabi |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -o3 --abi=eabi |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -g -o2 |
| Perennial C11 | 33418 | 0 | -vmosp -o2 -mf4 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf3 -g |
| Perennial C11 | 33600 | 0 | -mv7M3 --code_state=16 -g -o2 |
| Perennial C11 | 33600 | 0 | -mv7R4 --code_state=16 -o2 -mf2 -os |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o1 |
| Perennial C11 | 33600 | 0 | -mv7m4 --float_support=fpv4spd16 -g -o2 |
| Perennial C11 | 33600 | 0 | -mv6M0 --code_state=16 -o3 -mf -oi20 |
| Perennial C11 | 33600 | 0 | -mv6M0 --code_state=16 -o3 -mf -oi20 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -mf3 -g |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o2 -g |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o3 -mf0 |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o4 |
| Perennial C11 | 33418 | 0 | -vmosp -o1 |
| Perennial C11 | 33429 | 0 | -vmosp -ml -o2 -mf0 |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o3 -mf0 |
| Perennial C11 | 33418 | 0 | -vmosp -o1 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g |
| Perennial C11 | 33429 | 0 | -vmosp -ml -o1 -mf0 |
| Perennial C11 | 33600 | 0 | -mv6 -o2 -g --symdebug:dwarf_version=4 |
| Perennial C11 | 33418 | 0 | -vmosp -o4 |
| Perennial C11 | 33600 | 0 | -mv6M0 --code_state=16 -g -o2 |

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|---------------|-------|---|---|
| Perennial C11 | 33598 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| Perennial C11 | 33429 | 0 | -vmspx -ml -o4 |
| Perennial C11 | 33418 | 0 | -vmsp -o1 -mf5 |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o2 -mf0 |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o3 |
| Perennial C11 | 33418 | 0 | -vmspx -o4 |
| Perennial C11 | 33418 | 0 | -vmspx -o2 -mf0 |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o1 -mf0 |
| Perennial C11 | 33600 | 0 | -mv7R4 -o0 --symdebug:none -s |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o3 |
| Perennial C11 | 29793 | 0 | -v28 -ml -g -o2 |
| Perennial C11 | 33418 | 0 | -vmsp -o3 -mf5 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu32 -ml -mt -ooff --symdebug:dwarf_version=4 --abi=eabi |
| Perennial C11 | 33017 | 0 | -ml -mt --float_support=fpu64 -o2 --symdebug:dwarf_version=4 --abi=eabi |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o3 -mf5 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o3 --rpt_threshold=80 |
| Perennial C11 | 33600 | 0 | -mv6 -o3 -mf5 -oi20 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -o3 -oi0 -mf0 |
| Perennial C11 | 33600 | 0 | -mv7M3 -o4 -mf2 --symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| Perennial C11 | 33418 | 0 | -vmspx -o3 -mf0 |
| Perennial C11 | 33600 | 0 | -mv5e --code_state=16 -o2 -g |
| Perennial C11 | 33598 | 0 | -mv7A8 --code_state=16 --neon -me --unaligned_access=off --float_support=vfpv3 -me -g -o2 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mi --abi=eabi |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o1 --abi=eabi |
| Perennial C11 | 33600 | 0 | -mv7m4 -o2 |
| Perennial C11 | 33598 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| Perennial C11 | 33418 | 0 | -vmspx --code_model=small --data_model=small -o1 -mf0 |
| Perennial C11 | 29793 | 0 | -v28 -ml -o1 |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -o1 -mf5 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi20 -mf5 |
| Perennial C11 | 33418 | 0 | -vmsp -o3 -mf0 |
| Perennial C11 | 33418 | 0 | -vmspx --code_model=small --data_model=small -o3 -mf0 |
| Perennial C11 | 33600 | 0 | -mv5e -o3 -oi0 -g |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mi |
| Perennial C11 | 33418 | 0 | -vmsp -o2 -mf5 --symdebug:dwarf_version=4 |

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| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -ooff --abi=eabi |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -g |
| Perennial C11 | 33428 | 0 | -vmssp --data_model=restricted -o2 -mf5 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o2 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g |
| Perennial C11 | 33600 | 0 | -mv4 -o2 -g --symdebug:dwarf_version=4 |
| Perennial C11 | 33579 | 0 | -mv7A8 --unaligned_access=off -me --neon -o3 -mf4 --plain_char=signed |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o1 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o3 |
| Perennial C11 | 33600 | 0 | -mv5e -g -o2 --symdebug:dwarf_version=4 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o2 |
| Perennial C11 | 33598 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| Perennial C11 | 33600 | 0 | -mv4 -o3 -oi0 -mf0 |
| Perennial C11 | 33600 | 0 | -mv5e --code_state=16 -o2 -g |
| Perennial C11 | 33428 | 0 | -vmssp --data_model=restricted -o1 -mf0 |
| Perennial C11 | 33600 | 0 | -mv6 -o3 -mf5 -oi20 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o1 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me -o2 |
| Perennial C11 | 33598 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| Perennial C11 | 33418 | 0 | -vmssp -o3 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7R4 --code_state=16 -o2 -mf2 -os |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 --rpt_threshold=80 --abi=eabi |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -g -mf3 |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -o3 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o2 -mf -g |
| Perennial C11 | 33428 | 0 | -vmssp --data_model=restricted -o1 |
| Perennial C11 | 33600 | 0 | -mv4 --code_state=16 -o2 -g |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o2 -g |
| Perennial C11 | 33600 | 0 | -mv6M0 --code_state=16 -g -o2 |
| Perennial C11 | 33598 | 0 | -mv7A8 --code_state=16 -neon -me --unaligned_access=off --float_support=vfpv3 -me -g -o2 |
| Perennial C11 | 33418 | 0 | -vmssp --code_model=small --data_model=small -o2 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o3 -oi20 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7M3 --code_state=16 -g -o2 |
| Perennial C11 | 33429 | 0 | -vmssp -ml -o3 -mf0 |

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| Perennial C11 | 33429 | 0 | -vmosp --data_model=large -g -o2 --symdebug:dwarf_version=4 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o1 |
| Perennial C11 | 33429 | 0 | -vmosp -ml -o2 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7M4 -o4 -mf4 -me --parallel=2 |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o1 -mf5 |
| Perennial C11 | 33418 | 0 | -vmosp -o3 -mf0 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o1 --symdebug:dwarf_version=4 --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o4 |
| Perennial C11 | 33600 | 0 | -mv4 --code_state=16 -o2 -mf3 -g --symdebug:dwarf_version=4 |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -g --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o3 |
| Perennial C11 | 33600 | 0 | -mv5e -o2 -g -me |
| Perennial C11 | 33600 | 0 | -mv5e -o2 -g -me |
| Perennial C11 | 33418 | 0 | -vmosp --code_model=small --data_model=small -o3 -mf5 |
| Perennial C11 | 33418 | 0 | -vmosp -o1 --symdebug:dwarf_version=4 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o2 -g |
| Perennial C11 | 33600 | 0 | -mv6 --code_state=16 -o3 -oi0 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -g -mf3 |
| Perennial C11 | 29793 | 0 | -v28 -ml -mt -o2 -mi --symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv7R4 --code_state=16 -g -o2 |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o1 -mf5 |
| Perennial C11 | 33600 | 0 | -mv6 -o2 -g |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 -g --symdebug:dwarf_version=4 --abi=eabi |
| Perennial C11 | 33017 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -ms --abi=eabi |
| Perennial C11 | 33418 | 0 | -vmosp -o4 --symdebug:dwarf_version=4 --parallel=4 |
| Perennial C11 | 33600 | 0 | -mv7R4 -g -o2 |
| Perennial C11 | 33598 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o2 -mf -g |
| Perennial C11 | 33600 | 0 | -mv7M3 -o4 -mf2 --symdebug:dwarf_version=4 --parallel=2 |
| Perennial C11 | 33418 | 0 | -vmosp -o1 -mf0 |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o3 -mf5 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g |
| Perennial C11 | 33428 | 0 | -vmosp --data_model=restricted -o3 -mf0 --symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv4 -o3 -oi0 -mf0 |
| Perennial C11 | 33418 | 0 | -vmosp -o2 -mf4 |

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| Perennial C11 | 33600 | 0 | -mv5e --code_state=16 -o3 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -g |
| Perennial C11 | 33428 | 0 | -vmspx --data_model=restricted -g -o2 |
| | | | -vmspx --code_model=small --data_model=small -o3 - |
| Perennial C11 | 33418 | 0 | mf5 |
| Perennial C11 | 33418 | 0 | -vmosp -o2 -mf5 |
| | | | -v28 --float_support=fpu32 -ml -mt -o2 -- |
| Perennial C11 | 29793 | 0 | rpt_threshold=80 |
| | | | -vmspx --data_model=restricted -o4 -- |
| Perennial C11 | 33428 | 0 | symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv4 --code_state=16 -o2 -g |
| | | | -vmspx --code_model=small --data_model=small -o2 - |
| Perennial C11 | 33418 | 0 | mf0 |
| Perennial C11 | 33600 | 0 | -mv7R4 -g -o2 |
| Perennial C11 | 33418 | 0 | -vmosp -o1 -mf5 |
| Perennial C11 | 33418 | 0 | -vmosp -o2 -mf0 |
| Perennial C11 | 33600 | 0 | -mv7M3 --code_state=16 -o3 -mf -oi20 |
| | | | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| Perennial C11 | 33598 | 0 | -neon --float_support=vfpv3 -o3 -mf |
| | | | -v28 --float_support=fpu32 -ml -mt -o0 -g -- |
| Perennial C11 | 29793 | 0 | symdebug:dwarf_version=4 |
| Perennial C11 | 33600 | 0 | -mv5e -g -o2 |
| Perennial C11 | 33600 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| | | | -mv7A8 --unaligned_access=off -me --neon -o3 -mf4 -- |
| Perennial C11 | 33579 | 0 | plain_char=signed |
| Perennial C11 | 33600 | 0 | -mv5e -o3 -oi0 -g |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -ms |
| Perennial C11 | 33418 | 0 | -vmosp -g -o2 |
| Perennial C11 | 33429 | 0 | -vmspx -ml -o2 |
| Perennial C11 | 29793 | 0 | -v28 --float_support=fpu32 -ml -mt -o1 |
| Perennial C11 | 33418 | 0 | -vmspx -o3 |
| | | | -vmspx --data_model=large -g -o2 -- |
| Perennial C11 | 33429 | 0 | symdebug:dwarf_version=4 |
| | | | -mv7M4 --code_state=16 -o3 -- |
| Perennial C11 | 33600 | 0 | embedded_constants=off |
| Perennial C11 | 33418 | 0 | -vmosp -o3 |
| Perennial C90 | 9111 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 -g |
| Perennial C90 | 9239 | 0 | -vmspx -o3 -mf0 |
| Perennial C90 | 9239 | 0 | --symdebug:dwarf_version=4 |
| Perennial C90 | 9278 | 0 | -mv5e --float_support=vfpv2 -me -o1 |
| | | | -v28 --float_support=fpu32 -ml -mt -o1 -- |
| Perennial C90 | 9111 | 0 | symdebug:dwarf_version=4 |
| Perennial C90 | 9111 | 0 | -v28 -ml -g -o2 --symdebug:dwarf_version=4 |
| | | | -v28 --float_support=fpu32 -ml -mt -o2 -ms -- |
| Perennial C90 | 9111 | 0 | abi=eabi |
| Perennial C90 | 9111 | 0 | -v28 -ml -o2 -g |
| Perennial C90 | 9111 | 0 | -ml -mt --float_support=fpu32 -o2 |
| Perennial C90 | 9111 | 0 | -ml -mt -o2 |
| Perennial C90 | 9111 | 0 | -v28 -ml -mt -o1 --symdebug:dwarf_version=4 |
| Perennial C90 | 9111 | 0 | -v28 --float_support=fpu32 -ml -o3 -mf |
| | | | -ml -mt --float_support=fpu32 -o2 -- |
| Perennial C90 | 9111 | 0 | symdebug:dwarf_version=4 |
| | | | -v28 --float_support=fpu64 -ml -o3 -mf --abi=eabi -- |
| Perennial C90 | 9111 | 0 | c11 |

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| Perennial C90 | 9111 | 0 | -v28 -ml -o1 --symdebug:dwarf_version=4 |
| | | | -mv6 --code_state=16 -o3 -oi0 -- |
| Perennial C90 | 9280 | 0 | symdebug:dwarf_version=4 |
| Perennial C90 | 9111 | 0 | -v28 --float_support=fpu32 -ml -mt -ooff |
| Perennial C90 | 9280 | 0 | -mv7m4 -o2 --symdebug:dwarf_version=4 |
| | | | -v28 -ml -mt -o1 --symdebug:dwarf_version=4 -- |
| Perennial C90 | 9111 | 0 | abi=eabi |
| Perennial C90 | 9111 | 0 | -v28 -ml -mt -o2 -mi |
| Perennial C90 | 9280 | 0 | -mv4 --code_state=16 -o2 -mf3 -g |
| Perennial C99 | 28853 | 0 | -vmsex -ml -o3 |
| Perennial C99 | 28853 | 0 | -vmsex -ml -o3 -mf5 |
| Perennial C99 | 29013 | 0 | -mv7R4 -o0 --symdebug:none -s |
| Perennial C99 | 28464 | 0 | -v28 --float_support=fpu64 -ml -o3 --abi=eabi |
| Perennial C99 | 29011 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| PHIL Regression | | | |
| Testing | 1243 | 0 | |
| PHIL Regression | | | -mv7A8 --unaligned_access=off -me -- |
| Testing | 123 | 0 | float_support=vfpv3 -o2 -g |
| PHIL Regression | | | |
| Testing | 126 | 0 | -v28 --abi=eabi |
| PHIL Regression | | | |
| Testing | 124 | 0 | -mv6 --code_state=16 -o2 -g |
| PHIL Regression | | | |
| Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -g |
| PHIL Regression | | | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| Testing | 124 | 0 | o3 -oi0 |
| PHIL Regression | | | |
| Testing | 126 | 0 | -mv7R4 -o1 |
| PHIL Regression | | | |
| Testing | 92 | 0 | -vmsex -ml -o2 -mf0 |
| PHIL Regression | | | |
| Testing | 124 | 0 | -v28 --float_support=fpu32 -o2 -mt -mf5 |
| PHIL Regression | | | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| Testing | 123 | 0 | -neon -o3 -mf3 |
| PHIL Regression | | | |
| Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - |
| PHIL Regression | | | -neon --float_support=vfpv3 -o2 -g |
| PHIL Regression | | | |
| Testing | 122 | 0 | -v28 -mt -o2 -mf3 --symdebug:dwarf_version=4 |
| PHIL Regression | | | |
| Testing | 123 | 1 | -mv4 -o1 -ms --symdebug:dwarf_version=4 |
| PHIL Regression | | | -o3 -g -vmsex --code_model=small -- |
| Testing | 89 | 0 | data_model=small --pack_all_structs |
| PHIL Regression | | | |
| Testing | 92 | 0 | -vmsex -ml -o3 |
| PHIL Regression | | | |
| Testing | 91 | 0 | -vmsex --data_model=restricted -o2 |
| PHIL Regression | | | |
| Testing | 91 | 0 | -vmsex --data_model=restricted -o2 -mf5 |
| PHIL Regression | | | |
| Testing | 232 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi |
| PHIL Regression | | | |
| Testing | 128 | 0 | -mv7R4 -o3 -mf2 |
| PHIL Regression | | | |
| Testing | 89 | 0 | -vmsex -o3 -mf5 |

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| PHIL Regression Testing | 128 | 0 | -mv7R4 --code_state=16 -o2 -g |
| PHIL Regression Testing | 123 | 0 | -mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 |
| PHIL Regression Testing | 128 | 0 | -mv7R4 --code_state=16 -o3 -mf4 -oi20 -g --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 233 | 0 | -v28 --float_support=fpu64 -mt -mf3 -o2 --abi=eabi |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -mf0 |
| PHIL Regression Testing | 89 | 0 | -o2 -g -vmspx --code_model=small --data_model=small |
| PHIL Regression Testing | 91 | 0 | -vmspx --data_model=restricted -o3 -mf5 |
| PHIL Regression Testing | 89 | 0 | -o1 -g -vmspx --pack_all_structs |
| PHIL Regression Testing | 125 | 0 | -mv5e --float_support=vfpv2 -me -o3 |
| PHIL Regression Testing | 124 | 0 | -mv7r4 -o4 -mf --parallel=4 |
| PHIL Regression Testing | 230 | 0 | -v28 --float_support=fpu32 -mt --abi=eabi |
| PHIL Regression Testing | 91 | 0 | -vmspx --data_model=restricted -o3 -mf0 |
| PHIL Regression Testing | 89 | 0 | -vmspx -o3 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 125 | 0 | -mv6 -o3 -oi20 -mf3 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 125 | 0 | -mv5e --code_state=16 -o3 -oi0 |
| PHIL Regression Testing | 124 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -mf0 -oi0 |
| PHIL Regression Testing | 89 | 0 | -vmspx --code_model=small --data_model=small -o3 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 88 | 0 | -vmsp -g -o2 |
| PHIL Regression Testing | 123 | 0 | -v28 --float_support=fpu32 -mt --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 89 | 0 | -vmspx -o3 -mf0 |
| PHIL Regression Testing | 121 | 1 | -mv7A8 --unaligned_access=off -me --neon -o0 -ms -mf0 |
| PHIL Regression Testing | 277 | 0 | |
| PHIL Regression Testing | 89 | 0 | -o2 -g -vmspx --data_model=small |
| PHIL Regression Testing | 7135 | 0 | |
| PHIL Regression Testing | 92 | 0 | -vmspx -ml -o1 |
| PHIL Regression Testing | 89 | 0 | -o2 -g -vmspx |
| PHIL Regression Testing | 124 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |

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| PHIL Regression Testing | 128 | 0 | -mv7M3 --code_state=16 -o2 -mv7R4 -me --code_state=16 -- |
| PHIL Regression Testing | 128 | 0 | float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| PHIL Regression Testing | 127 | 1 | -mv7M4 --code_state=16 -me -o3 -mf2 --float_support=none --enum_type=int -g |
| PHIL Regression Testing | 126 | 0 | -mv6 -o3 -oi0 -mf0 |
| PHIL Regression Testing | 2321 | 0 | |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 230 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu32 -ooff -mf3 |
| PHIL Regression Testing | 89 | 0 | -vmspx -o2 -mf0 |
| PHIL Regression Testing | 89 | 0 | -vmspx --code_model=small --data_model=small -o1 |
| PHIL Regression Testing | 121 | 1 | -mv7A8 --unaligned_access=off -me -o1 -ms |
| PHIL Regression Testing | 127 | 0 | -mv7R4 -o2 -g |
| PHIL Regression Testing | 125 | 0 | -v28 --tmu_support=tmu0 -mt -o3 --fp_mode=relaxed |
| PHIL Regression Testing | 122 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o0 |
| PHIL Regression Testing | 124 | 0 | -mv4 --code_state=16 -o2 |
| PHIL Regression Testing | 124 | 0 | -v28 --float_support=fpu32 -mt -mf3 -o2 |
| PHIL Regression Testing | 92 | 0 | -vmspx -ml -o3 -mf5 |
| PHIL Regression Testing | 129 | 0 | -mv7R4 -o3 -oi0 --code_state=16 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 92 | 0 | -vmspx -ml -o2 -mf5 |
| PHIL Regression Testing | 125 | 0 | -mv7r4 --float_support=vfpv3d16 -o2 -mf3 --pack_all_structs --unaligned_access=off |
| PHIL Regression Testing | 89 | 0 | -vmspx --code_model=small --data_model=small -o3 -mf0 |
| PHIL Regression Testing | 89 | 0 | -o2 -g -vmspx --data_model=small --pack_all_structs |
| PHIL Regression Testing | 124 | 0 | -mv4 --code_state=16 -o0 |
| PHIL Regression Testing | 92 | 0 | -o2 -g -vmspx -ml --pack_all_structs |
| PHIL Regression Testing | 89 | 0 | -vmspx -o0 |
| PHIL Regression Testing | 3500 | 0 | |
| PHIL Regression Testing | 125 | 0 | -mv5e -o3 -mf -oi25 --symdebug:dwarf_version=4 |

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| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o2 -g |
| PHIL Regression Testing | 123 | 0 | -mv5e --float_support=vfpv2 -me -o1 |
| PHIL Regression Testing | 233 | 0 | -v28 --float_support=fpu64 -o3 -mt --rpt_threshold=20 --abi=eabi |
| PHIL Regression Testing | 126 | 0 | -mv5e --pack_all_structs |
| PHIL Regression Testing | 233 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu64 -o3 -mf5 |
| PHIL Regression Testing | 125 | 0 | -mv6 -o2 -g |
| PHIL Regression Testing | 120 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o1 -mf2 --symdebug:none |
| PHIL Regression Testing | 91 | 0 | -vmbsp --data_model=restricted -o2 --align_for_power |
| PHIL Regression Testing | 233 | 0 | -v28 --float_support=fpu64 -o3 -mt -mf -mi --abi=eabi |
| PHIL Regression Testing | 233 | 0 | -v28 --float_support=fpu64 -o0 -mt -g --symdebug:dwarf_version=4 --abi=eabi |
| PHIL Regression Testing | 121 | 0 | -v28 |
| PHIL Regression Testing | 126 | 0 | -mv6 -o3 -g -os |
| PHIL Regression Testing | 124 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o3 |
| PHIL Regression Testing | 122 | 0 | -v28 -mt -o3 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 233 | 0 | -v28 --float_support=fpu64 -o2 -mt -mf5 --abi=eabi |
| PHIL Regression Testing | 124 | 0 | -mv7A8 --unaligned_access=off -me -o3 |
| PHIL Regression Testing | 125 | 0 | -mv7r4 --float_support=vfpv3d16 -o1 --pack_all_structs --unaligned_access=off |
| PHIL Regression Testing | 92 | 0 | -o2 -g -vmbsp -ml |
| PHIL Regression Testing | 92 | 0 | -vmbsp -ml -o3 -mf0 |
| PHIL Regression Testing | 91 | 0 | -o2 -g -vmbsp --data_model=restricted --pack_all_structs |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me -o2 -g |
| PHIL Regression Testing | 123 | 0 | -mv6M0 -g --opt_level=off |
| PHIL Regression Testing | 124 | 0 | -mv6M0 --code_state=16 -o1 -mf4 |
| PHIL Regression Testing | 127 | 0 | -mv7M3 -o4 -mf2 |
| PHIL Regression Testing | 125 | 0 | -mv5e -o2 -g |
| PHIL Regression Testing | 122 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o1 |

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| PHIL Regression Testing | 130 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 126 | 0 | -mv7M4 --code_state=16 -g |
| PHIL Regression Testing | 129 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 -mf1 |
| PHIL Regression Testing | 125 | 0 | -mv4 -o2 -g |
| PHIL Regression Testing | 89 | 0 | -vmbsp --code_model=small --data_model=small -o0 |
| PHIL Regression Testing | 124 | 0 | -v28 --float_support=fpu32 -o3 -mt --rpt_threshold=20 |
| PHIL Regression Testing | 91 | 0 | -vmbsp --data_model=restricted -o0 |
| PHIL Regression Testing | 91 | 0 | -vmbsp --data_model=restricted -o1 |
| PHIL Regression Testing | 125 | 0 | -mv7M4 -o4 -mf4 -me --parallel=2 |
| PHIL Regression Testing | 123 | 0 | -mv7a8 -me -o3 --neon --float_support=vfpv3 -mf5 --pack_all_structs --unaligned_access=off |
| PHIL Regression Testing | 89 | 0 | -vmbsp --code_model=small --data_model=small -o2 -mf0 |
| PHIL Regression Testing | 91 | 0 | -vmbsp --data_model=restricted -o2 -mf0 |
| PHIL Regression Testing | 124 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o3 -mf5 -oi20 |
| PHIL Regression Testing | 127 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| PHIL Regression Testing | 89 | 0 | -o2 -g -vmbsp --data_model=small --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 630 | 0 | |
| PHIL Regression Testing | 89 | 0 | -vmbsp --code_model=small --data_model=small -o3 -mf5 |
| PHIL Regression Testing | 124 | 0 | -mv5e --code_state=16 -o2 -g |
| PHIL Regression Testing | 124 | 0 | -v28 --float_support=fpu32 -o3 -mt -mf -mi |
| PHIL Regression Testing | 122 | 0 | -mv6 -o1 --symdebug:none |
| PHIL Regression Testing | 126 | 0 | -mv7M4 --code_state=16 -o1 -me |
| PHIL Regression Testing | 231 | 0 | -v28 --abi=eabi --idiv_support=idiv0 --float_support=fpu32 -mt -o1 -mf1 |
| PHIL Regression Testing | 124 | 0 | -mv4 -o2 -mf1 -me -rr5 -rr6 |
| PHIL Regression Testing | 122 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o1 |
| PHIL Regression Testing | 89 | 0 | -vmbsp -o2 -mf5 |

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| PHIL Regression Testing | 127 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| PHIL Regression Testing | 121 | 0 | -v28 -mt |
| PHIL Regression Testing | 124 | 0 | -mv5e --code_state=16 -o0 |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon -o2 -g |
| PHIL Regression Testing | 123 | 0 | -mv7A8 -o3 -mf -g --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 126 | 0 | -mv7r4 --float_support=vfpv3d16 -mf3 |
| PHIL Regression Testing | 232 | 0 | -v28 --float_support=fpu64 -mt --abi=eabi |
| PHIL Regression Testing | 92 | 0 | -o2 -g -vmSPX -ml --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 124 | 0 | -v28 --float_support=fpu32 -o0 -mt -g --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 124 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o3 -oi20 |
| PHIL Regression Testing | 232 | 0 | -v28 --tmu_support=tmu1 -mt -o3 --fp_mode=relaxed --abi=eabi |
| PHIL Regression Testing | 125 | 0 | -mv5e -o2 -g --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 123 | 0 | -v28 --float_support=fpu32 -mt |
| PHIL Regression Testing | 91 | 0 | -vmSPX --data_model=restricted -o3 |
| PHIL Regression Testing | 92 | 0 | -vmSPX -ml -o0 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 126 | 0 | -v28 -mt --abi=eabi |
| PHIL Regression Testing | 122 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o1 |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o2 -g |
| PHIL Regression Testing | 89 | 0 | -vmSPX --code_model=small --data_model=small -o2 -mf5 |
| PHIL Regression Testing | 126 | 0 | -mv5e -o3 -oi0 |
| PHIL Regression Testing | 127 | 0 | -mv7M3 --code_state=16 -o1 -mf4 --symdebug:dwarf_version=4 |
| PHIL Regression Testing | 232 | 0 | -v28 --float_support=fpu64 -mt --symdebug:dwarf_version=4 --abi=eabi |
| PHIL Regression Testing | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -mf1 |
| PHIL Regression Testing | 89 | 0 | -vmSPX -o1 |
| PHIL Regression Testing | 125 | 0 | -mv6M0 -o3 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M3 --code_state=16 -o1 -ms -os |
| Plum Hall ANSI/ISO C Conformance | 23392 | 0 | -vmSPX --code_model=small --data_model=small -o2 -g -mf0 |

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| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -ms |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmsex -ml -o1 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 23392 | 0 | -vmsex --code_model=small --data_model=small -o2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --code_state=16 -o2 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmsex -o2 -g -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -pm |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --code_state=16 -o3 -oi0 -me |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 --parallel=2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -o2 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 -o2 -mf0 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 -ml -o0 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 --code_state=16 -o2 |
| Plum Hall ANSI/ISO C Conformance | 21961 | 0 | -vmsex -o1 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmsex --data_model=restricted -o2 |
| Plum Hall ANSI/ISO C Conformance | 21658 | 0 | -vmsex --code_model=small --data_model=small -o2 -g -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o4 --symdebug:dwarf_version=4 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o2 -mf0 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -o4 -mf -mt |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -o2 -ms |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7A8 --unaligned_access=off -me -o0 |

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| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -o3 -mf5 -oi20 |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 --symdebug:none --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 --code_state=16 -o3 -mf2 --float_support=none --enum_type=int -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv6 --code_state=16 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv6M0 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o1 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmSPX -ml -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf3 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o2 -mi |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7M4 --code_state=16 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 -o4 -mf4 -me --symdebug:dwarf_version=4 --parallel=2 |
| Plum Hall ANSI/ISO C Conformance | 18060 | 0 | -vmSPX --code_model=small --data_model=small -o1 |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmSP -o2 -g -mf0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -mi --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Conformance | 23392 | 0 | -vmSPX --code_model=small --data_model=small -o2 -os |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 --code_state=16 -o2 -mf2 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -o1 -g --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv4 --code_state=16 -o0 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 --code_state=16 -o3 -oi0 |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o1 --plain_char=signed -ms -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 --float_support=fpv4spd16 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o3 -mf3 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmSPX -ml -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -o3 -g |

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| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --float_support=vfpv2 -me -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -o3 --symdebug:dwarf_version=4 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -o3 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -ms --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 --float_support=fpu32 -ml -mt --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf3 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 -o2 -me |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --code_state=16 -o1 -g --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off --pack_all_structs -mf5 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M3 -o4 -mf2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -o2 -mi --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o1 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --code_state=16 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -o0 -g |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt --float_support=fpu64 -o2 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 -ml -mt |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -o3 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -mf5 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt --float_support=fpu32 -o2 |

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| Plum Hall ANSI/ISO C Conformance | 25559 | 0 | -vmssp -o2 -mf0 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M3 --code_state=16 -o3 -mf -oi20 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 --code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 28870 | 0 | -mv5e -o0 -ms --plain_char=signed |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -vmssp --data_model=restricted -o0 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmssp -ml -o2 -mf0 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --code_state=16 -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -o3 -mf2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv6M0 --code_state=16 -o1 -ms -os |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --neon --code_state=16 -o2 -mf3 |
| Plum Hall ANSI/ISO C Conformance | 18060 | 0 | -vmssp --code_model=small --data_model=small -o0 |
| Plum Hall ANSI/ISO C Conformance | 23392 | 0 | -vmssp --code_model=small --data_model=small -o2 -mf0 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 -me --float_support=none -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o2 -ms -g |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --fp_reassoc=off |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o1 -g --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e -o2 -mf3 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -vmssp -ml -o0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -mi |
| Plum Hall ANSI/ISO C Conformance | 23400 | 0 | -vmssp --data_model=small -o2 |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7A8 -me --unaligned_access=off -g |
| Plum Hall ANSI/ISO C Conformance | 18060 | 0 | -vmssp --code_model=small --data_model=small -o1 -s |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt --float_support=fpu32 -o2 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmssp -o2 --pack_all_structs |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M3 --code_state=16 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -mv4 -o1 -me -ms --plain_char=signed |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -vmssp --data_model=large -o2 --pack_all_structs |

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| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 -me --unaligned_access=off -o3 -mf -oi25 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv5e --float_support=vfpv2 -me -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o4 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --rpt_threshold=20 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -pm --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7R4 --code_state=16 -o0 -s |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 -ml -mt -o0 |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -vmbsp --data_model=restricted -o2 --pack_all_structs |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmbsp --data_model=large -o2 |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -mv6 -o1 -me --plain_char=signed |
| Plum Hall ANSI/ISO C Conformance | 25559 | 0 | -vmbsp -o2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmbsp --data_model=restricted -o2 -mf0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -o2 -mf |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmbsp --data_model=restricted -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 --code_state=16 -o2 -g |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -vmbsp --data_model=restricted -o1 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 --fp_reassoc=off --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me -o3 -mf5 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -mf1 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o3 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmbsp -o2 -g -ss --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 -ml |
| Plum Hall ANSI/ISO C Conformance | 17612 | 0 | -vmbsp -o1 |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -float_support=vfpv3 -g |

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| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv7M4 --code_state=16 -g -me --embedded_constants=off |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 --code_state=16 -o2 -g -me |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 --code_state=16 -o1 |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmisp -o2 -os |
| Plum Hall ANSI/ISO C Conformance | 21210 | 0 | -vmisp -o2 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 -me -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -o2 -mf --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o3 --rpt_threshold=20 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -pm --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -me --code_state=16 --float_support=vfpv3d16 --unaligned_access=off -o3 -mf0 -oi0 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -o1 --symdebug:none |
| Plum Hall ANSI/ISO C Conformance | 28871 | 0 | -mv5e --float_support=vfpv2 -me -o0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt --float_support=fpu32 -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o3 -mf -pm |
| Plum Hall ANSI/ISO C Conformance | 23392 | 0 | -vmisp --code_model=small --data_model=small -o2 -g -ss |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv4 --code_state=16 -o3 -oi0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -o2 |
| Plum Hall ANSI/ISO C Conformance | 17612 | 0 | -vmisp -o1 -s |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --abi=eabi -ml -mt --idiv_support=idiv0 --float_support=fpu32 -o2 -mf1 |
| Plum Hall ANSI/ISO C Conformance | 25559 | 0 | -vmisp -o2 -mf5 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o1 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 --rpt_threshold=20 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 --parallel=2 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 17612 | 0 | -vmisp -o0 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Conformance | 28869 | 0 | -v28 --float_support=fpu64 -ml -mt --symdebug:dwarf_version=4 --abi=eabi |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 -ml -mt -o3 -mf |

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| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7R4 -o3 -mf3 |
| Plum Hall ANSI/ISO C Conformance | 29446 | 0 | -mv7A8 --unaligned_access=off -me --neon -o1 --plain_char=signed -ms |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv6 -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Conformance | 21385 | 0 | -vmssp -o0 |
| Plum Hall ANSI/ISO C Conformance | 29445 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -mf1 |
| Plum Hall ANSI/ISO C Conformance | 29447 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 -ml -mt --tmu_support=tmu0 --fp_mode=relaxed -o2 |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf3 --abi=eabi |
| Plum Hall ANSI/ISO C Stress Test | 28139 | 0 | -vmssp --data_model=large -o2 |
| Plum Hall ANSI/ISO C Stress Test | 28139 | 0 | -mv7A8 -me --code_state=16 -o3 -mf -oi20 |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf3 |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 -ml -mt -o3 -mf3 --symdebug:dwarf_version=4 |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 -ml -mt --tmu_support=tmu1 --fp_mode=relaxed -o2 --abi=eabi |
| Plum Hall ANSI/ISO C Stress Test | 29230 | 0 | -v28 -ml -mt --idiv_support=idiv0 --float_support=fpu64 -o3 --abi=eabi -mf4 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o2 -mf5 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o4 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o2 -ms --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -ms |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o3 -mf3 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o3 -g |
| Plum Hall C++03 | 2837 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o3 -mf |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o1 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -ms |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -g |
| Plum Hall C++03 | 2837 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o2 -ms |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o1 --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o0 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o2 -mf0 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o4 -mf --parallel=2 |

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| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o3 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -mf0 |
| Plum Hall C++03 | 2790 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -g |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o1 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -ms |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml --exceptions --rtti -o2 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -mf5 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o4 -ms --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -mi -mo |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o2 -g |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o3 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o3 --rpt_threshold=40 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o3 -mf3 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o0 -g |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o3 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml --exceptions --rtti -o0 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o1 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o2 -g |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o2 -ms --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -mf2 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti -o3 --rpt_threshold=40 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o0 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o4 |
| Plum Hall C++03 | 2837 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o4 -ms --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -mf |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -g |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -mf2 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -g |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o1 |
| Plum Hall C++03 | 2790 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt --exceptions --rtti |

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| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o2 -g -- rpt_threshold=80 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt -- exceptions --rtti -o3 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o3 --rpt_threshold=40 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o4 -mf --parallel=2 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o1 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt -- exceptions --rtti -o3 -mf3 |
| Plum Hall C++03 | 2790 | 0 | -v28 --float_support=fpu32 -ml -mt --exceptions --rtti -o2 -mf5 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o2 -mi |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt -- exceptions --rtti -o2 -mf0 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 |
| Plum Hall C++03 | 2837 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o4 -mf |
| Plum Hall C++03 | 2790 | 0 | -v28 --abi=eabi --float_support=fpu32 -ml -mt -- exceptions --rtti |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml -mt --exceptions --rtti -o3 -mf |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o2 |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi -ml -mt --exceptions --rtti -o3 -mi -mo |
| Plum Hall C++03 | 2842 | 0 | -v28 --abi=eabi --float_support=fpu64 -ml -mt -- exceptions --rtti -o2 --symdebug:dwarf_version=4 |
| Plum Hall C++03 | 2790 | 0 | -v28 -ml --exceptions --rtti -o3 -mf3 |
| Plum Hall C++14 | 4984 | 0 | -vmisp --rtti -o3 -mf5 |
| Plum Hall C++14 | 2840 | 0 | -vmispx -ml --rtti -o2 -os |
| Plum Hall C++14 | 4927 | 0 | -vmisp --rtti --exceptions -o2 -mf0 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - neon --float_support=vfpv3 -rtti --exceptions -- extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 4927 | 0 | -vmispx --code_model=small --data_model=small -- rtti --exceptions -o3 -mf0 |
| Plum Hall C++14 | 2878 | 0 | -mv5e --code_state=16 -rtti --exceptions -- extern_c_can_throw -o2 -mf2 -s |
| Plum Hall C++14 | 2876 | 0 | -vmispx -ml --rtti --exceptions -o1 |
| Plum Hall C++14 | 2876 | 0 | -vmispx -ml --rtti --exceptions -o2 -mf0 |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 --code_state=16 -rtti --exceptions -o3 -- embedded_constants=off |
| Plum Hall C++14 | 3103 | 0 | -vmispx --code_model=small --data_model=small -- rtti -o3 -mf0 |
| Plum Hall C++14 | 5631 | 0 | -vmisp --rtti --exceptions -o3 -mf5 |
| Plum Hall C++14 | 4826 | 0 | -vmispx --code_model=small --data_model=small -- rtti --exceptions -o1 -s |
| Plum Hall C++14 | 3123 | 0 | -vmisp --rtti -o2 -mf0 |
| Plum Hall C++14 | 2878 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -- extern_c_can_throw -o1 -os |
| Plum Hall C++14 | 2876 | 0 | -vmispx --rtti --exceptions -o1 |
| Plum Hall C++14 | 3815 | 0 | -vmisp --rtti -o0 |

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| Plum Hall C++14 | 3123 | 0 | -vmssp --rtti -o2 -os |
| Plum Hall C++14 | 2878 | 0 | -mv6 --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o2 -mf0 |
| Plum Hall C++14 | 2841 | 0 | -mv7M3 --code_state=16 -rtti -o3 -mf -oi20 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 3123 | 0 | -vmssp --code_model=small --data_model=small --rtti -o2 -os |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -o2 -mf0 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -o2 -mf5 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o1 |
| Plum Hall C++14 | 3608 | 0 | -vmssp --code_model=small --data_model=small --rtti -o0 |
| Plum Hall C++14 | 2878 | 0 | -mv5e -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 3608 | 0 | -vmssp --rtti -o1 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o2 -mf0 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 --code_state=16 -rtti --exceptions -g -me --embedded_constants=off |
| Plum Hall C++14 | 3231 | 0 | -vmssp --code_model=small --data_model=small --rtti -o3 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o2 -os |
| Plum Hall C++14 | 2841 | 0 | -mv7M4 -me --float_support=none -o2 |
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -o4 |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 --code_state=16 -rtti --exceptions -g |
| Plum Hall C++14 | 2841 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --neon --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 --plain_char=signed -ms |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o3 |
| Plum Hall C++14 | 4927 | 0 | -vmssp --code_model=small --data_model=small --rtti --exceptions -o2 -mf5 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -rtti --exceptions --extern_c_can_throw -o3 -mf -oi20 |
| Plum Hall C++14 | 4927 | 0 | -vmssp --rtti --exceptions -o2 -os --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o1 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o3 -mf5 |
| Plum Hall C++14 | 2876 | 0 | -vmssp -ml --rtti --exceptions -o2 -os |
| Plum Hall C++14 | 4397 | 0 | -vmssp --code_model=small --data_model=small --rtti --exceptions -o4 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 -rtti -o0 -g -os |
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -o3 -mf5 |
| Plum Hall C++14 | 2878 | 0 | -mv4 --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -mf1 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -o0 |

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| Plum Hall C++14 | 2840 | 0 | -vmosp -ml --rtti -o0 |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 -o4 -rtti --exceptions -mf4 -me |
| Plum Hall C++14 | 3103 | 0 | -vmosp --rtti -o4 |
| Plum Hall C++14 | 2952 | 0 | -vmosp --code_model=small --data_model=small --rtti -o4 |
| Plum Hall C++14 | 2876 | 0 | -vmosp -ml --rtti --exceptions -o0 |
| Plum Hall C++14 | 3123 | 0 | -vmosp --code_model=small --data_model=small --rtti -g -o2 |
| Plum Hall C++14 | 2840 | 0 | -vmosp --data_model=restricted --rtti -o4 |
| Plum Hall C++14 | 2876 | 0 | -vmosp -ml --rtti --exceptions -s |
| Plum Hall C++14 | 2840 | 0 | -vmosp --data_model=restricted --rtti -s |
| Plum Hall C++14 | 2878 | 0 | -mv4 -rtti --exceptions --extern_c_can_throw -o3 -mf -oi20 |
| Plum Hall C++14 | 2841 | 0 | -mv6M0 --code_state=16 -rtti -o3 -mf -oi20 |
| Plum Hall C++14 | 3608 | 0 | -vmosp --rtti -o3 -mf0 |
| Plum Hall C++14 | 2876 | 0 | -vmosp --rtti --exceptions -s |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o0 -g |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 -neon --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -g |
| Plum Hall C++14 | 2878 | 0 | -mv5e --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 -g |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 --code_state=16 -o2 |
| Plum Hall C++14 | 2878 | 0 | -mv6 -rtti --exceptions --extern_c_can_throw -o1 -mf0 |
| Plum Hall C++14 | 4984 | 0 | -vmosp --code_model=small --data_model=small --rtti -o3 -mf5 |
| Plum Hall C++14 | 2841 | 0 | -mv6M0 -o2 |
| Plum Hall C++14 | 2840 | 0 | -vmosp --rtti -o2 -os |
| Plum Hall C++14 | 3608 | 0 | -vmosp --code_model=small --data_model=small --rtti -o1 |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 --code_state=16 -rtti -o3 -mf0 -oi20 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2841 | 0 | -mv7M3 -rtti -o4 -mf2 |
| Plum Hall C++14 | 2841 | 0 | -mv7M3 --code_state=16 -rtti -o2 -g |
| Plum Hall C++14 | 2876 | 0 | -vmosp --data_model=restricted --rtti --exceptions -s |
| Plum Hall C++14 | 2876 | 0 | -vmosp --rtti --exceptions -o0 |
| Plum Hall C++14 | 2878 | 0 | -mv5e --code_state=16 -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 --code_state=16 --float_support=vfpv3d16 -me --unaligned_access=off -mf5 -o2 |
| Plum Hall C++14 | 2840 | 0 | -vmosp -ml --rtti -o3 |
| Plum Hall C++14 | 2952 | 0 | -vmosp --rtti --exceptions -o4 -mf5 --parallel=4 |
| Plum Hall C++14 | 4826 | 0 | -vmosp --code_model=small --data_model=small --rtti --exceptions -o1 |
| Plum Hall C++14 | 3837 | 0 | -vmosp --rtti -o2 -mf5 |
| Plum Hall C++14 | 2876 | 0 | -vmosp -ml --rtti --exceptions -o2 -mf5 |

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| Plum Hall C++14 | 2841 | 0 | -mv5e --float_support=vfpv2 -me -o2 -- symdebug:dwarf_version=4 |
| Plum Hall C++14 | 4503 | 0 | -vmisp --rtti --exceptions -o4 |
| Plum Hall C++14 | 2876 | 0 | -vmispx -ml --rtti --exceptions -o3 |
| Plum Hall C++14 | 2841 | 0 | -mv6M0 --code_state=16 -rtti -o2 -g |
| Plum Hall C++14 | 2840 | 0 | -vmispx --rtti -o1 |
| Plum Hall C++14 | 2878 | 0 | -mv6 --code_state=16 -rtti --exceptions -- extern_c_can_throw -o3 -oi0 -mf3 |
| Plum Hall C++14 | 2878 | 0 | -mv5e -rtti --exceptions --extern_c_can_throw -o2 - mf3 --symdebug:none |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 --code_state=16 -rtti --exceptions -o1 |
| Plum Hall C++14 | 4774 | 0 | -vmispx --code_model=small --data_model=small -- rtti --exceptions -o2 -mf0 |
| Plum Hall C++14 | 2840 | 0 | -vmispx -ml --rtti -o2 -mf0 |
| Plum Hall C++14 | 5086 | 0 | -vmisp --rtti --exceptions -o1 -s |
| Plum Hall C++14 | 2840 | 0 | -vmispx --rtti -s |
| Plum Hall C++14 | 2876 | 0 | -vmispx -ml --rtti --exceptions -o3 -mf0 |
| Plum Hall C++14 | 2840 | 0 | -vmispx --rtti -o3 -mf0 |
| Plum Hall C++14 | 2841 | 0 | -mv7A8 -me --unaligned_access=off -o3 -mf -oi25 |
| Plum Hall C++14 | 2878 | 0 | -mv5e -rtti --exceptions --extern_c_can_throw -o3 - mf1 -me --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2841 | 0 | -mv7M4 --code_state=16 -rtti -o3 -mf2 -- float_support=None -g |
| Plum Hall C++14 | 3759 | 0 | -vmispx --code_model=small --data_model=small -- rtti -o2 -mf5 |
| Plum Hall C++14 | 2840 | 0 | -vmispx --data_model=restricted --rtti -o0 |
| Plum Hall C++14 | 2840 | 0 | -vmispx -ml --rtti -o2 -mf5 |
| Plum Hall C++14 | 2840 | 0 | -vmispx --rtti -o3 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --neon -rtti -- exceptions --extern_c_can_throw -g |
| Plum Hall C++14 | 4774 | 0 | -vmispx --code_model=small --data_model=small -- rtti --exceptions -o2 -g |
| Plum Hall C++14 | 2840 | 0 | -vmispx --data_model=restricted --rtti -o3 |
| Plum Hall C++14 | 2876 | 0 | -vmispx --rtti --exceptions -o2 -os |
| Plum Hall C++14 | 2876 | 0 | -vmispx --data_model=restricted --rtti --exceptions - o2 -mf5 |
| Plum Hall C++14 | 2841 | 0 | -mv7M4 -me -o2 |
| Plum Hall C++14 | 2878 | 0 | -mv4 --code_state=16 -rtti --exceptions -- extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2876 | 0 | -vmispx -ml --rtti --exceptions -o4 -mf0 --parallel=2 |
| Plum Hall C++14 | 2877 | 0 | -mv7M4 --code_state=16 -rtti --exceptions -o3 -mf5 - me |
| Plum Hall C++14 | 3123 | 0 | -vmisp --rtti -g -o2 |
| Plum Hall C++14 | 2841 | 0 | -mv6 --code_state=16 -o2 -- symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2876 | 0 | -vmispx --data_model=restricted --rtti --exceptions - o2 -g |
| Plum Hall C++14 | 2841 | 0 | -mv7M3 --code_state=16 -o2 |
| Plum Hall C++14 | 2878 | 0 | -mv4 -rtti --exceptions --extern_c_can_throw -o3 - mf0 -oi0 |
| Plum Hall C++14 | 2841 | 0 | -mv5e -o2 |
| Plum Hall C++14 | 4927 | 0 | -vmisp --rtti --exceptions -o2 -g |
| Plum Hall C++14 | 2841 | 0 | -mv4 -o2 |

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| Plum Hall C++14 | 5086 | 0 | -vmssp --rtti --exceptions -o3 -mf0 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o2 -g |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o3 -mf0 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o2 -mf5 |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 -rtti -o2 -mf5 |
| Plum Hall C++14 | 2878 | 0 | -mv4 -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 3608 | 0 | -vmssp --code_model=small --data_model=small --rtti -s |
| Plum Hall C++14 | 3608 | 0 | -vmssp --rtti -o3 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -o4 |
| Plum Hall C++14 | 2841 | 0 | -mv7M3 --code_state=16 -rtti -o1 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o2 -mf0 |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 --code_state=16 -rtti -o2 -g |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o3 -mf5 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o3 |
| Plum Hall C++14 | 3123 | 0 | -vmssp --code_model=small --data_model=small --rtti -o2 -mf0 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --neon --code_state=16 -rtti --exceptions --extern_c_can_throw -o3 -mf5 |
| Plum Hall C++14 | 2841 | 0 | -mv7A8 --code_state=16 --neon -me --unaligned_access=off -o2 |
| Plum Hall C++14 | 5561 | 0 | -vmssp --code_model=small --data_model=small --rtti --exceptions -o3 -mf5 |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me -rtti --exceptions --extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -g -o2 |
| Plum Hall C++14 | 4927 | 0 | -vmssp --code_model=small --data_model=small --rtti --exceptions -o3 |
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -o3 -mf0 |
| Plum Hall C++14 | 2841 | 0 | -mv6M0 --code_state=16 -rtti -o1 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o3 -mf5 |
| Plum Hall C++14 | 5178 | 0 | -vmssp --rtti --exceptions -o3 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -g -o2 |
| Plum Hall C++14 | 2876 | 0 | -vmssp -ml --rtti --exceptions -o2 -g |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me --neon --float_support=vfpv3 -rtti --exceptions --extern_c_can_throw -o3 -mf4 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o2 -mf5 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o0 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions -o4 |
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -g -o2 |
| Plum Hall C++14 | 4774 | 0 | -vmssp --code_model=small --data_model=small --rtti --exceptions -o2 -os |
| Plum Hall C++14 | 2841 | 0 | -mv7M4 --float_support=fpv4spd16 -o2 --symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2878 | 0 | -mv4 --code_state=16 -rtti --exceptions --extern_c_can_throw -o1 |

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|-----------------|------|---|--|
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -o1 |
| Plum Hall C++14 | 2841 | 0 | -mv7A8 -me --unaligned_access=off -g |
| Plum Hall C++14 | 5086 | 0 | -vmssp --rtti --exceptions -o1 |
| Plum Hall C++14 | 2841 | 0 | -mv5e --code_state=16 -o2 |
| Plum Hall C++14 | 5226 | 0 | -vmssp --rtti --exceptions -o2 -mf5 |
| Plum Hall C++14 | 2840 | 0 | -vmssp --rtti -o3 -mf5 |
| Plum Hall C++14 | 2876 | 0 | -vmssp -ml --rtti --exceptions -o3 -mf5 -- symdebug:dwarf_version=4 |
| Plum Hall C++14 | 2878 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -- extern_c_can_throw -o2 -g |
| Plum Hall C++14 | 2878 | 0 | -mv5e --float_support=vfpv2 -me -rtti --exceptions -- extern_c_can_throw -o3 -mf -oi20 |
| Plum Hall C++14 | 2841 | 0 | -mv7R4 -rtti -o2 -g |
| Plum Hall C++14 | 2878 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -rtti --exceptions -- extern_c_can_throw -o3 -mf5 -oi20 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --rtti --exceptions -o3 -mf0 |
| Plum Hall C++14 | 2878 | 0 | -mv5e -o4 -mf4 -rtti --exceptions -- extern_c_can_throw --parallel=2 |
| Plum Hall C++14 | 2876 | 0 | -vmssp --data_model=restricted --rtti --exceptions - o2 -os |
| Plum Hall C++14 | 2840 | 0 | -vmssp --data_model=restricted --rtti -o3 -mf0 |
| Plum Hall C++14 | 2840 | 0 | -vmssp -ml --rtti -s |
| simplelink_sdk | 305 | 0 | |
| simplelink_sdk | 84 | 0 | |
| simplelink_sdk | 417 | 0 | |
| simplelink_sdk | 79 | 0 | |
| simplelink_sdk | 148 | 0 | |
| simplelink_sdk | 27 | 0 | |
| simplelink_sdk | 111 | 0 | |
| simplelink_sdk | 101 | 0 | |
| simplelink_sdk | 276 | 0 | |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o0 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 --abi=eabi -- parallel=2 |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -ms |
| STL Port | 217 | 0 | -v28 -ml -mt -o2 --rpt_threshold=80 |
| STL Port | 197 | 0 | -mv7M3 --code_state=16 -o2 -mf3 -os |
| STL Port | 217 | 0 | -v28 -ml -o3 -mf3 --abi=eabi |
| STL Port | 217 | 0 | -v28 -ml -mt -o1 |
| STL Port | 197 | 0 | -vmssp -ml -o2 -mf5 |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -g -me -- embedded_constants=off |
| STL Port | 197 | 0 | -mv7R4 -o1 --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -vmssp --data_model=restricted -o3 |
| STL Port | 197 | 0 | -mv7M3 -o4 -mf2 --parallel=2 |
| STL Port | 217 | 0 | -v28 -ml -o3 -mf3 |
| STL Port | 216 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -mf |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 --abi=eabi -- parallel=2 |
| STL Port | 217 | 0 | -v28 -ml -mt -o4 -ms |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o4 |
| STL Port | 217 | 0 | -v28 -ml -mt -o2 --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -mv4 --code_state=16 -o1 -mf3 -s |
| STL Port | 197 | 0 | -mv7R4 -o3 |

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| STL Port | 197 | 0 | -mv6M0 --code_state=16 -o2 -mf3 -os |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o2 -mf0 |
| STL Port | 197 | 0 | -vmspx -o3 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o4 --abi=eabi |
| STL Port | 197 | 0 | -vmspx -o3 -mf0 |
| STL Port | 197 | 0 | -vmspx -ml -o0 |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o3 -mf0 |
| STL Port | 197 | 0 | -mv5e --float_support=vfpv2 -me -o3 -mf |
| STL Port | 197 | 0 | -mv7R4 --code_state=16 -o3 |
| STL Port | 197 | 0 | -vmspx -o2 -mf5 |
| STL Port | 216 | 0 | -v28 --float_support=fpu32 -ml -mt -o2 -ms |
| STL Port | 197 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -o2 |
| STL Port | 197 | 0 | -mv6 -o2 -g --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o3 -mf5 -g -- symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -mv7A8 --neon -me --unaligned_access=off -o2 |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o4 -mf5 --parallel=2 |
| STL Port | 197 | 0 | -mv7A8 --unaligned_access=off -me --neon -o2 -o3 - mf -oi20 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mf -- abi=eabi |
| STL Port | 197 | 0 | -mv5e --code_state=16 -o2 -g |
| STL Port | 217 | 0 | -v28 -ml -mt -o3 -mf |
| STL Port | 217 | 0 | -v28 -ml -mt -o2 -ms |
| STL Port | 197 | 0 | -mv4 --code_state=16 -o3 -mf -oi20 -g |
| STL Port | 197 | 0 | -vmspx -ml -o1 |
| STL Port | 182 | 0 | -mv6 --code_state=16 -o0 -ms -ss |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -g -- symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -mv5e --code_state=16 -o3 -me -ms -os |
| STL Port | 197 | 0 | -mv7M3 --code_state=16 -o3 -oi0 |
| STL Port | 197 | 0 | -vmspx -o4 |
| STL Port | 197 | 0 | -mv5e -o2 -g |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o2 -- symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -vmspx --data_model=large -o2 |
| STL Port | 197 | 0 | -mv5e -o3 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -ooff --abi=eabi |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o2 -mf5 |
| STL Port | 197 | 0 | -vmspx -o2 --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me |
| STL Port | 217 | 0 | -v28 -ml -mt -o2 -mi |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -mi -- symdebug:dwarf_version=4 --abi=eabi |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mi -- symdebug:dwarf_version=4 |
| STL Port | 217 | 0 | -v28 --float_support=fpu32 -ml -mt -o3 -mf |
| STL Port | 197 | 0 | -vmspx -o1 |
| STL Port | 197 | 0 | -mv7m4 --float_support=fpv4spd16 -o2 -- symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -vmspx --data_model=restricted -o1 |

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| STL Port | 217 | 0 | -v28 -ml -mt -o3 -mf3 --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -mv7R4 -o2 -g |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o0 --abi=eabi |
| STL Port | 197 | 0 | -mv7R4 --code_state=16 -o2 -g |
| STL Port | 197 | 0 | -mv5e --float_support=vfpv2 -me -o0 |
| STL Port | 197 | 0 | -mv4 -o3 -oi0 -mf1 -g |
| STL Port | 217 | 0 | -v28 -ml -o2 |
| STL Port | 197 | 0 | -mv5e --code_state=16 -o3 -mf3 |
| STL Port | 217 | 0 | -v28 -ml -mt -o2 --symdebug:dwarf_version=4 --abi=eabi |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -o3 -mf2 --float_support=none -g |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -mf --abi=eabi |
| STL Port | 197 | 0 | -vmssp -o0 |
| STL Port | 197 | 0 | -mv7A8 --unaligned_access=off -me -o3 -oi0 -mf0 |
| STL Port | 197 | 0 | -mv4 -o2 -g |
| STL Port | 197 | 0 | -mv7M3 --code_state=16 -o2 |
| STL Port | 197 | 0 | -vmssp --data_model=restricted -o0 |
| STL Port | 197 | 0 | -mv7M4 -o4 -mf4 -me |
| STL Port | 217 | 0 | -v28 -ml -mt -o4 |
| STL Port | 197 | 0 | -mv6 -o3 -mf1 -g |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -o1 |
| STL Port | 197 | 0 | -vmssp -ml -o3 -mf5 |
| STL Port | 217 | 0 | -v28 -ml -mt -o3 --symdebug:none |
| STL Port | 197 | 0 | -mv4 --code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| STL Port | 197 | 0 | -vmssp --data_model=small -o2 |
| STL Port | 197 | 0 | -vmssp -ml -o2 |
| STL Port | 197 | 0 | -vmssp -ml -o3 -mf0 -g |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -o2 --abi=eabi |
| STL Port | 197 | 0 | -mv5e --symdebug:none -s |
| STL Port | 197 | 0 | -vmssp -o4 -mf5 |
| STL Port | 197 | 0 | -vmssp -o3 -mf5 -g |
| STL Port | 197 | 0 | -vmssp -ml -o4 -mf0 |
| STL Port | 197 | 0 | -vmssp -ml -o2 -mf0 |
| STL Port | 216 | 0 | -v28 --float_support=fpu32 -ml -mt -ooff |
| STL Port | 197 | 0 | -vmssp -ml -o3 |
| STL Port | 197 | 0 | -vmssp -o2 -mf0 |
| STL Port | 217 | 0 | -v28 -ml -o1 --symdebug:dwarf_version=4 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o3 -ms --abi=eabi |
| STL Port | 197 | 0 | -mv7M4 --code_state=16 -o3 --embedded_constants=off |
| STL Port | 217 | 0 | -v28 -ml -o3 |
| STL Port | 197 | 0 | -mv7R4 --code_state=16 -o2 -mf2 |
| STL Port | 217 | 0 | -v28 --float_support=fpu64 -ml -mt -o2 -ms --abi=eabi |
| TUEV | 123 | 0 | -mv5e --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7R4 --plain_char=signed --symdebug:dwarf_version=4 |

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| TUEV | 123 | 0 | -mv7R4 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7M4 --code_state=16 -g -me -- embedded_constants=off |
| TUEV | 123 | 0 | -mv7A8 --code_state=16 --neon -- float_support=vfpv3 -me --unaligned_access=off -- plain_char=signed |
| TUEV | 123 | 0 | -mv4 --plain_char=signed |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -- float_support=vfpv3 -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv6 --code_state=16 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -- float_support=vfpv3 --code_state=16 -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7M3 --code_state=16 --plain_char=signed |
| TUEV | 123 | 0 | -mv7R4 -DBIG_ENDIAN -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -o2 -g |
| TUEV | 123 | 0 | -mv4 -DBIG_ENDIAN -o2 -g -ss -al -mv7M4 --code_state=16 -o3 -mf2 -- float_support=none --enum_type=int -g - DBIG_ENDIAN |
| TUEV | 123 | 0 | -mv7M4 --code_state=16 -o1 -DBIG_ENDIAN |
| TUEV | 123 | 0 | -mv7A8 --neon -me --unaligned_access=off -- plain_char=signed |
| TUEV | 123 | 0 | -mv7R4 -me --code_state=16 -- float_support=vfpv3d16 --unaligned_access=off -o2 - g |
| TUEV | 123 | 0 | -mv4 --code_state=16 --plain_char=signed -- symdebug:dwarf_version=4 |
| TUEV | 123 | 0 | -mv7R4 --code_state=16 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv5e -DBIG_ENDIAN -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv5e -DBIG_ENDIAN -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7M3 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al -mv7A8 --unaligned_access=off -me --neon -- |
| TUEV | 123 | 0 | code_state=16 -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv6M0 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7M4 --code_state=16 -g -DBIG_ENDIAN |
| TUEV | 123 | 0 | -mv7R4 -DBIG_ENDIAN -o2 -g -ss -al -mv7A8 --unaligned_access=off -me --neon -- |
| TUEV | 123 | 0 | float_support=vfpv3 -o2 -g |
| TUEV | 123 | 0 | -mv5e -DBIG_ENDIAN -o2 -g -mv7m4 --float_support=fpv4spd16 -- |
| TUEV | 123 | 0 | plain_char=signed -mv4 --code_state=16 -DBIG_ENDIAN -o2 -g -ss -al -- |
| TUEV | 123 | 0 | symdebug:dwarf_version=4 -mv7A8 --unaligned_access=off -me -- |
| TUEV | 123 | 0 | float_support=vfpv3 --code_state=16 -o2 -g |
| TUEV | 123 | 0 | -mv7R4 --code_state=16 --plain_char=signed |
| TUEV | 123 | 0 | -mv4 --code_state=16 -DBIG_ENDIAN -o2 -g |

| | | | |
|------|-----|---|--|
| TUEV | 123 | 0 | -mv7M4 --code_state=16 -o3 -mf5 -me -- symdebug:dwarf_version=4 |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me -- float_support=vfpv3 -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv7R4 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me --neon -- code_state=16 -o2 -g --symdebug:dwarf_version=4 |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv7M3 --code_state=16 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv5e --float_support=vfpv2 -me -o2 -g |
| TUEV | 123 | 0 | -mv7M4 --code_state=16 -o3 -- embedded_constants=off -DBIG_ENDIAN |
| TUEV | 123 | 0 | -mv5e --code_state=16 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv6 -DBIG_ENDIAN -o2 -g -ss -al |
| TUEV | 123 | 0 | -mv4 -DBIG_ENDIAN -o3 -mf -g -ss -al |
| TUEV | 123 | 0 | -mv5e --float_support=vfpv2 -me -o3 -mf -g -ss -al -- symdebug:dwarf_version=4 |
| TUEV | 123 | 0 | -mv4 -DBIG_ENDIAN -o2 -g |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me -o2 -g |
| TUEV | 123 | 0 | -mv7A8 --unaligned_access=off -me --code_state=16 - o2 -g |
| TUEV | 123 | 0 | -mv7R4 -me --code_state=16 -- float_support=vfpv3d16 --unaligned_access=off -o3 - mf0 -oi0 |
| TUEV | 123 | 0 | -mv6 -DBIG_ENDIAN -o3 -mf -g -ss -al |