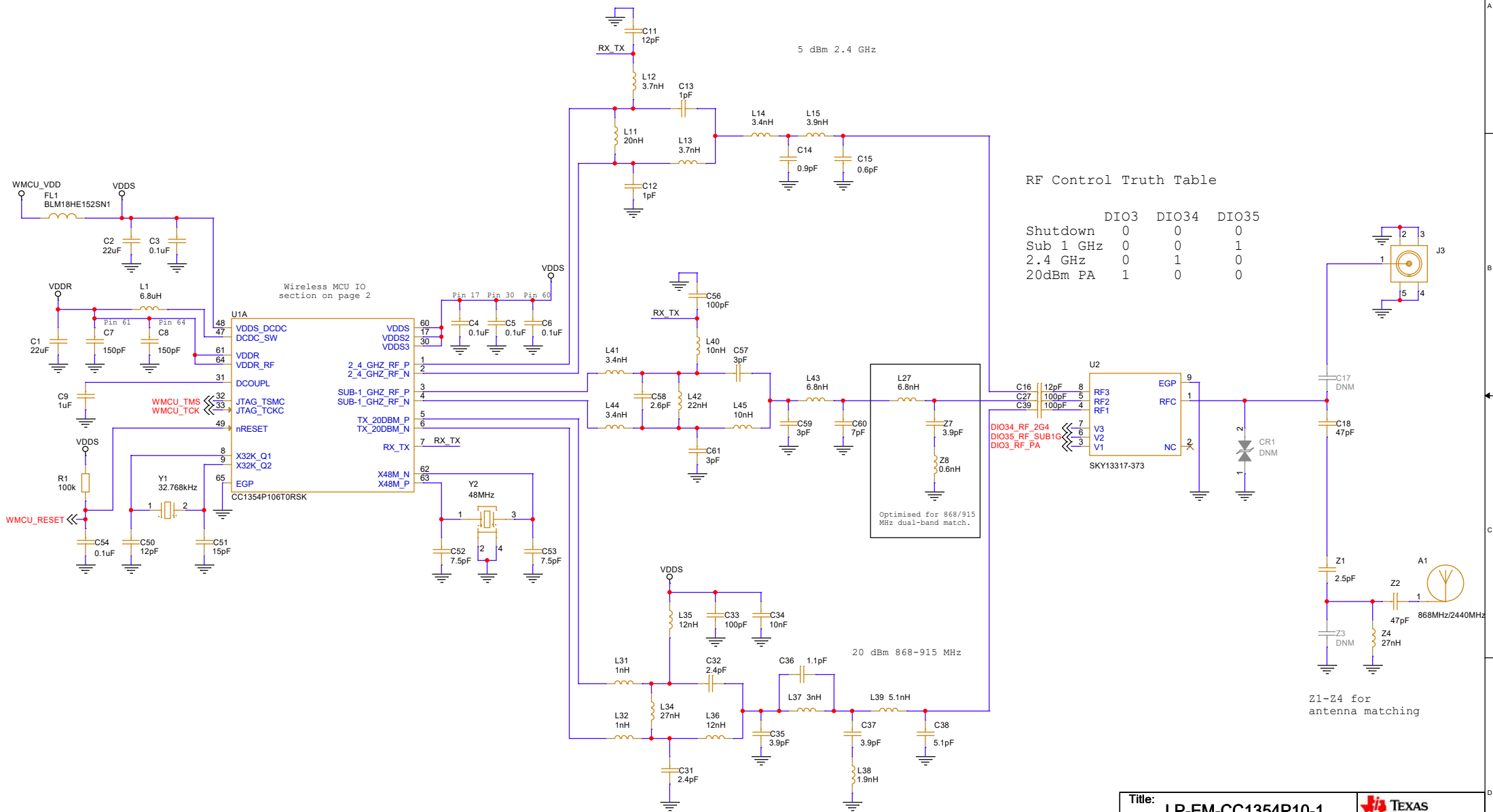


Wireless MCU RF

Wireless MCU IO block placed on page 2



RF Control Truth Table

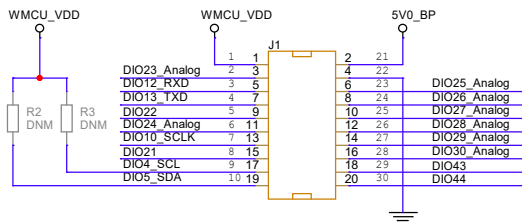
	DIO3	DIO34	DIO35
Shutdown	0	0	0
Sub 1 GHz	0	0	1
2.4 GHz	0	1	0
20dBm PA	1	0	0

Refer to App Note SWRA640
for more information on 868 MHz
and 915 MHz single band
matching networks

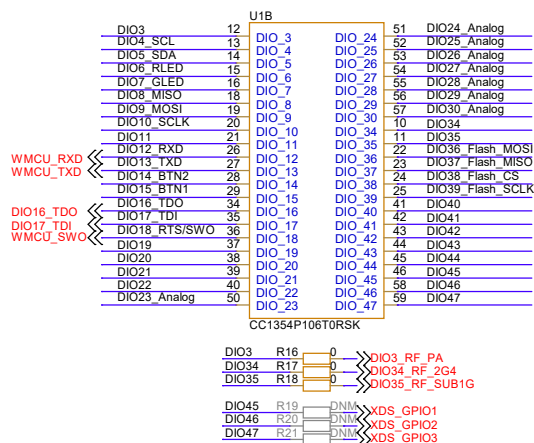
<https://www.ti.com/lit/swra640>

Title: LP-EM-CC1354P10-1		TEXAS INSTRUMENTS	
Drawn: RGW	PN: MCU087		
Checked: ZDC			
Size: A3	Rev: A	Sheet: 1 of 4	
Date:	Monday, June 12, 2023		

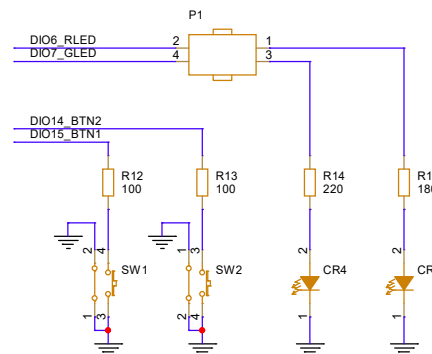
BoosterPack Headers and Peripherals



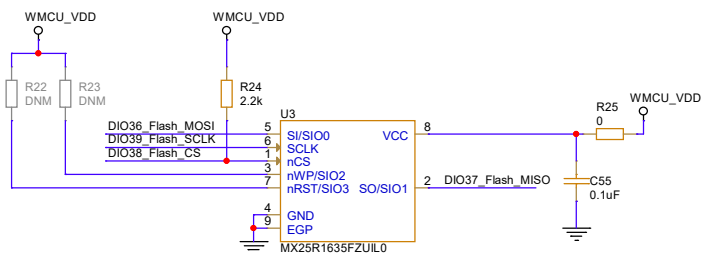
Wireless MCU IO block



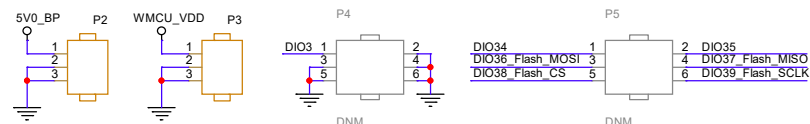
Buttons / LEDs



External flash



Extra headers




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Checked: ZDC			
Size: A3	Rev: A	Sheet: 2 of 4	
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Pin connection diagram for J4 connector:

- Pin 1: XDS_GPIO1
- Pin 2: XDS_GPIO2
- Pin 3: XDS_GPIO3
- Pin 4: WMCU_SWO
- Pin 5: XDS_BoardID_SCL
- Pin 6: XDS_BoardID_SDA
- Pin 7: WMCU_TMS
- Pin 8: WMCU_TCK
- Pin 9: WMCU_RESET
- Pin 10: WMCU_TXD
- Pin 11: WMCU_RXD
- Pin 12: 5V0_BP
- Pin 13: WMCU_VDD
- Pin 14: (unconnected)
- Pin 15: (unconnected)
- Pin 16: (unconnected)
- Pin 17: (unconnected)
- Pin 18: (unconnected)
- Pin 19: DIO16_TDO
- Pin 20: DIO17_TDI

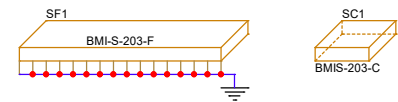
The 'DEBUG' section of the schematic shows the connection of a debugger to the microcontroller. It includes the following components and connections:

- WMCU_VDD**: A power supply pin connected to the debugger's VDD pin (pin 1) and the microcontroller's VDD pin (pin 1).
- P6**: A 10-pin connector that interfaces between the debugger and the microcontroller. The debugger pins are connected to the microcontroller pins as follows:
 - Debugger pin 1 to P6 pin 1
 - Debugger pin 2 to P6 pin 2
 - Debugger pin 3 to P6 pin 3
 - Debugger pin 4 to P6 pin 4
 - Debugger pin 5 to P6 pin 5
 - Debugger pin 6 to P6 pin 6
 - Debugger pin 7 to P6 pin 7
 - Debugger pin 8 to P6 pin 8
 - Debugger pin 9 to P6 pin 9
 - Debugger pin 10 to P6 pin 10
- CR2**: A 10-pin connector that interfaces between the debugger and the microcontroller. The debugger pins are connected to the microcontroller pins as follows:
 - Debugger pin 1 to CR2 pin 1
 - Debugger pin 2 to CR2 pin 2
 - Debugger pin 3 to CR2 pin 3
 - Debugger pin 4 to CR2 pin 4
 - Debugger pin 5 to CR2 pin 5
 - Debugger pin 6 to CR2 pin 6
 - Debugger pin 7 to CR2 pin 7
 - Debugger pin 8 to CR2 pin 8
 - Debugger pin 9 to CR2 pin 9
 - Debugger pin 10 to CR2 pin 10
- TPDE004RSE**: Two diode arrays, each containing four diodes. The debugger pins are connected to the diode arrays as follows:
 - Debugger pin 1 to TPDE004RSE pin 1
 - Debugger pin 2 to TPDE004RSE pin 2
 - Debugger pin 3 to TPDE004RSE pin 3
 - Debugger pin 4 to TPDE004RSE pin 4
 - Debugger pin 5 to TPDE004RSE pin 5
 - Debugger pin 6 to TPDE004RSE pin 6
 - Debugger pin 7 to TPDE004RSE pin 7
 - Debugger pin 8 to TPDE004RSE pin 8
 - Debugger pin 9 to TPDE004RSE pin 9
 - Debugger pin 10 to TPDE004RSE pin 10

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Drawn: RGW		PN: MCU087	
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Mechanical

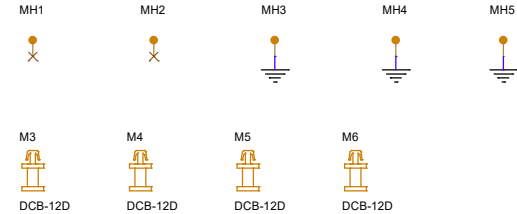
Shield



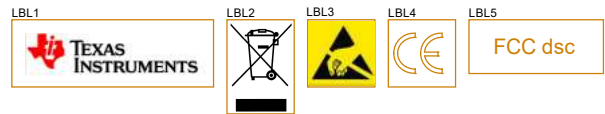
Jumpers (P1)



Mounting Holes




Labels



Fiducials



Title: LP-EM-CC1354P10-1		 TEXAS INSTRUMENTS
Drawn: RGW	PN: MCU087	
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